

DIFFERENTIAL FAST SYNCHRONOUS TIMING

chip vendor	TX		RX		Delta	
	setup	hold	setup	hold	setup	hold
A	35	45	0	10	35	35
B	35	45	5	15	30	30
C	27	42	15	20	<u>12</u>	22
SCSI-2*	25	35	0	10	25	25

all values must be \geq skew budget below

foiled delay transmitter (differential) cable receiver (differential) foiled delay drop cable receiver distortion effects

chip to chip skew (SCSI-2 was $\frac{31}{4}$ ns)

Proposed skew budget:

TX connector	----->	6 ns
RX connector	----->	4 ns
	----->	6 ns
	----->	1 ns
	----->	3 ns
		<hr/>
		21 ns

- ① Agree on skew budget
- ② On non-compliant combinations, decide how to split difference between TX + RX.

⇒ NOTE: At least 2 fast silicon vendors not represented above.

* SCSI-2 says these are at connector, although chip spec was probably intended.

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