

GAP: MICRO-
NATIONAL SEM.
2/91

4.4.2.1. Output Characteristics

Each signal driven by an SCSI device shall have the following output characteristics when measured at the SCSI device's connector:

V_{OL} (Low-level output voltage) = 1.7 V maximum at I_{OL} (Low-level output current) = 55 milliamps.

V_{OH} (High-level output voltage) = 2.7 V minimum at I_{OH} (High-level output current) = -55 milliamps.

V_{OD} (Differential output voltage) = 1.0 V minimum with common-mode voltage ranges from -7 to +12 volts dc.

V_{OL} and V_{OH} shall be measured between the output terminal and the SCSI device's logic ground reference.

The output characteristics shall additionally conform to ISO 8482 with the following exceptions (references made to ISO 8482 First edition 1987-11-15):

Figure 9 - Replace 270 ohm resistors with 375 ohm resistors (receiver load model).

Figure 10 - Remove the 27 ohm resistor connected between the load center and terminal C.

Paragraph 10.2 - Replace "-10 V to +15 V" with "-7 V to +12 V" (absolute maximum output voltage range).

4.4.2.2. Input Characteristics

SCSI devices shall meet the following electrical characteristics on each signal (including both receivers and passive drivers):

I_I (Input current on either input) = +/- 2.0 milliamps maximum.

Maximum input capacitance = 25 picofarads

The I_I requirement shall be met with the input voltage varying between -7 and +12 volts dc, with power on or off, and with the hysteresis equalling 35 millivolts, minimum.

The input characteristics shall additionally conform to ISO 8482 with the following exceptions (references made to ISO 8482 First edition 1987-11-15):

Paragraph 9.1 - Replace +/- 0.3 V with +/- 0.2 V (minimum differential input voltage)

Paragraph 9.1 - Replace "-10 V to +15 V" with "-7 V to +12 V" (absolute maximum input voltage range).

Paragraph 9.2 - Replace +/- 0.6 V with +/- 0.4V (input balance test voltage).

Figure 11 - Replace +11.7 V and -6.7 V with +11.8 V and -6.8 V, respectively (input voltage graph).

Figure 12 - Replace +11.4 V and -6.4 V with +11.6 V and -6.6 V, respectively (input balance graph).