X3/90-0630-X,S

X3T9.2/90-63

Micropolis Corporation
21211 Nordoff Street
Chatsworth Ca. 91311

March 12, 1990

Ms. Lynn Barra (X3 Secretariat)
CBEMA
311 First Street, N.W.
Suite 500
Washington D.C. 20001-2178

Dear Ms. Barra:

This letter is a reply to Del Shoemaker's letter responding to BSR X3.131-198x (SCSI-2) public review comment #4 submitted by Micropolis. Although we would prefer to have the 16 bit "P" cable option included in the specification as an appendix, Micropolis will accept the decision to mention that X3T9.2 is in process of documenting a 16 bit single cable option.

Sincerely

MICROPOLIS CORPORATION

Anthony T. M. Robertson
Tony Robertson
Vice President - Engineering

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Manager SCSI Firmware

Micropolis Corporation
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Chatsworth Ca. 91311

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Dear Ms. Barra:

This letter is a reply to Del Shoemaker's letter responding to BSR X3.131-198x (SCSI-2) public review comment #5 submitted by Micropolis. It was our intention to address a serious problem in page 8. As large buffers become more common place ,the ability to partition the buffer into Multiple segments to handle multiple read ahead operations rather than a single thread becomes more attractive. However without a clearly defined method for the user to select the number of segments there will be in-compatibilities between each vendor that offers multiple segments and there own "unique" implementation.

Because of oversights in the definition of page 8 cache page Micropolis has implemented page 8 to support caching and the Quantum unique page 38 to support multiple segments. However we have experienced problems with conflicts between the two pages and we have now decided to drop support of page 38. We now suggest the following method to derive the number of segments from information on page 8 and suggest this method be considered as part of the SCSI-3 working documentation.

Data Buffer Size / Maximum prefetch = Number of data segments

We accept the decision to address this concern as part of the SCSI-3 working draft.

Sincerely

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