Accredited Standards Committee[*]
X3, Information Processing Systems

Doc. No.	X3T10/94-153R0
Date:	July 21, 1994
Project:	
Ref. Doc.:	
Reply to:	S. Finch

To: Membership of X3T10

From: Lamers/Finch

Subject: Minutes of X3T10 ATA Working Group July 21, 1994

Agenda

<u>**Results of Meeting**</u>

1.   **Opening Remarks**

Steve Finch convened the meeting at 9:00 am. Steve thanked Ralph Weber of DEC for hosting the meeting.

As is customary, the people attending introduced themselves. A copy of the attendance list was circulated for attendance and corrections.

It was stated that the meeting had been authorized by X3T10 and would be conducted under the X3 rules. Ad hoc meetings take no final actions, but prepare recommendations for approval by the X3T10 task group. The voting rules for the meeting are those of the parent committee, X3T10. These rules are: one vote per company; and any participating company member may vote.

The minutes of this meeting will be posted to the SCSI BBS and the ATA Reflector and will be included in the next committee mailing.

2.   **Attendance and Membership, Introductions**

Attendance at working group meetings does not count toward minimum attendance requirements for X3T10 membership. Working group meetings are open to any person or company to attend and to express their opinion on the subjects being discussed.

The following people attended the meeting.

Richard Kalish	Adaptec	rkalish@rahul.net
Lawrence J. Lamers	Adaptec	ljlamers@adaptec.com
Ken Scherzinger	Amphenol Spectra Strip	
Dennis Pak	Apple Computer	
John Lohmeyer	AT&T/NCR Microelectronics	john.lohmeyer@ftcollinsco.ncr.com
Joseph Chen	Cirrus Logic	chen@cirrus.com
Ed Gardner	DEC	
Wayne Buckholdt	DPT	
Dal Allan	ENDL	250-752@mcimail.com
Robert Liu	Fujitsu	
Rene Macare	Future Domain	renem@fdc.mhs.compuserve.com
Mark Raymond	Future Domain	
Nancy Cheng	Hitachi Computer Products	n.chen@hitachi.com
Motoyasu Tsunoda	Hitachi Micro Systems	mtsunoda@hmsi.hitachi.com
David McFadden	Honda	
Dan Colegrove	IBM	colegrove@vnet.ibm.com
John Sceible	IBM	
Geoffrey Barton	Iomega	
Dean Wallace	Linfinity	
Ray Heineman	Maxtor	ray_heineman@maxtor.com
Peter Brown	Oak	
Mike Carpenter	Qlogic	m_carpenter@qlc.com
Jim McGrath	Quantum Corp	JMCGRATH@QNTM.COM
John Masiewicz	Seagate Technology	masiewicz@notes.seagate.com
Gene Milligan	Seagate Technology	milligan@notes.seagate.com
Gene Milligan	Seagate Technology	Gene_Milligan@notes.seagate.com
Stephen Finch	Silicon Systems	5723283@mcimail.com
Paul Aloisi	Unitrode Integrated Circuit	Aloisi@uicc.com
Tom Hanan	Western Digital	t_hanan@dt.wdc.com
Duncan Penman	Zadian Technologies	

3. Approval of Agenda

The agenda was approved as proposed.

4. Document Distribution

Copies of ATA-2 Proposed Working Draft revision 2e was distributed.

5. Old Business

5.1 Project Proposals Comment Resolution [94-019,94-022] (Finch)

Revised proposals were reviewed and modified. These proposals reflected the agreement reached at the prior working group meeting to establish two projects, one for ATA-3 disk and one for ATAPI. The working group unanimously agreed that the chair ask X3T10 to replace the previous four ATA-3/ATAPI related project proposals with these new, revised project proposals and go throught the appropriate approval processing.

5.2 ATA-2 Technical Issues discovered during editing sessions

Ray Heineman noted that the phrase 'the device shall enter the idle mode.' in 8.11 par 3 to 'the device shall enter the standby mode'. This error will be corrected.

5.3 ATA-2 Technical Review

Jim McGrath moved and John Masciewicz seconded that ATA-2 revision 2e be recommeded for acceptance as the working draft for project 948D. Passed 13:0.

6. New Business

6.1 Termination Issues

Tom Hanan stated that implementation of mode 4 in many cases benefits from use of series termination on the device.

Jim McGrath moved and Tom Hanan seconded that the ATA Working Group consider a proposal for an informative annex for the ATA-2 standard be considered at the next Working Group meeting on August 30th, 1994 providing that the proposed informative annex be posted to the ATA reflector prior to August 17, 1994 and be made available in hard copy at the meeting on August 30, 1994. Passed 9:4.

6.2 Check Power Mode (Heineman)

Ray Heineman presented a proposal for check power mode enhancements.

7. Meeting Schedule

August 31 - September 1, 1994 in Milpitas, CA
September 15, 1994 in Houston, TX
October 19, 1994 in Longmont, CO
November 10, 1994 in Palm Springs, CA

Please note that the two day meeting will be in two locations. August 31, 1994 will be at the IBM San Jose facility and the September 1, 1994 meeting will be hosted by Adaptec.

8. Adjournment

The meeting adjourned at approximately 12:00 PM.