

Attendance:

Mr. Bryan Kantack	Agilent Technologies, Inc.
Mr. Jesse Jaramillo	Amphenol
Mr. Greg McSorley	Amphenol
Mr. Mickey Felton	EMC
Mr. Ramez Rizk	Emulex
Mr. Barry Olawsky	Hewlett Packard Co.
Mr. Harvey Newman	Infineon Technologies
Mr. Michael Jenkins	LSI Corp.
Mr. Gabriel Romero	LSI Corp.
Mr. Jacky Chow	Marvell Semiconductor, Inc.
Mr. Galen Fromm	Molex Inc.
Mr. Rick Hernandez	PMC-Sierra
Mr. Guillaume Fortin	PMC-Sierra
Mr. Yuming Tao	PMC-Sierra
Mr. Joseph Chen	Samsung
Mr. Alvin Cox	Seagate Technology
Mr. Allen Kramer	Seagate Technology
Mr. Bruce Johnson	Seagate Technology
Mr. Himanshu Desia	Seagate Technology
Mr. Benoit Mercier	STMicroelectronics
Mr. Bent Hessen-Schmidt	Synthesys Research, Inc.
Mr. Mike Fogg	TycoElectronics
Mr. Mahbubul Bari	Vitesse Semiconductor
Mr. Larry McMillan	WDC
Mr. Ramya Dissanayake	WDC

25 in attendance

Agenda:

1. SAS-2 Calibration of Jitter Measurement Devices [Cox]

<http://www.t10.org/ftp/t10/document.07/07-443r1.pdf>

Expecting something next week.

2. Transmitter common mode measurements to validate the currently proposed chart. [Seagate to present; Intel, LSI looking at providing data]

Updated data posted at: <http://www.t10.org/ftp/t10/document.07/07-445r1.pdf>

Measurements made through SAS mated pair will be included next week along with scope measurements for comparison. Discussion indicated that including the SAS connector could influence the results to be either better or worse. Data is needed to decide on the limit value, whether to include the SAS connector, shape of the graph, test methodology, and the required test data pattern.

3. Description of SSC profile allowed discontinuities. [Hernandez]

No update.

4. Should the cable specification be done by common mode requirements or fall under channel simulation? [Amphenol]

Amphenol believes that both parameters will be asked for by some. They are still wrestling with what to propose. Hope to have something out for review next week.

5. Define the delivered signal characteristics for physical receiver testing. Include 0.1UI sinusoidal jitter to do the equivalent of receiver tolerance testing. [Bari, Jenkins, Newman, Witt]
Additional data has been collected but it was not yet in a presentable format.

6. Refine/provide status on simulation technology. [Jenkins, Newman]

An optimized version of code now available on the StatEye web site. The revised version runs much quicker than previous ones.

<http://www.t10.org/ftp/t10/document.07/07-463r0.pdf>

Concern brought up that backplane simulations have not been run/confirmed, however, the original StatEye was primarily developed for backplane analysis.

Expectations for November T10 meeting:

- Present final silicon cross correlation.
- Propose receiver and transmitter electrical characteristics for standard based on 6Gbps silicon measurement.
- Present final proposal for transmitter, channel and receiver compliance methodology.
- Report on engagement of Edotronik for development of GUI and API to measurement equipment.

New item:

SAS-2 Mini SAS 8i connectors and cable assemblies [Elliott]

<http://www.t10.org/ftp/t10/document.07/07-449r0.pdf>

Barry indicated that the sideband location needs to be reviewed by those who manufacture cables or intend to use this new internal version. Please review. This item is planned to be included in SAS2.

Next conference call: November 1, 2007

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International Access/Caller Paid Dial In Number: (636)651-3190

PARTICIPANT CODE: 3243413

Webex information:

<https://seagate.webex.com/seagate>

Topic: SAS-2 PHY WG

Date: Thursday

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)

Meeting number: 826 515 680

Meeting password: 6gbpsSAS