

## Attendance:

Mr. Bryan Kantack	Agilent Technologies, Inc.
Mr. Paul von Stamwitz	AMCC
Mr. Jesse Jaramillo	Amphenol
Mr. Jaremy Flake	ATL Technology
Mr. Ramez Rizk	Emulex
Mr. Douglas Wagner	FCI
Mr. Mike Fitzpatrick	Fujitsu
Mr. Barry Olawsky	Hewlett Packard Co.
Mr. Dan Colegrove	Hitachi Global Storage Tech.
Mr. Harvey Newman	Infineon Technologies
Dr. Mark Seidel	Intel Corp.
Mr. Michael Jenkins	LSI Corp.
Mr. Gabriel Romero	LSI Corp.
Mr. Kevin Witt	Maxim Semiconductor
Mr. Hock Seow	NEC Electronics America, Inc
Mr. Guillaume Fortin	PMC-Sierra
Mr. Alvin Cox	Seagate Technology
Mr. Bruce Johnson	Seagate Technology
Ms. Himanshu Desai	Seagate Technology
Mr. Benoit Mercier	STMicroelectronics
Mr. Bent Hessen-Schmidt	Synthesys Research, Inc.
Mr. Doug Loree	Toshiba
Mr. Scott Shuey	TycoElectronics
Mr. Mahbubul Bari	Vitesse Semiconductor
Mr. Larry McMillan	WDC
Mr. Ramya Dissanayake	WDC

26 in attendance

## Agenda:

1. SAS-2 Calibration of Jitter Measurement Devices [Cox]  
<http://www.t10.org/ftp/t10/document.07/07-443r1.pdf>

Bent has some editorial updates for the background section not to be included in the specification. A paragraph was added to the original posting by Chuck to define the variable  $\Delta T$  so that independence from the data speed is explained. Alvin will update and repost. Bruce was concerned about the low corner frequency and FM he has experienced in systems. Bent indicated that this should not be a serious issue. John Hill has verified the adjustment procedure on equipment he questioned in being able to meet the 2.6MHz corner frequency and the 72-75 dB attenuation.

Alvin has updated per Bent's editorial input and posted as r1 (link above). Please review prior to the next call so this item can be completed then.

2. Transmitter common mode measurements to validate the currently proposed chart. [Seagate to present; Intel, LSI looking at providing data]  
<http://www.t10.org/ftp/t10/document.07/07-445r0.pdf>

Seagate presented test data that indicates a flat line rather than the current sloping line would be more realistic for the specification. It also showed a PRBS7 versus CJTPAT data pattern. Seagate will take additional measurements with a scope to be compared with those of the spectrum analyzer. Discussion included the use of a divider rather than a splitter and termination concerns. Measurements are not made through a mated SAS connector pair.

3. Description of SSC profile allowed discontinuities. [Hernandez]  
Rick is currently reviewing SATA work in this area.

4. Should the cable specification be done by common mode requirements or fall under channel simulation? [Amphenol]  
Need input as soon as available. This item is being actively worked.

5. Define the delivered signal characteristics for physical receiver testing. Include 0.1UI sinusoidal jitter to do the equivalent of receiver tolerance testing. [Bari, Jenkins, Newman, Witt]  
Data collected and needs to be sorted before posting. Mahbubal has the data but needs to make in a presentable form. Harvey is currently working the issue also and has equipment in place.

6. Refine/provide status on simulation technology. [Jenkins, Newman]  
Kevin found the issue causing the strange eye shape and will provide an update to the posted data along with files.

Next conference call 10/18/07:

Toll Free Dial in Number: (877)810-9442  
International Access/Caller Paid Dial In Number: (636)651-3190  
PARTICIPANT CODE: 3243413

Webex information:

<https://seagate.webex.com/seagate>

Topic: SAS-2 PHY WG

Date: Thursday

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)

Meeting number: 826 515 680

Meeting password: 6gbpsSAS