Minutes of SAS PHY Working Group conference call October 4, 2007 T10/07-440r0

Attendance:

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| Mr. Bryan Kantack | Agilent Technologies, Inc. |
| Mr. Charles Hill | Alta Engineering |
| Mr. Jesse Jaramillo | Amphenol |
| Mr. Greg McSorley | Amphenol |
| Mr. Jaremy Flake | ATL Technology |
| Mr. Mickey Felton | EMC |
| Mr. Ramez Rizk | Emulex |
| Mr. Douglas Wagner | FCI |
| Mr. Mike Fitzpatrick | Fujitsu |
| Mr. Barry Olawsky | Hewlett Packard Co. |
| Mr. Rob Elliott | Hewlett Packard Co. |
| Mr. Dan Colegrove | Hitachi Global Storage Tech. |
| Mr. Harvey Newman | Infineon Technologies |
| Mr. Anthony Sanders | Infineon Technologies |
| Dr. Mark Seidel | Intel Corp. |
| Mr. Michael Jenkins | LSI Corp. |
| Mr. Jacky Chow | Marvell Semiconductor, Inc. |
| Mr. Kevin Witt | Maxim Semiconductor |
| Mr. Galen Fromm | Molex Inc. |
| Mr. Hock Seow | NEC Electronics America, Inc |
| Mr. Rick Hernandez | PMC-Sierra |
| Mr. Guillaume Fortin | PMC-Sierra |
| Mr. Yuming Tao | PMC-Sierra |
| Mr. Joseph Chen | Samsung |
| Mr. Alvin Cox | Seagate Technology |
| Mr. Allen Kramer | Seagate Technology |
| Mr. Daniel Smith | Seagate Technology |
| Mr. Bruce Johnson | Seagate Technology |
| Mr. Benoit Mercier | STMicroelectonics |
| Mr. Doug Loree | Toshiba |
| Mr. Scott Shuey | TycoElectronics |
| Mr. Mahbubul Bari | Vitesse Semiconductor |
| Mr. Larry McMillan | WDC |
| Mr. Ramya Dissanayake | WDC |

34 in attendance

Agenda:

1. Refine/provide status on simulation technology. [Jenkins, Newman]
StatEye:
Anthony explained why the center of eye is offset and why right side of contour closes faster than the left side (DFE). New posting of information presented:
http://www.t10.org/ftp/t10/document.07/07-439r0.pdf

1010 pattern is good for measuring RJ. SSC on is bring up some jitter measurement concerns.

StatEye scope interaction should be functional around Nov 1. Anthony plans to attend the November T10 working group.

Stressed signal simulation:
Kevin Witt has run the LSI software and shared preliminary results.

2. Zero length test load proposal. [Olawsky}
http://www.t10.org/ftp/t10/document.07/07-304r3.pdf
Latest changes seemed to be acceptable. Please review and supply any comments to Barry or the reflector.

3. Transmitter common mode measurements to validate the currently proposed chart. [Seagate, Intel, LSI looking at providing data]
Seagate has made some measurements and plans to present and post for next teleconference.

4. Should the cable specification be done by common mode requirements or fall under channel simulation? [Amphenol, all]
Working the issue.

5. Description of SSC profile allowed discontinuities. [Hernandez]
Suggested that the SATA work be reviewed as part of this effort.

6. Define the delivered signal characteristics for physical receiver testing. Include 0.1UI sinusoidal jitter to do the equivalent of receiver tolerance testing. [Bari, Jenkins, Newman, Witt]
Bari has equipment in place and making progress. Witt is helping. Expect more items for discussion next week.

Newman indicated that the StatEye way of looking at the transmitter can also be used to produce the receiver test signal. Did not want to discuss during the transmitter portion of the item 1 discussion since this easily confuse matters with the similarities.

7. Proposal for JTF calibration of JMD's [Cox]
Status: Discussed items with John Hill and will complete proposal before next call.

Next conference call: 10/11/2007

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Webex information:
https://seagate.webex.com/seagate
Topic: SAS-2 PHY WG
Date: Thursday
Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)
Meeting number: 826 515 680
Meeting password: 6gbpsSAS