

Attendance:

Mr. Bryan Kantack	Agilent Technologies, Inc.
Mr. Charles Hill	Alta Engineering
Mr. Jesse Jaramillo	Amphenol
Mr. Ramez Rizk	Emulex
Mr. Mike Fitzpatrick	Fujitsu
Mr. Barry Olawsky	Hewlett Packard Co.
Mr. Rob Elliott	Hewlett Packard Co.
Mr. Harvey Newman	Infineon Technologies
Dr. Mark Seidel	Intel Corp.
Mr. Pankaj Kumar	Intel Corp.
Mr. Michael Jenkins	LSI Corp.
Mr. Gabriel Romero	LSI Corp.
Mr. Jacky Chow	Marvell Semiconductor, Inc.
Mr. Kevin Witt	Maxim Semiconductor
Mr. Galen Fromm	Molex Inc.
Mr. Hock Seow	NEC Electronics America, Inc
Mr. Rick Hernandez	PMC-Sierra
Mr. Guillaume Fortin	PMC-Sierra
Mr. Yuming Tao	PMC-Sierra
Mr. Tim Symons	PMC-Sierra
Mr. Alvin Cox	Seagate Technology
Mr. Benoit Mercier	STMicroelectronics
Mr. Doug Loree	Toshiba
Mr. Scott Shuey	TycoElectronics
Mr. Mahbubul Bari	Vitesse Semiconductor
Mr. Larry McMillan	WDC
Mr. Ramya Dissanayake	WDC
Mr. William Harmon	

28 in attendance

1. Zero length test load proposal. [Olawsky]
<http://www.t10.org/ftp/t10/document.07/07-304r2.pdf>

Need to update the equation due to the calculated value at 10MHz (0.03 dB) and that the measurement equipment generally starts at 50MHz. 50MHz is still 0.075 dB per the equation; 100MHz is 0.11 dB.

Barry will update and have ready for next week.

2. Should the cable specification be done by common mode requirements or fall under channel simulation? [All]
Don't leave the number for skew.
Need a frequency domain requirement.
Amphenol will work this issue.
Channel modeling may be acceptable for 6G. Need to resolve how to incorporate 6G application into the specification.

3. Description of SSC profile allowed discontinuities. [Hernandez]
Status? Working. No ETA.

4. Transmitter common mode measurements to validate the currently proposed chart. [Anyone with silicon tests]

Intel and Seagate hope to have data next week. LSI is also working to provide some data.

5. Define the delivered signal characteristics for physical receiver testing. Include 0.1UI sinusoidal jitter to do the equivalent of receiver tolerance testing. [Bari, Jenkins, Newman, Witt]

Mahbubul should have the equipment arriving Monday to have items in place to start working on the signal description.

What is the spectral bound on the jitter? Use what is available and let us know what it was. Not intended to be a 3GHz requirement. Reference MJSQ.

More questions next week as testing starts.

6. Refine/provide status on simulation technology. [Jenkins, Newman]

Mike demonstrated the DFEEYE software and will post today. Requires MATLAB. Consider as a 30-day evaluation until all legal issues worked out. See comments at the first of the file concerning simulation data requirements.

<http://www.t10.org/ftp/t10/document.07/07-436r0.pdf>

<http://www.t10.org/ftp/t10/document.07/07-436r0.zip>

StatEye calculation speed is slow due to a rush release. Harvey to ask concerning asymmetry of the StatEye results presented. Next release may have the ability to accept a signal file as input.

This is not intended to be a competition between the two simulations, but rather an ability to have comparable results between two different simulation tools. They do provide different outputs and have different capabilities.

7. Proposal for JTF calibration of JMD's [Cox]

Alvin needs to talk with John Hill concerning assumptions on SSCtol. Plan to have posted by the next call.

Next conference call: 10/4/07

Toll Free Dial in Number: (877)810-9442

International Access/Caller Paid Dial In Number: (636)651-3190

PARTICIPANT CODE: 3243413

Webex information:

<https://seagate.webex.com/seagate>

Topic: SAS-2 PHY WG

Date: Thursday

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)

Meeting number: 826 515 680

Meeting password: 6gbpsSAS