

Attendance:

Mr. Bernhard Laschinsky	Agere Systems
Mr. Bill Boyne	Amphenol
Mr. Paul von Stamwitz	AMCC
Mr. David Freeman	Finisar
Mr. Barry Olawsky	Hewlett Packard Co.
Mr. Dan Colegrove	HGST
Mr. Harvey Newman	Infineon Technology
Mr. Schelto van Doorn	Intel Corp.
Mr. Brian Day	LSI Logic Corp.
Mr. Gabriel Romero	LSI Logic Corp.
Mr. Keith Maloney	LSI Logic Corp.
Mr. David Geddes	Marvell Semiconductor, Inc.
Mr. Hock Seow	NEC Electronics America, Inc
Mr. Alvin Cox	Seagate Technology
Ms. Judy Westby	Seagate Technology
Mr. Stephen Finch	STMicroelectronics
Mr. Kevin Witt	Vitesse Semiconductor

17 in attendance

Agenda:

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SAS-2 Modifications to the SAS Speed Negotiation [Wassal]
<http://www.t10.org/ftp/t10/document.06/06-324r2.pdf>

Amr has the two following questions related to the updated proposal:

Q1. Initialization of scrambler used to be when a single SOF or SOAF primitive is detected. Now, with the TRAIN primitive being redundant it seems to me that there is a chance the scramblers on the two ends may be out of synch depending on which primitive in the redundant sequence is detected first.

Primitives are not scrambled, so this should not be an issue.

Q2. SL_IR is now asserted after train is complete and DWS is acquired (i.e., when moving from TRAIN to TRAIN_DONE). However, we may need a mechanism to "deassert it" if the other end does not complete the training and the training window has to be repeated to downgrade to the next common settings.

With going into a RCD for the next speed negotiation, this should be taken care of. The various conditions have this set in a de-asserted state, so it is assumed that it is taken care of.

General: The idea is to get everyone to read it and give feedback via email. That thought follow through with the three specific proposals listed below for the September 12 agenda.

Next meeting September 12, 2006, 9:00 am
Crowne Plaza Nashua
2 Somerset Parkway
Nashua, NH 03063

Agenda:

1. SAS-2 Spread-spectrum clocking [Elliott]
<http://www.t10.org/ftp/t10/document.06/06-263r5.pdf>
2. SAS-2 SNW-3 bit definitions
<http://www.t10.org/ftp/t10/document.06/06-363r2.pdf>
3. SAS-2 Modifications to the SAS Speed Negotiation [Wassal]
<http://www.t10.org/ftp/t10/document.06/06-324r2.pdf>
4. New items
5. PHY specification: Let the games begin!
 - 10-meter cable requirements
 - Return loss
 - Fixed transmitter de-emphasis
 - Rise time
 - Stateye?
 - Reference transmitter and receiver
 - Etc.