

Attendance:

Mr. Tim Symons	Adaptec, Inc.
Mr. Henry Wong	Agilent
Mr. Rob Elliott	Hewlett Packard Co.
Mr. Barry Olawsky	Hewlett Packard Co.
Mr. Bill Bissonette	Intel Corporation
Mr. Dick Uber	Maxtor
Mr. Michael Rost	Molex
Mr. Bill Lye	PMC-Sierra
Mr. Alvin Cox	Seagate Technology
Mr. Allen Kramer	Seagate Technology
Mr. Bill Gintz	Sues
Mr. Kalev Sepp	Tektronics
Mr. Don Schulte	Vitesse

13 People Present

Agenda:

04-337r2

<http://www.t10.org/ftp/t10/document.04/04-337r2.pdf>

Reviewed the new update. Alvin suggested that the text regarding probe points should be added to the compliance point definition section. That would locate it before the figures showing compliance point locations that also mention probe points. It would be okay to have in both places

04-375r0 SAS 1.1, Phy hot plug and transients on SAS and SAS/SATA environments

<http://www.t10.org/ftp/t10/document.04/04-375r0.pdf>

Review SATA transient definition. Concern about high voltage application on high impedance SATA devices. Dick Uber to find out what SATA devices are high impedance. If hosts, then the issue of voltage sensitivity is not as critical because they would not be plugged into a backplane slot for a drive. Dick will also draft a definition of "mode transitions".

05-007r0 SAS 1.1 external cable electrical specification

<http://www.t10.org/ftp/t10/document.05/05-007r0.pdf>

Alvin showed the initial draft and will post later in the day. Numbers are based on the SATA 4-lane external electrical specification since these are the same cable configurations and signal properties. Please review.

04-370r1 SAS-1.1 Merge IT and IR with XT and XR

<http://www.t10.org/ftp/t10/document.04/04-370r1.pdf>

The figures have been greatly improved. Barry will work on the definition of the IT point with reference to FCAL and the TCTF.

Next call: December 9, 2004

Thursday, 10 am CST.

Same webex and call number for all calls:

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