Kevin Marks	Dell
Barry Olawsky	HP
Andrew Cable	Intel
George Penokie	IBM
Mike Jenkins	LSI Logic
Richard Uber	Maxtor
Bill Lye	PMC-Sierra
Alvin Cox	Seagate
David Springberg	Vitesse
Bill Gintz	Zues

10 people present.

1.   04-195r2   SAS-1.1 Internal Wide Connector/Cable Electrical Requirements   Barry Olawsky

Barry presented information is SFF-8410 and indicated that the note is correct. Will drop "equivalent" from table entry and leave in note. Update will be posted to T10 web site and reviewed next call.

2. Impact to SAS 1.1 PHY to include SATA at 3 Gbps.   Andrew Cable

Andrew reviewed issues on Table 27 and considers this a good place to start. An effort will be made to make reference to the SATA specification rather than have numbers in the SAS specification wherever possible. A note may be added to clarify the numbers (if any left) are requirements for the SAS expander and do not affect the SATA device.

3.   New items

Requested that the SAS PHY section be reviewed for any items to be clarified, changed, or added. This could include partial incorporation of MJSQ and missing items. Since SAS was defined before actual hardware was available for testing, some items may be missing or are in need of update. Must also consider when comparing to SATA that the SAS requirements are based on eye diagrams as other serial specifications rather than the methods used by SATA.

Conference call schedule:

Bi-weekly, 10:00am CDT on Thursdays.
Next call:

Date: Thursday, August 26, 2004
Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)
All Participants should use the following information to reach the conference call:
Toll Free Dial in Number: (866) 279-4742
International Access/Caller Paid Dial In Number: (309) 229-0118
PARTICIPANT CODE: 3243413

WEBEX information:

url: seagate.webex.com (no www)
Topic: SAS PHY WG
Date: Thursday, August 26, 2004

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)
Meeting number: 825549498
Meeting password: section5