To: T10 Technical Committee
From: Kevin Marks (Kevin_Marks@dell.com), Dell, Inc.
Date: August 22, 2003
Subject: SES-2 Enclosure element status page change for device/slot correspondence

Revision History

Revision 0 (8/21/03)

Related Documents

SCSI Enclosure Services – 2 (SES-2r4)

Overview

This proposal recommends allocating a reserved bit in the Enclosure element status page to indicate whether the enclosure complies with the Device/Slot Correspondence definition that is described in Serial ATA II: Extensions to Serial ATA 1.0a revision 1.1.

Suggested Changes

Change **Table 68 — Enclosure element for status type diagnostic pages** to add the DEV/SLOT CORR bit to byte-1, bit-6 as follows:

Byte\Bit	7	6	5	4	3	2	1	0
0	COMMON STATUS							
1	IDENT	DEV/ SLOT CORR	Reserved					
2	Reserved						FAILURE INDICATION	WARNING INDICATION
3							FAILURE REQUESTED	WARNING REQUESTED

Add the following text to section 7.3.16 after the description of COMMON STATUS following Table 68.

The DEV/SLOT CORR bit specifies whether the enclosure complies with the device/slot correspondence definition in Serial ATA II: Extensions to Serial ATA 1.0a revision 1.1. If the enclosure complies then the bit shall be set to one. If the enclosure does not comply then the bit shall be set to zero.