ISO/IEC Report 11/2000

Project	#	Description	Status	Months Pending	Editor
25.13.11.02	DIS9316-2	CAM	In Publication	Completed	Bill Dallas
25.13.11.13	IS 9316-1	SCSI-2	Published IS	Completed	Larry Lamers
25.13.11.08	IS 14776-411	SAM	Published IS	Completed	Charles Monia
25.13.11.15	IS 14776-341	SCC	Published IS	Completed	George Penokie
25.13.11.13	CD 14776-351	SMC	Awaiting DIS text res of comments complete	22 - Target 7/99	Erich Oetting
25.13.11.25	CD 14776-112	SPI-2	DIS text provided with resolution of comments	Current step Completed	George Penokie
25.13.11.22	CD 14776-371	SES	Awaiting DIS text with resolution of comments	22 – Target 8/99	Bob Snively
25.13.11.23	DIS 14776-342	SCC-2	Publishing	Current step completed	George Penokie
25.13.13.02	CD 14776-221	FCP	Awaiting DIS text with resolution of comments	22 – Target 8/99	Bob Snively
25.13.11.16	IS 14776-381	OMC	Published IS	Completed	Japan
25.13.11.17	CD 14776-361	MMC	Withdrawn		Ron Roberts
25.13.11.10	CD 14776-321	SBC	FDIS text with resolution of comments provided	Current step completed	Gene Milligan
25.13.11.20	DIS 14776-232	SBP-2	Fast Track DIS closes 9/15/00		Peter Johansson
25.13.11.26	FCD 14776-113	SPI-3	NWIP passed & FCD pending		George Penokie
25.13.11.xx	CD 14776-326	RBC	NWIP closes 1/20/01 & FCD closes 4/15/01		Ron Roberts
25.13.11.17	CD 14776-362	MMC-2	NWIP & CD Contribution submitted	MMC project changed to MMC-2	Ron Roberts
25.13.11.11	CD 14776-331	SSC	NWIP & CD Contribution submitted		Dave Peterson

The NWIP for SCSI SPI-3 met the acceptance criteria -- details:
Q.1/2 : 12 Yes votes
Q.3 : 5 Yes Votes (CA,CN!,DE,JP,US)
Q.4 : 2 Yes votes (Finland & US) (Q.4 is asking for project editor)
Q.5/6 : 1 Yes vote (US)

ISO/IEC JTC 1/SC 25 N 0669
Date of Circulation of NP: 2000-10-25
Date of Ballot Close: 2001-01-31
Please return all votes and comments directly to the JTC 1/SC 25 Secretariat by the due date indicated.
Proposal for a new work item on
ISO/IEC 14776-362: Information technology - Small Computer System Interface (SCSI) - Part 362: Multimedia Commands (MMC-2). This ballot was or will be cancelled & MMC project changed to MMC-2 per 6/2000 SC 25 meeting.

US TAG: NCITS/T10
SC: 25 N 624
Date Due to TAG Administrator: 04/02/01
Subject: For FCD ballot, ISO/IEC FCD 14776-326: IT - Small Computer System Interface - Part 326: SCSI Reduced Block Commands
SC/JTC 1 Due Date: 04/15/01

US TAG: NCITS/T10
SC: 25 N 663
Date Due to TAG Administrator: 01/05/01
Subject: For NP Ballot in SC25, Proposal for a new work item on:
ISO/IEC FCD 14776-326: IT - Small Computer System Interface -
Part 326: SCSI Reduced Block Commands
SC/JTC 1 Due Date: 01/20/01

Dr. Pritchard, IEEE TAG Administrator, advised us that IEEE would like to form a project TAG to be responsible for IEEE projects presently assigned to the US TAG for JTC1/SC25/WG4.

BACKGROUND: - Projects in question are IEEE Microprocessor standards. Previously, such projects were assigned to JTC1/SC26, but SC26 was dissolved in Nov. 1999 for lack of interest/activity. (including by the pertinent IEEE committees). At that time, SC25 agreed to assume

(maintenance) responsibility for the SC26 projects, all of which were published or approved ISO/IEC standards, and JTC1 transferred them to SC25. In June 2000, SC25 assigned all those projects to its WG4.

In the meantime, the IEEE Microprocessor Standards Committee has expressed interest in introducing an IEEE Std into the JTC1 arena, and is embarking on a Revision of an IEEE standard for which there is an identical ISO/IEC Standard in the group of transferred projects (which the should be revised as well to maintain synchronization).

The IEEE proposal was discussed between the IR and NCITS during the annual reports and the IR and NCITS approved in principal the proposal.

Current needed action on the former SC 26 projects was not completed by the US TAG and NCITS has requested that T10 step in and take emergency action on the following items:

US TAG: SC26 TAG
SC: N JT/00-0380
Date Due to TAG Administrator: 10/17/00
Subject: For JTC 1 TAG Letter Ballot, Systematic Review of International Standards published five or ten years ago, or more
(SC recommendation is to Confirm):
- **IEC 559:1989, Binary floating-point arithmetic for microprocessor systems**
- **IEC 796-1:1990, Microprocessor system bus -- 8-bit and 16-bit data (MULTIBUS I) -- Part 1: Functional description with electrical and timing specifications**
- **IEC 796-2:1990, Microprocessor system bus -- 8-bit and 16-bit data (MULTIBUS I) -- Part 2: Mechanical and pin descriptions for the system bus configuration, with edge connectors (direct)**
- **IEC 796-3:1990, Microprocessor system BUS I, 8-bit and 16-bit data (MULTIBUS I) -- Part 3: Mechanical and pin descriptions for the Eurocard configuration with pin and socket (indirect) connectors**
- **ISO/IEC 14536:1995, Information technology -- Microprocessor systems -- Futurebus+TM, Profile M (military)**

SC/JTC 1 Due Date: 11/30/00

ISO/IEC Business (Roll Call Votes)

Move to approve the NWIP SC: 25 N 663 for an
ISO/IEC FCD 14776-326: IT - Small Computer System Interface -
Part 326: SCSI Reduced Block Commands
With the following answers to the six questions:
Q.1 Do you accept the proposal in document 25 N 663 as a sufficient definition of the new work item? Yes.
Q.2 Do you support the addition of the new work item to the programme of work of the joint technical committee? Yes.
Q.3 Do you commit yourself to participate in the development of this new work item? Yes.
Q.4 Are you able to offer a project editor who will dedicate his/her efforts to the advancement and maintenance of this project? Yes (If "YES," please identify) Gene Milligan
Q.5 Do you have a major contribution or a reference document ready for submittal? Yes.
Q.6 Will you have such a contribution in ninety days? Yes.

Move to endorse the recommendations made by Dr. Pritchard at the SC 25/WG 4 meeting as reflected in the minutes of the WG 4 meeting SC 25 N 644 and to answer the questions for the five year reviews as follows:

a) IEC 559:1989, Binary floating-point arithmetic for microprocessor systems

Question 1: (Answer yes to one of three options.)

Are you in favour of
confirmation of the International Standard for a further period of five years? __No__
revision of the International Standard? If YES, please give reasons. Yes. Update needed.
withdrawal of the International Standard? If YES, please give reasons. ____No_

Question 2:

If the voting results show a need to revise the standard, are you prepared to participate in the development of the project? Yes

Question 3:

Has this ISO standard been implemented as a national standard or is it use per se by your national industry? Yes

Question 4:

If the response to question 3 is affirmative, is the standard implemented or used without change? In case of modifications, please identify the changes on a separate sheet. Yes

Question 5 (optional):
Please indicate the number of the corresponding national standard: IEEE 754

b) IEC 796-1:1990, Microprocessor system bus -- 8-bit and 16-bit data (MULTIBUS I) -- Part 1: Functional description with electrical and timing specifications

Question 1: (Answer yes to one of three options.)
Are you in favour of
confirmation of the International Standard for a further period of five years? ___No__
revision of the International Standard? If YES, please give reasons. ___No__
withdrawal of the International Standard? If YES, please give reasons. Yes. Withdrawing national standard.

Question 2:
If the voting results show a need to revise the standard, are you prepared to participate in the development of the project? No

Question 3:
Has this ISO standard been implemented as a national standard or is it use per se by your national industry? Yes

Question 4:
If the response to question 3 is affirmative, is the standard implemented or used without change? In case of modifications, please identify the changes on a separate sheet. Yes

Question 5 (optional):
Please indicate the number of the corresponding national standard: IEEE 796 but being withdrawn.

c) IEC 796-2:1990, Microprocessor system bus -- 8-bit and 16-bit data (MULTIBUS I) -- Part 2: Mechanical and pin descriptions for the system bus configuration, with edge connectors (direct)

Same answers to the questions as (b).

d) IEC 796-3:1990, Microprocessor system BUS I, 8-bit and 16-bit data (MULTIBUS I) -- Part 3: Mechanical and pin descriptions for the Eurocard configuration with pin and socket (indirect) connectors

Same answers to the questions as (b).

e) ISO/IEC 14536:1995, Information technology -- Microprocessor systems -- Futurebus+TM, Profile M (military)

Same answers to the questions as (b) except for Question 5.

Question 5 (optional):

Please indicate the number of the corresponding national standard: IEEE 896.5 but being withdrawn.