Organization	Name	s	Vote	Add'l Info
Adaptec, Inc.	lawrence lamers	P	Yes	
Advansys		P	Yes	
AMP, Inc.		P	Yes	
Amphenol Interconnect			Yes	
Ancot Corp.	Bart Raudebaugh			
Andataco			Yes	
Berg Electronics			Yes	
BREA Technologies, Inc. Circuit Assembly Corp.	-		Yes Yes	
CMD Technology			Yes	
Compag Computer Corp.		_	Yes	
Crossroads Systems, Inc.			Yes	
Dallas Semiconductor			Yes	
Dell Computer			DNV	
ENDL	Ralph Weber	Α	YesC	IV Cmnts
Fujitsu	Eugene Lew	P	Yes	
General Dynamics	Nathan Hastad	P	Yes	
Hewlett Packard Co.	Stewart Wyatt	P	Yes	
Hitachi Cable Manchester,Inc		P	Yes	
Hitachi Storage Products			Yes	
Honda Connectors	Thomas J. Kulesza			
IBM Corp.			Yes	
KnowledgeTek, Inc.		_	Yes	
Linfinity Micro			Yes	
LSI Logic Corp. Madison Cable Corp.	-		Yes	
Maxtor Corp.	jie fan Pete McLean		Yes Yes	
Molex Inc.	Joe Dambach		Yes	
Ophidian Designs	Edward A. Gardner			IV
Panasonic Technologies, Inc			Yes	_,
Philips Electronics			Yes	
QLogic Corp.			Yes	
Quantum Corp.	Mark Evans	P	Yes	
Seagate Technology	Gene Milligan	P	Yes	IV
Storage Technology Corp.		P	Yes	
Sun Microsystems Computer Co	_	P	Yes	
Texas Instruments			Yes	
Toshiba America Elec. Comp.	Tasuku Kasebayashi	Ρ		
Western Digital Corporation			DNV	
Key:				
P Voter indicated he/she is	principal member			
A Voter indicated he/she is				
O Voter indicated he/she is	observer member			
? Voter indicated he/she is	not member or does no	ot	know	status
YesC Yes with comments vote				
Abs Abstain vote				
DNV Organization did not vote				
IV Individual vote (not organ Cmnts Comments were included wi				
NoCmnts No comments were included			rea co	nments
DUP Duplicate ballot (last ba				
PSWD The password was not corr		_	0\	· ·//
ORG? Organization is not voting member of T10 (vote not counted)				
Ballot totals:				
37 Yes				
0 No				
0 Abstain				
2 Organization(s) did not vote				
39 Total voting organizations				
1 Ballot(s) included comments				

This 2/3rds majority ballot passed.

Comments attached to YesC ballot from Ralph Weber of

Reference information regarding the AT bit is inconsistent and misleading. Clause 4.2 states: "If the device supports both address types, the address types can be selected using the MODE SELECT command by setting the address type (AT) bit of optical memory card device mode parameter header (see SCSI-3 SPC)." Clause 5.1 states: "If the address type (AT) bit of mode parameter header (see SCSI-3 SBC) is set to zero, ..." Yet neither SPC nor SBC define the AT bit. The AT bit is defined in 6.3 of this standard.

It is true that the AT bit appears in the Device-Specific Parameter field of a

mode parameter header. Because the Device-Specific Parameter field of a mode parameter header is described in both SPC and SBC, references to those standards may be appropriate. However, the reference should be consistent in both 4.2 and 5.1 and the reference must include 6.3 of this standard. My recommendation would be to construct the reference as follows: "(see 6.3 and SCSI-3 SPC)".

The following sentence in 5.1 is not consistent with SCSI-3 terminology: "The READ CARD CAPACITY data shall be sent during DATA IN phase of the command." The wording "... DATA IN phase ..." is specific to the parallel SCSI bus and fails to recognize implementations such as Fibre Channel. The proper wording would be: "The READ CAPACITY data shall be returned to the application client in the Data-In Buffer."

******** End of Ballot Report ***************