Monday, July 15, 1996 X3T10.1/96a142r1

Accredited Standards Committee X3, Information Processing Systems

Doc: X3T10.1/96a142r1
Date: July 15, 1996
Project: X3T10.1/1146D
Ref Doc.: SSA-PH2 rev 1
Reply to: John Scheible

To: X3T10.1 Membership From: John Scheible

Subject: SSA-PH2 changes proposal

#### **BACKGROUND**

Following the release of SSA-PH2 rev 1, several changes have been proposed on the reflector. This proposal pulls these changes together in a single proposal. Revision 1 of this proposal includes those changes agreed to at the June 1996 X3T10.1 plenary meeting.

## PROPOSAL 1 (Receiver does not have bypass capacitors)

Changes: When I created rev 1, I added bypass capacitors to both the receiver and driver. This is not correct, as the Receiver has no bypass capacitors. Make the following changes.

- 1) Figure 2 Remove the bypass capacitors shown in both receivers.
- 2) Figure 10 Remove the bypass capacitors shown in the receiver.

## PROPOSAL 2 (Need words on Driver resister values)

Changes: Add...

- a) A new sub clause 7.1.6 as defined below.
- b) Modify Figure 6 as shown below.
- c) Change the first paragraph of 7.1.3 to...

Line drivers shall be tested using the test circuit shown in Figure 6. Line Driver measurements shall be made at the port connector. The distance between the port connector and the load resistance LR (labeled X in Figure 6) shall not exceed 5 cm. The distance between the measurement point and the load resistance LR (labeled Y in Figure 6) shall not exceed 5 mm. The difference between the X values of the LineOut+ and LineOut- lines of the driver shall not exceed 5 mm. The resistance of the combined load resistance LR and measurement resistance MR shall be 75  $\Omega \pm 2\%$ . The connecting media shall meet the requirements in 7.5 for this test. The total additional capacitive load from the scope probing scheme between Line+ and ground and between Line- and ground at the port connector shall be 2 pF  $\pm$  20%. This capacitance includes the sensing probes and equipment added to enable probing. Additional discrete capacitors between the scope probes and the port connector ground may be needed. The load shall conform to the characteristic impedance requirements in 7.3 at its connector.

#### 7.1.6 Line Driver termination

The LineOut+ and LineOut- lines of the Line Driver shall be terminated between the driver and the series capacitor with terminating resistors of 75  $\Omega$   $\pm$  1% connected to the terminating voltage (see Figure 6). The series capacitor between the terminating resister and the connector shall have a value of TBD when measured with the measurement scheme specified in TBD.

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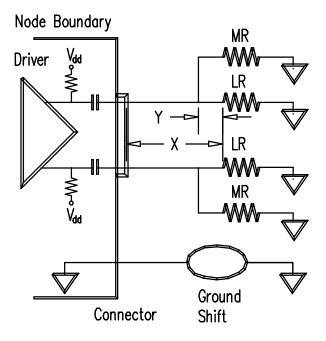


Figure 6 - Test environment for drivers

# Sincerely,

John Scheible

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