

To: X3T10.1 Membership

X3T10.1/96a104r0

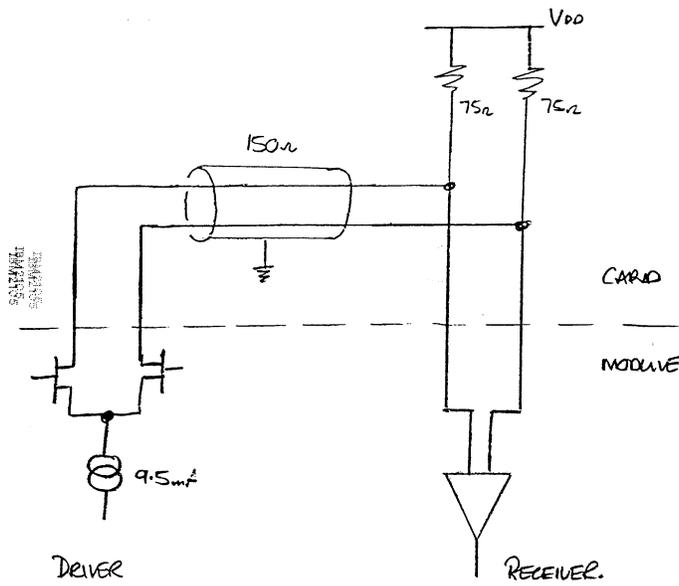
From: P. Murfet

Subject: Source Terminations Options

Date: January 1996

96A104r0

SSA20 TERMINATION



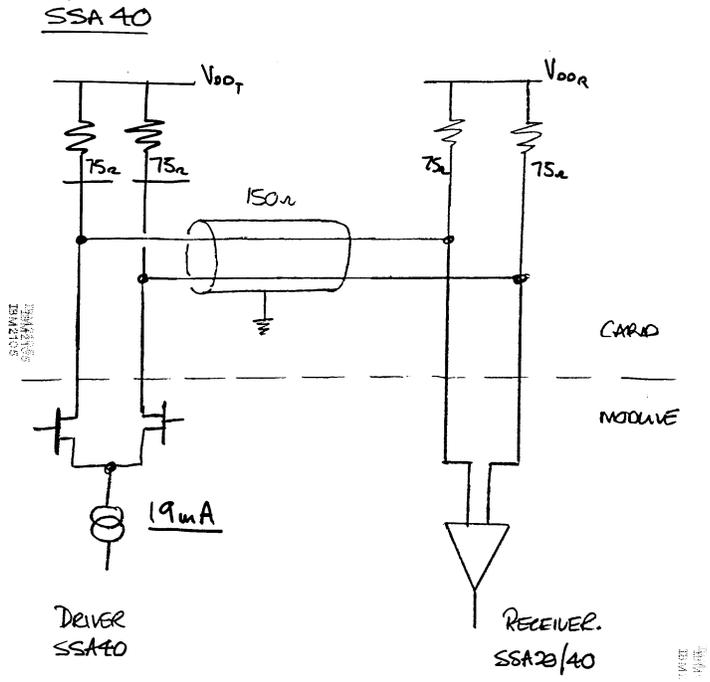
96A104r0

NEAR END TERMINATION REQUIRED?

ALTERNATIVE

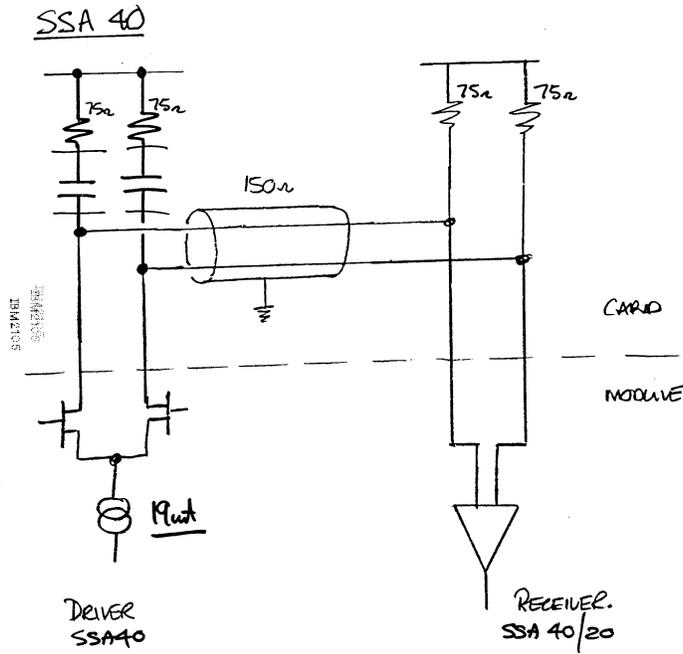
- \* HIGHER ERROR RATES
- \* SHORTER CABLE
- \* MORE EXPENSIVE CARDS, BACKPLANE, SILICON...
- \* SSA40 WOULD BE KNOWN AS A DIFFICULT STANDARD TO USE

96A104A0



- \* SSA20 COMPATIBLE.
- \* RECEIVE EYE UNCHANGED & DRIVER.
- \* RECEIVER DC COMMON MODE CHANGE (WITHIN SSA20/40)
- \* DC CURRENT PATH

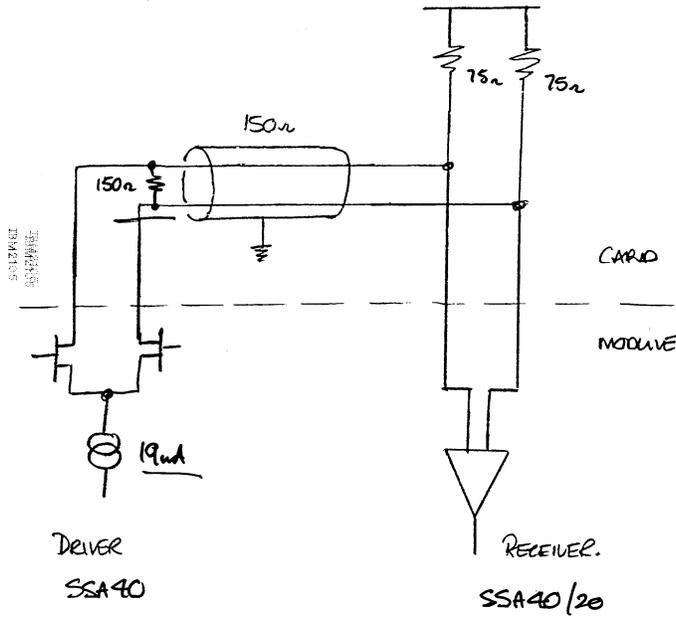
96A104B0



- \* SSA20 COMPATIBLE
- \* RECEIVER EYE UNCHANGED & DRIVER
- \* RECEIVER DC COMMON MODE CHANGE -350mV (WITHIN SSAPH1)

96A104EC

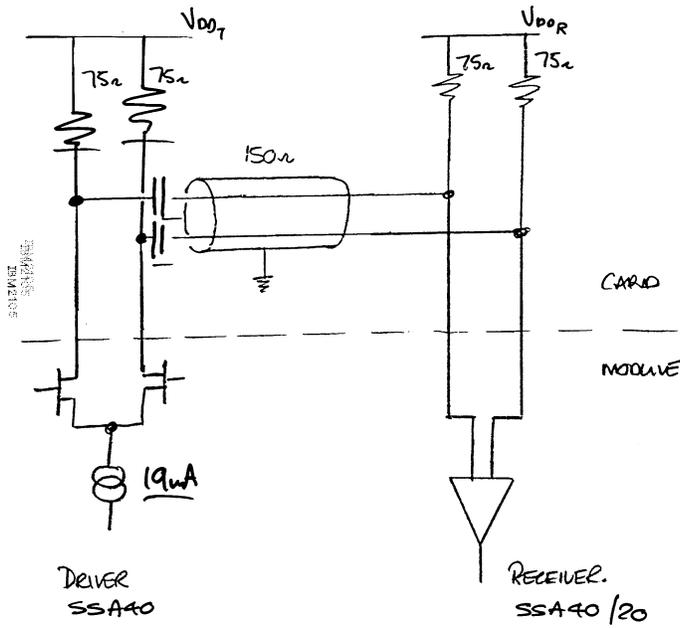
SSA 40



- \* SSA20 COMPATIBLE
- \* RECEIVER EYE UNCHANGED & DRIVE
- \* RECEIVER COMMON MODE INPUT -356mV (WITHIN SSA40/20)

96A104D0

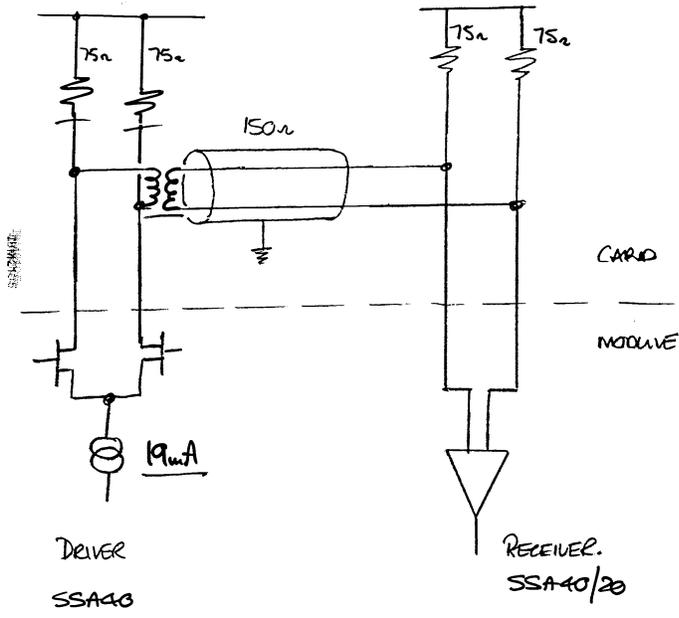
SSA 40.



- \* SSA20 COMPATIBLE
- \* RECEIVER EYE UNCHANGED.  
  & DRIVER.
- \* RECEIVER DC COMMON MODE  
  +356mV (WITHIN SSA40)

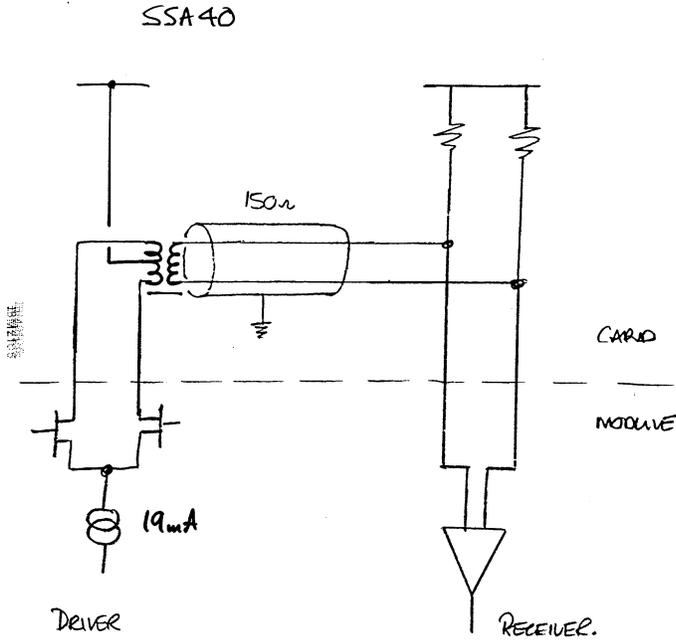
96A104E0

SSA40



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96A104F0



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96A10460

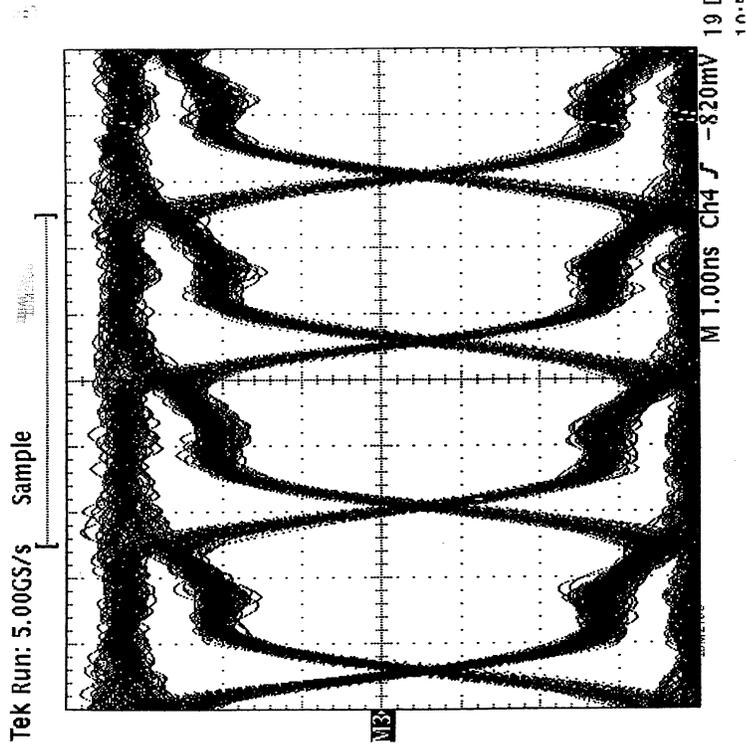
SSAAO TERMINATION

SOULMART  
SOLUTIONS

- \* RECEIVER TERMINATION  
MUST NOT CHANGE.
- \* DOUBLE OUTPUT CURRENT  
+ DRIVER TERMINATION  
OPTION FOR PH2.
- \* IMPROVE COMMON MODE  
INPUT DEFINITION FOR  
PH1 & 2.
- \* RECOMMEND A PREFERRED  
METHOD ---- ?

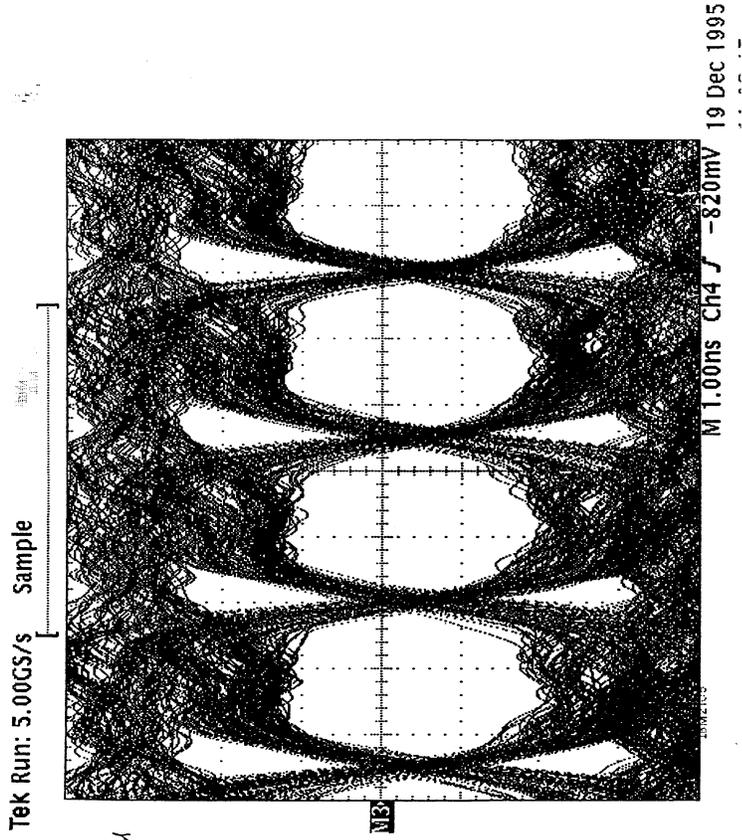
SOULMART  
SOLUTIONS

96A104H0



in 28 gang  
Doble terminated

96A104E0



1m 28 gerge  
Single terminated

96A104TD