



X3T9.2/92-11

Date: May 14, 1992
To: X3T9.2 Committee
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Subject: ATA Extensions Proposal -- ATA 68 Pin Connection

This document is a proposal describing how the ATA signals are connected to a 68-pin connector. This is the same connector that is used by PCMCIA Cards.

These are the same signals as defined in the ATA Rev 3.0 document. Because a drive using this connector could be plugged into a PCMCIA socket by mistake, two signals have been added to the ATA-68 connector: -IDE1 and -IDE2. These two signals will be grounded in the socket. It's recommended that an ATA drive monitor these signals and only respond if these two signals are true (0). A PCMCIA socket will never have these two signals true at the same time.

The following table defines the 68-pin connection:

Pin #	ATA Signal	Notes
1	GND	
2	DD3	
3	DD4	
4	DD5	
5	DD6	
6	DD7	
7	-CS1FX	
8		
9	-IDE1	
10		
11		
12		
13		
14		
15	-IDE2	
16	INTRQ	
17	VCC	
18		
19		
20		
21		
22		
23		
24		
25		
26		
27	DA2	
28	DA1	
29	DA0	
30	DD0	
31	DD1	
32	DD2	
33	-IOCS16	
34	GND	

Pin #	ATA Signal	Notes
35	GND	
36		
37	DD11	
38	DD12	
39	DD13	
40	DD14	
41	DD15	
42	-CS3FX	
43		
44	-DIOR	
45	-DIOW	
46		
47		
48		
49		
50		
51	VCC	
52		
53		
54		
55		
56	SPSYNC:CEL	
57		
58	-RESET	
59	IORDY	
60	DMARQ	
61	-DMACK	
62	-DASP	
63	-PDIAG	
64	DD8	
64	DD9	
66	DD10	
67		
68	GND	

All undefined signals are reserved and shall be left unconnected at the ATA drive. (Some of these reserved pins may be connected in the socket.)