IEEE Standards PROJECT AUTHORIZATION REQUEST (PAR)

1. Date of Request: 02/04/92		2. Assigned Project #: P1285
3. Does this PAR revise a previ	ously approved PAR?	
4. Description of Proposed Document	Standard K New Recommended Practice Guide	X Trial Use □ □ of Std Full Use □X
5. Project Title:		
Standard for Scalable	Storage Interface	
"storage unit" can enco (such as Flash memory) coordinated storage uni partitioning, among oth	mpass rotating storage (such as a d . The interface is intended for use ts. Issues of concurrency, latency, b ers, will be addressed. Support is t	nent to printed circuit boards. The term lisk drive) or non-rotating, non-volatile stor with either a single storage unit or with ma andwidth, extensibility, negotiation and o be provided for deterministically scheduli
Small-sized storage unithat allows elimination interface model in which channel, represents a particle of data transfers spanning by real-time application which may be determing a SPONSOR: Society: Committee: Mi	of today's connectors and cables, for a storage units become part of main radigm shift which simplifies the stang large numbers of units. The increase underscore the need for support in istic. In the standard: C/MM P1285 Works are to storage to the standard:	nit boards require a new interface architector reasons of cost, size, reliability, etc. An memory, rather than attachments to an I/corage unit design and can support scheduliceasing demands placed on storage performs in future systems of data transfer scheduling results. Standards Subcommittee
Small-sized storage unithat allows elimination interface model in which channel, represents a particle of data transfers spanning by real-time application which may be determing as SPONSOR: Society: Constitute: Misser Committee: Misser Completion Date: 10. Target Completion Date: 11. Proposed Coordination: (See SCC10 (IEEE Diction ASC X3 (T9.2) by Completion Date)	ts attached directly to printed circulation of today's connectors and cables, for a storage units become part of main radigm shift which simplifies the standigm shift which simplifies the standigm shift which simplifies the standards of units. The increase underscore the need for support in istic. In the standards of the stand	nit boards require a new interface architecter reasons of cost, size, reliability, etc. An memory, rather than attachments to an I orage unit design and can support scheduli reasing demands placed on storage performs in future systems of data transfer scheduling as Standards Subcommittee

Last PAR Revision: March 11, 1992

PROJECT AUTHORIZATION (cont'd)	N REQUEST (PAR)
13. Copyright Agreement for IEEE Standards	
I hereby acknowledge my appointment as Official Reporter to the P1285	
Standards Publication (entitled or to be entitled) Standard:	for
Scalable Storage Interface	
In consideration of my appointment and the publication of the Standards Pu	ion any copyrighted or proprietary material of another
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martin I le	man
Name /s/ Martin Freem (chair of working grou	an
(chair of working ground title Principal Scientist	· *****
Date 3/18/92	t .
Base O / 10/ /	
14. Person delegated to receive communications and conduct liaison with interested	ed parties:
(This is normally the chair of the working group. If not, please indicate IEEE	g position.)
Name: Martin Freeman	Telephone: 415-354-0329
Company: Philips Research	Fax: 415-354-0309
Address: 4189 Donald Drive	Telex:
City: Palo Alto State: CA	Zip: 94306 E Mail mfreeman@sierra.stari
,	
15. Submitted by: (This is normally the sponsor's liaison to the Standards Board. If not, please	indicate IEEE position and relationship to the sponsor.)
	-
Name: Stephen L. Diamond	Telephone: 415-336-4190
Company: SunSoft, Inc.	Fax: 415-336-4477
Address: 2550 Garcia Ave., M/S MTV08-221	Telex:
City: Mountain View State: CA	Zip: 94043 E Mail steve.diamond@Eng.Sun