

PROPOSAL #1

SHORT DISTANCE DIFFERENTIAL SCSI TRANSCEIVER

ADVANTAGES:

- o HIGHER DATA RATE - CAN CLOCK DATA SOONER THAN SINGLE-ENDED (SE).
- o LESS SENSITIVE THAN SE TO CABLE AND TERM POWER PROBLEMS.
- o FAST SCSI AT 6 METERS?
- o SIMPLE, CHEAP AND EASY TO INTEGRATE. ADDS ONLY 11 PINS.
- o BACKWARD COMPATIBLE WITH SE.

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10/27/89



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X379.2/89-131

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DISADVANTAGES:

- o LIMITED TO SHORT CABLES.
- o WAVEFORMS "UGLY"

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TRANSCEIVER PARTITIONING AND CHARACTERISTICS

DATA AND PARITY LINES (9)

- o COMPLIMENTRY OPEN COLLECTOR DRIVER.
- o DIFFERENTIAL RECEIVER.

REQUEST AND ACKNOWLEDGE LINES (2)

- o ACTIVE PULL-UP AND PULL-DOWN DIFF. DRIVER (RS-422 TYPE).
- o DIFFERENTIAL RECEIVER.

CONTROL LINES (7)

- o SINGLE-ENDED DRIVER AND RECEIVER
(CURRENT SPECIFICATION).

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SINGLE-ENDED (SE) CABLE PIN ASSIGNMENTS ALLOW EXPANSION TO DIFFERENTIAL AND BACKWARD COMPATABILITY.

- o ODD LINES (EXCEPT #25) ARE GROUNDED ON SE CABLE.
- o CONVERT ADJACENT GROUND LINE TO SECOND DIFFERENTIAL LINE FOR A DIFFERENTIAL SYSTEM.
- o FOR BACKWARD COMPATABILITY, DESIGN DIFFERENTIAL RECEIVER HAVING TTL THRESHOLD WHEN INVERTING INPUT IS OPEN. (THRESHOLD COULD ADJUST FOR OPTIMUM SE NOISE MARGIN).

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SINGLE-ENDED SCSI:

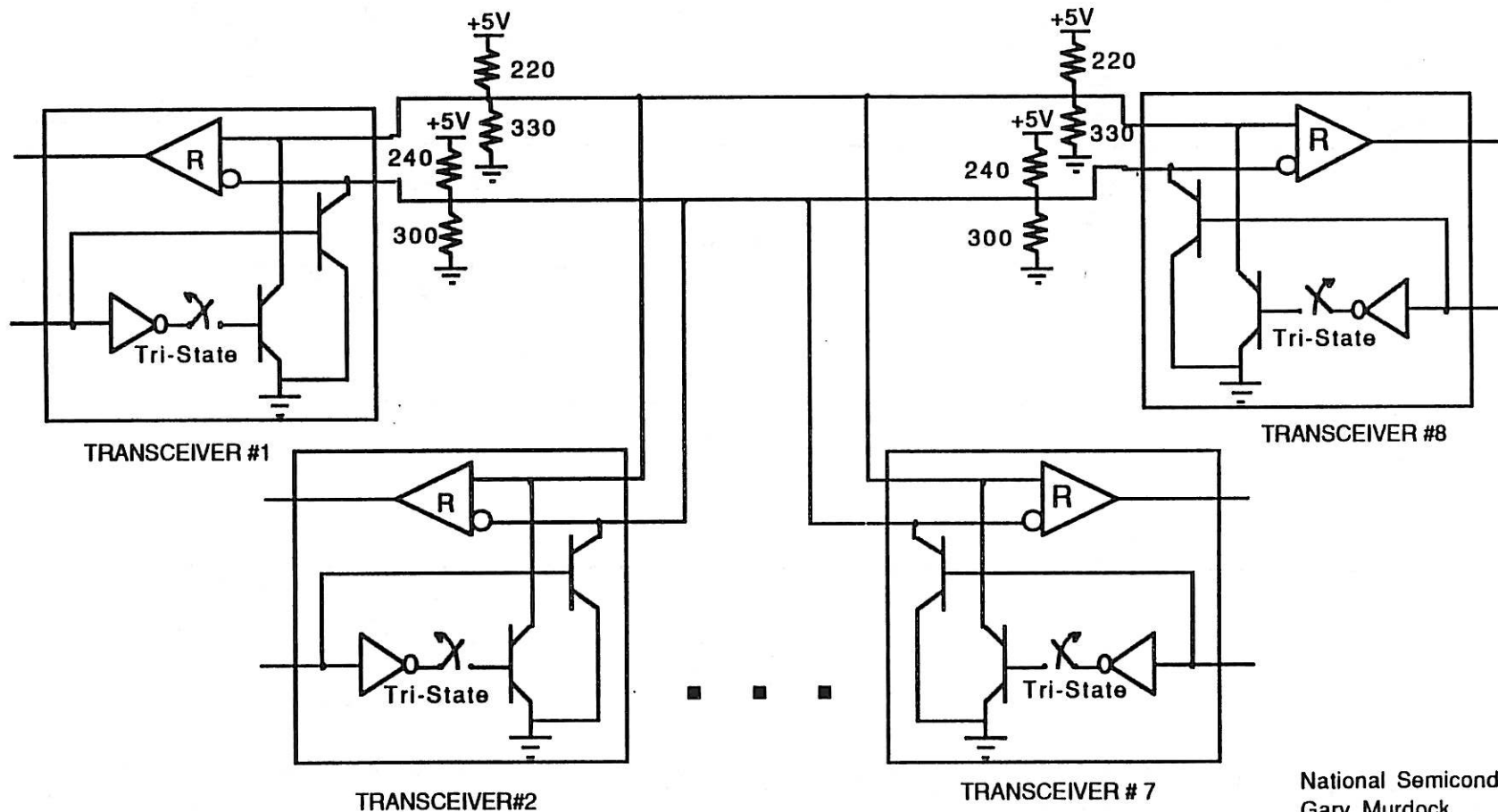


The diagram illustrates a bidirectional bus system. A central cable has two signal lines: SIGNAL (+) and SIGNAL (-). On the left, the IN signal is connected to a tri-state buffer and an inverter (R). The tri-state buffer's output is connected to the SIGNAL (+) line through a 240Ω resistor to +5V and a 300Ω resistor to ground. The inverter's output is connected to the SIGNAL (-) line through a 220Ω resistor to +5V and a 330Ω resistor to ground. The OUT signal is connected to the SIGNAL (+) line through a 220Ω resistor to +5V and a 330Ω resistor to ground. On the right, the SIGNAL (+) line is connected to an inverter (R) and a tri-state buffer. The inverter's output is connected to the OUT signal. The tri-state buffer's output is connected to the SIGNAL (-) line through a 240Ω resistor to +5V and a 300Ω resistor to ground. The SIGNAL (-) line is also connected to a tri-state buffer and an inverter (R) through a 240Ω resistor to +5V and a 300Ω resistor to ground. The tri-state buffer's output is connected to the IN signal. The inverter's output is connected to the SIGNAL (+) line through a 220Ω resistor to +5V and a 330Ω resistor to ground.

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CIRCUIT DIAGRAM: ONE CHANNEL, DATA AND PARITY LINE TYPE.

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BENCH TEST - FAST SCSI DATA LINE (100ns PULSE WIDTH)

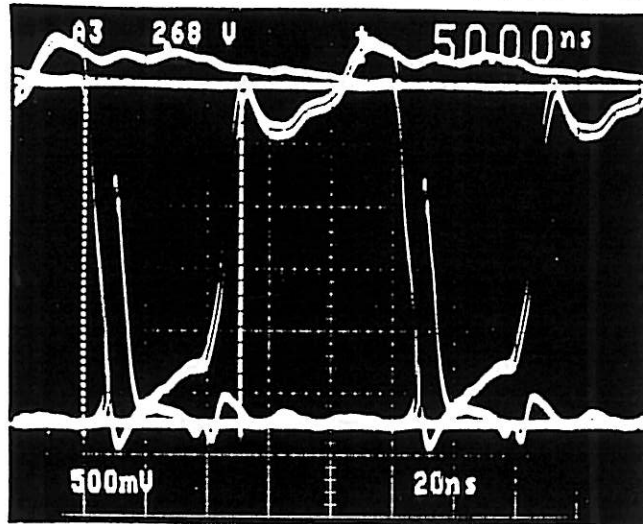
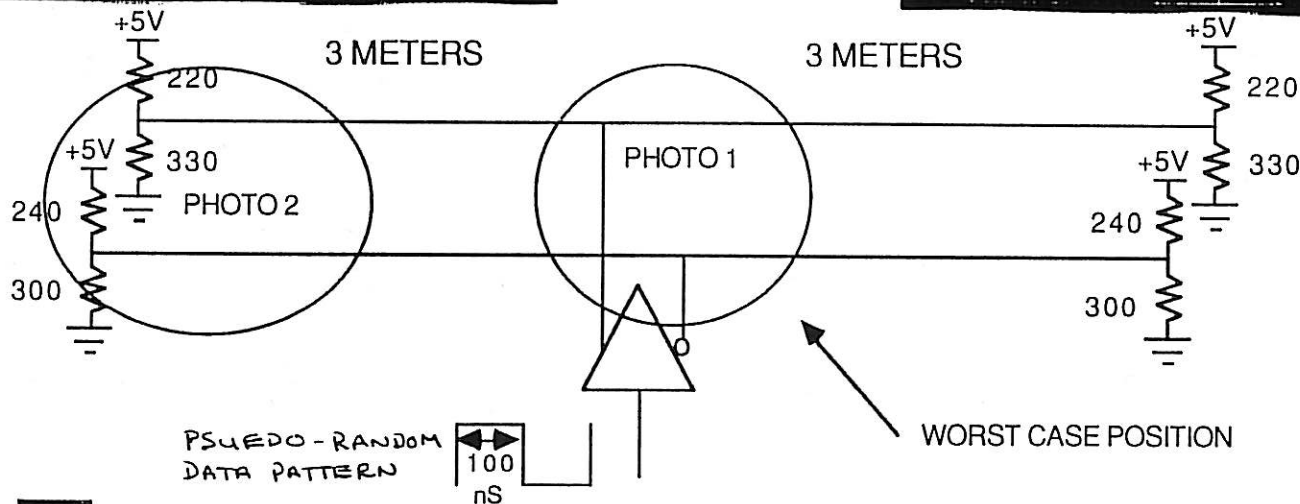
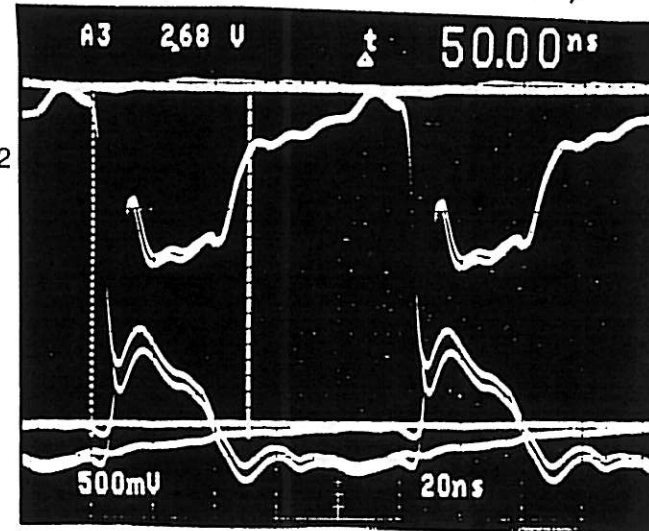


PHOTO 1

PHOTO 2



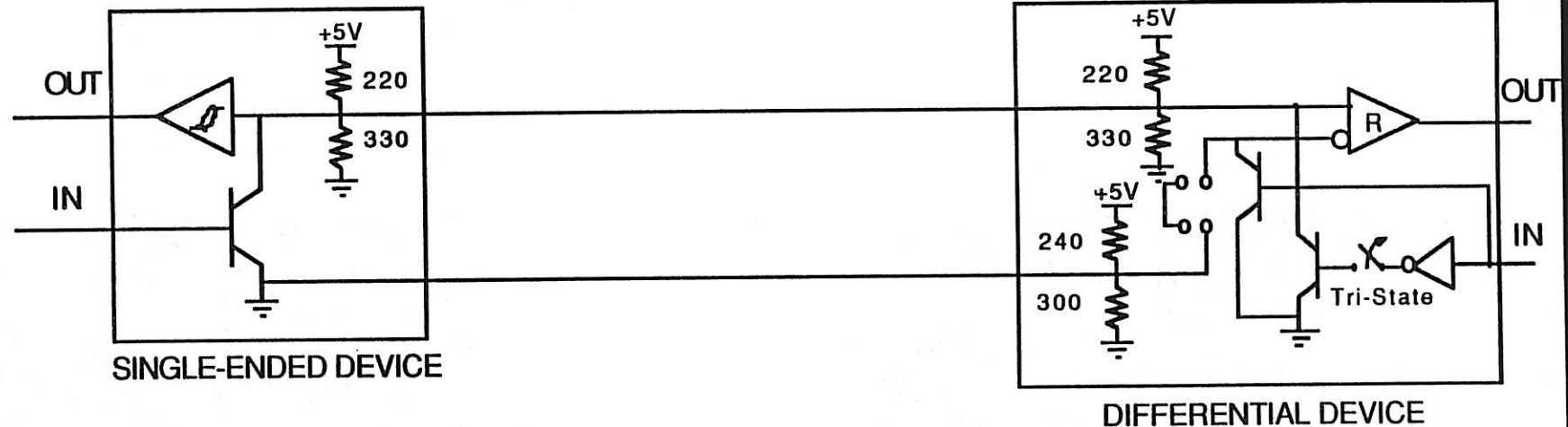
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INTERFACING SINGLE-ENDED TO DIFFERENTIAL SCSITO INTERFACE:

- 1) REMOVE 240/300 TERMINATOR AND REPLACE WITH SHORT TO GROUND.
- 2) REMOVE JUMPER BETWEEN INVERTING INPUT OF DIFFERENTIAL RECEIVER AND 240/300 TERMINATOR.
- 3) TOO MUCH TERM POWER IF DIFFERENTIAL DEVICE CONNECTED TO SINGLE-ENDED DEVICE BEFORE PERFORMING STEPS (1) AND (2) ABOVE?

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