November 9, 1988

7._._. Read.Write Error Recovery Parameters Page

Table 7-__: Read-Write Error Recovery Parameters

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The read-write error recovery parameters page (Table 7._) specifies the error recovery and reporting parameters that the target shall use when transferring data between the initiator and the medium. These parameters only apply to read-write errors and do not affect message system retries or positioning error recovery procedures.

An automatic write reallocation enabled (AWRE) bit of one indicates that the target shall enable automatic reallocation of defective data blocks during write operations. An AWRE bit of zero specifies that the target shall not perform automatic reallocation of defective data blocks during write operations.

An automatic read reallocation enabled (ARRE) bit of one indicates that the target shall enable automatic reallocation of defective data blocks during read operations. An ARRE bit of zero specifies that the target shall not perform automatic reallocation of defective data blocks during read operations.

Automatic read and write reallocation, if supported, is performed at the discretion of the target in a manner similar to the REASSIGN BLOCKS command (see 8._._). All error recovery and reporting actions specified by the TB, RC, EER, PER, DTE, and DCR bits shall be executed. Data is placed in the reallocated block if the data could be recovered within the limits established by the error recovery parameters. Error reporting as required by the error recovery parameters shall be performed only after completion of the reassignment. The reassignment process shall report any failures that occur during the reassignment process. See the REASSIGN BLOCKS command for error procedures.

A transfer block (TB) bit of one indicates that a data block that is not recoverable within the specified recovery limits shall be transferred to the initiator before CHECK CONDITION status is returned. A TB bit of zero indicates that the unrecoverable data block shall not be transferred to the initiator. Data blocks that can be recovered within the recovery limits are always transferred, regardless of the value of the TB bit.

A read continuous (RC) bit of one indicates that the target shall transfer the entire requested length of data without adding delays to perform error recovery. This implies that the target may send data that is erroneous or fabricated in order to maintain a continuous flow of data. The target shall assign priority to this bit over the EER, PER, DTE, and DCR bits within this byte. A RC bit of zero indicates that target shall use the specified error recovery and reporting parameters when an error occurs during the requested data transfer. Data shall not be fabricated when the RC bit is set to zero.

IMPLEMENTORS NOTE: This bit set to one is typically used in image processing, audio, or video applications where uninterrupted data flow is more important than error-free data.

The error recovery bits (EER, PER, DTE, and DCR) are defined in Table _._. Device types may provide additional definition for combinations of these bits in the appropriate device section.

Error Recovery Bit Definitions

EER PER DTE DCR Description An enable early recovery (EER) bit of one indicates that the target shall use the most expedient error recovery algorithm (e.g., attempt error correction prior to readwrite retries). An EER bit of zero indicates that the target shall use the most deliberate read-write error recovery algorithm, within the limits established by the other error recovery parameters (e.g., attempt to recover the block error-free prior to using error correction). A post error (PER) bit of one indicates that the target shall return CHECK CONDITION status to report recovered errors. A PER bit of zero indicates that the target shall not report errors recovered within the limits established by the error recovery parameters. If this bit is zero, the DTE bit must also be set to zero. 1 A disable transfer on error (DTE) bit of one indicates that the target shall terminate the data transfer after a recovered read-write error occurs. All data from the recovered block shall be transferred prior to terminating the read-write operation. A DTE bit of zero indicates that the target shall not terminate the transfer for errors recovered within the limits established by the read-write error recovery parameters. A disable correction (DCR) bit of one indicates that the target shall use the minimal error correction allowable by the logical unit during error recovery. A DCR bit of zero indicates that the target may use any available error correction algorithm for error recovery. IMPLEMENTORS NOTE: Some logical units may have error correction algorithms not controllable by the target.

The read retry count specifies the number of times that the target should attempt its recovery algorithm during a read operation before an unrecoverable error is reported. A read retry count of zero indicates that the target shall not use its recovery algorithm during read operations.

The correction span field specifies the size, in bits, of the largest burst data error for which data error correction may be attempted. A value of zero indicates that no correction span is specified.

The head offset count field specifies, in two's complement notation, an incremental position to which the head(s) shall be offset from the nominal setting. The effect of this field on write operations is unspecified. A positive value indicates moving the head(s) in a positive direction as specified by the target. A negative value indicates moving the head(s) in a negative direction as specified by the target. Any value specified in the head offset count field does not preclude the target from using positive or negative head offset during error recovery. However, after any error recovery is completed, the target shall return the head offset to the value specified in this field. A value of zero indicates that no offset is specified.

The Data Strobe Offset Count field specifies, in two's-complement notation, an incremental position to which the recovered data strobe, or its equivalent, shall be adjusted from the nominal setting. The effect of this field on write operations is unspecified. A positive value indicates movement in a positive direction as defined by the target. A negative value indicates movement in the negative direction as defined by the target. Any value specified in this field does not preclude the target from using positive or negative data strobe offset during error recovery. However, after any error recovery is completed, the target shall return the data strobe offset to the value specified in this field. A value of zero indicates that no data strobe offset is specified.

The target shall return CHECK CONDITION status and shall set the sense key to ILLEGAL REQUEST if an unsupported head or data strobe offset count is specified. The valid bit shall be set to one and the information bytes shall be set to the positive value of the maximum offset count that is supported. The target shall set the valid bit to zero if it cannot determine the maximum offset count supported.

IMPLEMENTORS NOTE: If the head offset or data strobe offset count field is implemented, it is recommended that the target support an equal number of positive and negative offset counts. The number of valid values and the degree of offset for each incremental count is device specific.

The write retry count specifies the number of times that the target should attempt its recovery algorithm during a write operation before an unrecoverable error is reported. A write retry count of zero indicates that the target shall not use its recovery algorithm during write operations.

The recovery time limit field specifies, in increments of one millisecond, the maximum time duration that the target shall use for data error recovery procedures for any individual logical block. The target may round this value as described in section 6.5.4. If both retry count and recovery time limit are specified, the field that requires the least amount of time for data error recovery shall have priority. A value of zero indicates that no recovery time limit is specified.