TEXAS INSTRUMENTS

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MEMORANDUM

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TO: John Lohmeyer, Chairman X3T9.2

FROM: Bill Spence, Texas Instruments

SUBJECT: Target Message Response Clarification

In our review of our error recovery paths, our people have encountered what we think is wording that fails quite to explicate what we believe everyone would recognize as the intended meaning. In the last paragraph of Scn. 5.1.10.2, p 5-9 of SCSI-2, Rev 3, it says that a target "...may...indicate that it has... received the message...by changing to BUS FREE...". The "e.g." (for instance) phrase following may not ameliorate the erroneous nature of this statement. A way to make this paragraph say what it is supposed to say may be as follows.

     POSAL

In the last paragraph of Scn 5.1.10.2, replace the second sentence with the following:

If, however, an ABORT or BUS DEVICE RESET message is included among the message bytes, the target shall change to the BUS FREE phase as soon as the ABORT or BUS DEVICE RESET message is decoded. Note: if the second of an exchange of Synchronous Data Transfer Request or Wide Data Transfer Request messages precedes the ABORT or BUS DEVICE RESET message, the indicated agreement is in effect between the two SCSI devices involved.

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