To: ANSI Working Group
From: Randy Weber, Emulex Corporation

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Subject: Sequential Access Recoverable Error Counters

1.0 Introduction

This document is intended as a proposal in conjunction with ANSI document X3T9.2/87-43, entitled "Common Read Log Command" by Greg Floryanco. This document proposes the addition of a Recoverable Error (Retry) Count Page to the Read Log Command for Sequential Access Devices.

2.0 Proposal

This proposal intends to use Page Code 6 as the "Recoverable Error Count" page, which will consist of the data described in section 2.1.
Description of Returned Data

For ParityCode 1, 2 and 3, the counter will be cleared whenever a power-on or reset occurs, or when new medium is inserted, or when the READ LOG command is issued with the NRD bit set to zero.

The Write Recoverable Error Count Length and Read Recoverable Error Count Length and ECC Correction Count Length fields indicate the length of each counter in bytes. It is recommended that the total parameter length (including the counter) be a multiple of 2. A length value of zero indicates that the counter is not supported.

The Write Recoverable Error Counter field is incremented each time the target uses a write retry method to secure a block of data on the medium.

The Read Recoverable Error Counter field is incremented each time the target uses a read-retry algorithm to obtain a block of data from the medium. An ECC correction without a read-retry shall also be considered a Read Recoverable Error.

The ECC Correction Counter field is incremented each time the target uses an ECC correction method to obtain a block of data from the medium.