

Change Request from Phoenix Technologies for EDD 3 Specification draft (D1572r1a)

1. Section 8.24.3.9.12

Define Byte offset 10-11 (decimal), Bit offset 12 (see Table 15) as below:

Bit shall be SET if the HPA (Host Protect Area) is active (active meaning, SET MAX command (either volatile or non-volatile) has been set to a value other than NAVITE MAX address. Otherwise bit shall be CLEARED. <OK>

2. Section 8.24.3.9.13

Define Byte offset 10-11 (decimal), Bit offset 13 (see Table 15) as below:

Bit shall be SET if the INT13h handler is using 48Bit LBA addressing for the device. Bit shall be CLEARED if the INT 13h handler is using 28Bit LBA addressing for the device. <OK>

3. Section 8.24.3.11.14

Define Byte offset 10-11 (decimal), Bit offset 14 (see Table 15) as below:

*The table version shall be set to **30h** indicating compliance with this standard. <OK>*

4. Section 8.17 and 8.24

Proposal to make it MANDATORY to return a 'valid' DPTE pointer in function 48h. This affects section 8.17 (bit offset 2) and section 8.24 (Table 12). <OK>

Section 8.17:

Table 1 - Check Extensions Present Buffer

Bit	Description
0	If the content of this bit is one, then the Fixed disk access subset shall be supported.
1	If the content of this bit is one, then the Device locking and ejecting subset shall be supported.
2	<i>If the content of this bit is one, then the Enhanced disk drive support subset shall be supported.</i>
3	If the content of this bit is one, then the 64-bit extensions are present. When the 64-bit extensions are present, the fields starting at offset 10h in the Device Address Packet are valid
4-15	Reserved. The content of these bits shall be zero.

Change:

2	This bit MUST bit set to one, indicating Enhanced disk drive support present.
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Section 8.24

Table 2 - Result Buffer

26	DWord	Pointer to the Device Parameter Table Extension (DPTE). This field follows the seg:offset address format. The DPTE shall only be present if INT 13h, Fn 41h, CX register bit 2 is set to one. This field points to a temporary buffer that the BIOS may invalidate on subsequent INT 13h calls. A value of FFFFh:FFFFh in this field means that the DPTE is not present. If the length of this result buffer is less than 30, the DPTE shall not be present. This field is only used for legacy INT 13h based systems configured with ATA or ATAPI devices.
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Changes:

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5. Typo error: Section 8.24

Error: Table 12: Offset 32

Offset 32 should be 44 (decimal) bytes, not 44h bytes. This spans offsets 30 through 73. <OK>