

To: T10 Membership
From: Lawrence J. Lamers, Adaptec, Inc. <ljlammers@ieee.org>
Subject: STT Behavior for Domain Validation
Date: Wednesday, November 17, 1999

Background: There has been a lot of discussion on how to ensure that the domain validation process when doing margining doesn't consume too much time. The STT timer in targets is one method to address this issue.

Add the following to SPI-4:

Targets supporting domain validation shall implement the STT timer and the Port Control page (19h).

The default value for the STT timer shall be 0001h. This will allow devices that cannot accept a MODE SELECT command prior to spin-up to function correctly during domain validation.

The driver sets the STT timer to a run-time value following a domain validation sequence.