NCITS SUBGROUP ANNUAL REPORT

Annual Report for: T10
Covering the Period: September 1998 to September 1999
Title of NCITS Subgroup: Lower Level Interfaces
Informal Description of Work: T10 develops standards and technical reports on I/O interfaces, particularly the Small Computer System Interface (SCSI).

I. Executive Summary

T10 continues to run smoothly with 39 approved projects. Most of the active projects are related to the SCSI-3 family of standards. T10 membership has increased slightly and is at 49 organizations.

Over the last three years, there has been a renewed interest in parallel SCSI. This is mostly due to work on the driver/receiver technology called Low-Voltage Differential (LVD). This work extends SCSI's maximum cable lengths and the maximum data rates supported with only a slight increase in costs. This work is documented in the SPI-2, SPI-3, and SPI-4 projects.

II. Projects

1. Interfaces Between Flexible Disks and Their Host Controllers

   a. Project 0052-M, Interfaces Between Flexible Disks and Their Host Controllers

   b. Target date for dpANS to NCITS:
      Original target date: 
      Previous target date: 
      Current target date: 

   c. Project Description: This standard has been withdrawn.

   d. Publications during the past year: none.

   e. Statement of Progress or Accomplishments During Year: T10 requested that this standard be withdrawn. The ISO standard, A/I 9315:1989[1994], remains active.

   f. Statement of Status as of This Report: Withdrawn.
2. Storage Module Interfaces (SMD-E)
   a. Project 0053-RF Storage Module Interfaces (SMD-E)
   b. Target date for dpANS to NCITS:
      Original target date:
      Previous target date:
      Current target date: Maintenance Phase -- 5yr review due in 2002.
   c. Project Description: This is a maintenance project on X3.91-1992, Storage Module Interfaces.
   d. Publications during the past year: none.
   e. Statement of Progress or Accomplishments During Year: Reaffirmed in 1997.
   f. Statement of Status as of This Report: Maintenance Phase.
   g. Future Plans: none.
   h. Reasons for Delay: none.

3. Small Computer System Interface (SCSI-2)
   a. Project 0375-R, Small Computer System Interface (SCSI-2)
   b. Target date for dpANS to NCITS:
      Original target date:
      Previous target date:
      Current target date: Maintenance Phase -- 5yr review due in 1999.
   c. Project Description: The SCSI protocol is designed to provide an efficient peer-to-peer I/O bus with up to 16 devices, including one or more hosts. Data may be transferred asynchronously at rates that only depend on device implementation and cable length. Synchronous data transfers are supported at rates up to 10 mega-transfers per second. With the 32-bit wide data transfer option, data rates of up to 40 megabytes per second are possible.

   SCSI-2 includes command sets for magnetic and optical disks, tapes, printers, processors, CD-ROMs, scanners, medium changers, and communications devices.
   d. Publications During Past Year: none.
   e. Statement of Progress or Accomplishments During Year: T10 recommended that NCITS reaffirm X3.131-1994.
   f. Statement of Status as of This Report: Maintenance Phase -- 5yr review due in 1999.
   g. Future Plans: none for SCSI-2; work continues on the SCSI-3 family of standards.
   h. Reasons for Delay: none.
4. **Enhanced Small Device Interface (ESDI)**
   a. Project 0587-M, Enhanced Small Device Interface (ESDI)
   b. Target date for dpANS to NCITS:
      Original target date: 
      Previous target date: 
      Current target date: Maintenance Phase -- 5yr review due in 1999.
   c. Project Description: This is a maintenance project on X3.170-1990[1994]/X3.170a-1991[1994].
   d. Publications During Past Year: none.
   e. Statement of Progress or Accomplishments During Year: T10 has recommended that NCITS withdraw this standard.
   f. Statement of Status as of This Report: This standard should have been withdrawn in early 1999.
   g. Future Plans: none.
   h. Reasons for Delay: none.

5. **SCSI Common Access Method (SCSI CAM)**
   a. Project 0792-M, SCSI Common Access Method (SCSI CAM)
   b. Target date for dpANS to NCITS:
      Original target date: 
      Previous target date: 
      Current target date: Maintenance Phase -- 5yr review due in 2001.
   c. Project Description: This project defines a common method to access SCSI devices through a standard software interface to SCSI host adapters for several popular operating systems. This should result in simplified integration of products.
   d. Publications During Past Year: none.
   e. Statement of Progress or Accomplishments During Year: ANSI X3.232-1996 published.
   f. Statement of Status as of This Report: Maintenance Phase.
   g. Future Plans: CAM-3 (Project 0990-D) is in development phase.
   h. Reasons for Delay: none.

6. **SCSI-3 Parallel Interface (SPI)**
   a. Project 0855-D, SCSI-3 Parallel Interface (SPI)
   b. Target date for dpANS to NCITS:
      Original target date: 
      Previous target date: 
      Current target date: Maintenance Phase -- 5yr review due in 2000.
c. Project Description: The SCSI-3 Parallel Interface standard maintains a high degree of compatibility with SCSI-2 while providing documentation for new capabilities including an option to permit 16-bit data transfers on a single cable and expanded bus connectivity options to increase the maximum number of SCSI devices on a cable from 8 to 16 or more. This standard does not address areas above the physical level (such as protocol and command sets). This standard is used in conjunction with the command sets defined in SCSI-2 and/or subsequent versions of SCSI.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: T10 has recommended that this standard be withdrawn in favor of SPI-2, X3.302:1999.

f. Statement of Status as of This Report: The public review to withdraw SPI closes 9 Nov 99.

g. Future Plans: SPI-3 (Project 1302-D) is in development phase and SPI-4 (Project 1365-D) has been authorized.

h. Reasons for Delay: none.

7. **SCSI-3 Interlocked Protocol (SIP)**

a. Project 0856-D, SCSI-3 Interlocked Protocol (SIP)

b. Target date for dpANS to NCITS:
   Original target date:
   Previous target date:
   Current target date: Maintenance Phase -- 5yr review due in 2002.

c. Project Description: The SCSI-3 Interlocked Protocol standard maintains a high degree of compatibility with the equivalent functions in SCSI-2 while defining several new features and functions. The candidate new features are support of more than 8 devices and other evolutionary features. This standard is intended to be used in conjunction with the SCSI-3 Parallel Interface standard and the SCSI-3 command set standards.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: T10 has recommended that this standard be withdrawn in favor of SPI-2, X3.302:1999.

f. Statement of Status as of This Report: The public review to withdraw this standard closes October 24, 1999.

g. Future Plans: No follow-on projects are planned for SIP because both SPI-2 and SPI-3 integrate the SIP functions.

h. Reasons for Delay: none.

8. **Serial Storage Architecture - Transport Layer - 1 (SSA-TL1)**

a. Project 0989-D, Serial Storage Architecture - Transport Layer (SSA-TL1)

b. Target date for dpANS to NCITS:
   Original target date:
   Previous target date:
   Current target date: Maintenance Phase -- 5yr review due in 2001.
c. Project Description: The SSA-TL1 standard will define a transport layer that uses the SSA physical layer to transport the protocol above it. The goals of SSA-TL1 are: 1) minimize gate count. 2) define a web that supports frame multiplexing. 3) define flow control that allows a tradeoff between distance and data rate. and 4) define a full duplex transfer mechanism.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Published.

g. Future Plans: SSA-TL2 (Project 1147-D) was completed.

h. Reasons for Delay: Completed ahead of schedule.

9. **Common Access Method - 3 (CAM-3)**

a. Project 0990-D, Common Access Method - 3 (CAM-3)

b. Target date for dpANS to NCITS:
   - Original target date: July 1994
   - Previous target date: July 1996
   - Current target date: November 1999 (optimistic)

c. Project Description: This project is intended to revise and enhance the SCSI Common Access Method (CAM) such as adding 64-bit addressing and additional queuing modes.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: Almost none -- there was one ad hoc meeting in June 1999 to discuss strategies to complete this project.

f. Statement of Status as of This Report: Progress continues to be slow. There is almost no interest in this project, except from the project editor.

g. Future Plans: none.

h. Reasons for Delay: Lack of interest.

10. **SCSI-3 Generic Packetized Protocol (GPP)**

a. Project 0991-DT, SCSI-3 Generic Packetized Protocol (GPP)

b. Target date for dpANTR to NCITS:
   - Original target date:
   - Previous target date:
   - Current target date: Maintenance Phase -- 5yr review due in 2002.

c. Project Description: The Generic Packetized Protocol is intended to provide a protocol that can take advantage of multiple physical interfaces in a length-independent manner (i.e., a minimum number of packets per I/O Process). The Generic Packetized Protocol encapsulates the SCSI protocol, functions, commands, status, and data requiring minimal services from the physical interface. This project was converted from a Standards project to a Technical Report project about a year ago.
d. Publications During Past Year: none.
e. Statement of Progress or Accomplishments During Year: none.
f. Statement of Status as of This Report: Published.
g. Future Plans: none.
h. Reasons for Delay: none.

11. **SCSI-3 Fibre Channel Protocol (FCP)**

a. Project 0993-D, SCSI-3 Fibre Channel Protocol (FCP)

b. Target date for dpANS to NCITS:
   - Original target date:
   - Previous target date:
   - Current target date: Maintenance Phase -- 5yr review due in 2001.

c. Project Description: The SCSI-3 Fibre Channel Protocol is intended to provide a protocol that can take advantage of the capabilities provided by the Fibre Channel physical layer to support an efficient, low-overhead transport service for SCSI products. The FCP is one of the protocols used in the FC-4 layer of Fibre Channel.

d. Publications During Past Year: none.
e. Statement of Progress or Accomplishments During Year: none.
f. Statement of Status as of This Report: Published.
g. Future Plans: FCP-2 (Project 1144-D) is in development phase.
h. Reasons for Delay: none.

12. **SCSI-3 Architecture Model (SAM)**

a. Project 0994-D, SCSI-3 Architecture Model (SAM)

b. Target date for dpANS to NCITS:
   - Original target date:
   - Previous target date:
   - Current target date: Maintenance Phase -- 5yr review due in 2001.

c. Project Description: The SCSI-3 Architecture Model defines the architecture of SCSI and provides a model for implementing several protocols on a variety of transport mechanisms. This standard will define a unifying framework for the implementation of SCSI.

d. Publications During Past Year: none.
e. Statement of Progress or Accomplishments During Year: none.
f. Statement of Status as of This Report: Published.
g. Future Plans: SAM-2 (Project 1157-D) is in development phase.
13. **SCSI-3 Primary Commands (SPC)**
   
a. Project 0995-D, SCSI-3 Primary Commands (SPC)
   
b. Target date for dpANS to NCITS:
   
   - Original target date: 
   - Previous target date: 
   - Current target date: Maintenance Phase -- 5yr review due in 2001.
   
c. Project Description: The SPC is intended to provide a definition of those commands absolutely necessary to function in an SCSI environment plus those commands that are defined consistently for more than one command set. This command set will provide the means to identify the device type and hence identify which command set is appropriate for the device.
   
d. Publications During Past Year: none.
   
e. Statement of Progress or Accomplishments During Year: none.
   
f. Statement of Status as of This Report: Published.
   
g. Future Plans: SPC-2 (Project 1236-D) is in development phase.
   
h. Reasons for Delay: none.

14. **SCSI-3 Block Commands (SBC)**
   
a. Project 0996-D, SCSI-3 Block Commands (SBC)
   
b. Target date for dpANS to NCITS:
   
   - Original target date: 
   - Previous target date: 
   - Current target date: Maintenance Phase -- 5yr review due in 2003.
   
c. Project Description: The SCSI-3 Block Commands is intended to provide a complete set of commands to complement the SCSI-3 Primary Commands, and will be applicable to devices which transfer data in fixed block sizes (e.g., disk drives).
   
d. Publications During Past Year: none.
   
e. Statement of Progress or Accomplishments During Year: none.
   
f. Statement of Status as of This Report: Published.
   
g. Future Plans: SBC-2 project may be proposed and RBC (Project 1240-D), which subsets the SBC command set, is in approval phase.
   
h. Reasons for Delay: none.

15. **SCSI-3 Stream Commands (SSC)**
   
a. Project 0997-D, SCSI-3 Stream Commands (SSC)
b. Target date for dpANS to NCITS:
   Original target date: June 1994
   Previous target date: November 1998
   Current target date: November 1999

c. Project Description: The SCSI-3 Stream Commands is intended to provide a complete set of commands to complement the SCSI-3 Primary Commands, and be applicable to devices which transfer data in a streaming manner (e.g., tape drives).

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: T10 letter ballot comments were resolved.

f. Statement of Status as of This Report: We expect to forward SSC to NCITS late this year.

g. Future Plans: An SSC-2 project will likely be proposed.

h. Reasons for Delay: Workload issues with the project editors -- we are on our third project editor who appears likely to complete the project.

16. **SCSI-3 Medium Changer Commands (SMC)**

a. Project 0999-D, SCSI-3 Medium Changer Commands (SMC)

b. Target date for dpANS to NCITS:
   Original target date:
   Previous target date:
   Current target date: Maintenance Phase -- 5yr review due in 2003.

c. Project Description: The SCSI-3 Medium Changer Commands is intended to provide a complete set of commands to complement the SCSI-3 Primary Commands, and be applicable to devices which can relocate data from an inventory location to and from a device.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Published.

g. Future Plans: An SMC-2 project may be proposed.

h. Reasons for Delay: none.

17. **SCSI-3 Controller Commands (SCC)**

a. Project 1047-D, SCSI-3 Controller Commands (SCC)

b. Target date for dpANS to NCITS:
   Original target date:
   Previous target date:
   Current target date: Maintenance Phase -- 5yr review due in 2002.
c. Project Description: The SCSI-3 Controller Commands standard is intended to provide a complete set of commands to complement the SCSI-3 Primary Command Set, and be applicable to devices which act as subsystem controllers, such as a disk array controllers.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: T10 has recommended that NCITS withdraw this standard in favor of SCC-2, NCITS318:1998.

f. Statement of Status as of This Report: Published.

g. Future Plans: none.

h. Reasons for Delay: none.

18. SCSI-3 Multimedia Commands (MMC)

a. Project 1048-D, SCSI-3 Multimedia Commands (MMC)

b. Target date for dpANS to NCITS:
   Original target date: 
   Previous target date: 
   Current target date: Maintenance Phase -- 5yr review due in 2002.

c. Project Description: The SCSI-3 Multimedia Commands standard is intended to provide, in conjunction with the SCSI-3 Primary Commands (SPC), a complete set of commands for CD devices, while maintaining a high degree of compatibility with SCSI-2 compliant CD-ROM devices.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Published.

g. Future Plans: MMC-2 (Project 1228-D) is in approval phase and MMC-3 (Project 1363-D) is in development phase.

h. Reasons for Delay: none.

19. Serial Storage Architecture - SCSI-3 Protocol (SSA-S3P)

a. Project 1051-D, Serial Storage Architecture - SCSI-3 Protocol (SSA-S3P)

b. Target date for dpANS to NCITS:
   Original target date: 
   Previous target date: 
   Current target date: Maintenance Phase -- 5yr review due in 2003.

c. Project Description: The SSA-S3P standard will define a protocol that maps the SCSI-3 command sets onto the transport layer and physical interface. This standard will maintain compatibility with SCSI-3 and the SCSI-3 Architecture Model. The goals of SSA-S3P are:
   a) support for dual port and alternate paths;  b) support for data field format extensions;  c) support for auto-sense;  d) support for third-party operations.

d. Publications During Past Year: none.
e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Published.

g. Future Plans: none.

h. Reasons for Delay: none.

20. **SCSI-3 Fast-20 Parallel Interface (Fast-20)**

a. Project 1071-D, SCSI-3 Fast-20 Parallel Interface (Fast-20)

b. Target date for dpANS to NCITS:
   - Original target date:
   - Previous target date:
   - Current target date: Maintenance Phase -- 5yr review due in 2001.

c. Project Description: The Fast-20 standard is intended to document extensions to SPI to permit transfer rates of 20 mega-transfers per second, while maintaining a high degree of compatibility with SPI.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: T10 has recommended that NCITS withdraw this standard in favor of SPI-2, X3.302:1999.

g. Future Plans: SPI-3 (Project 1302-D) is near the end of development phase and SPI-4 (Project 1365-D) is starting development phase.

h. Reasons for Delay: none.


b. Target date for dpANS to NCITS:
   - Original target date:
   - Previous target date:
   - Current target date: Maintenance Phase -- 5yr review due in 2001.

c. Project Description: The SSA-S2P standard will define a protocol that maps the SCSI-2 command sets onto the transport layer and physical interface. This standard will maintain compatibility with SCSI-2 to the extent possible in a serial environment. The goals of SSA-S2P are: a) provide an easy migration path to a serial interface; b) minimize the impact in converting firmware in existing devices; c) provide an architected error recovery mode; d) improve performance by reducing command overhead; e) define the data field format; f) provide the support needed for concurrent I/O processing.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Published.
Future Plans: SSA-S3P (Project 1051-D) was completed. No further SSA work is planned.

Reasons for Delay: none.

22. **SCSI Parallel Interface - 2 (SPI-2)**
   a. Project 1142-D, SCSI Parallel Interface - 2 (SPI-2)
   b. Target date for dpANS to NCITS:
      Original target date: 
      Previous target date: 
      Current target date: Maintenance Phase -- 5yr review due in 2004.
   c. Project Description: The SPI-2 standard will define a physical and protocol layers that will support the SCSI-3 command sets above it, while maintaining a high degree of compatibility with the current SPI and SIP standards. Candidates for inclusion in the SPI-2 draft standard are: 1) definition of a new driver/receiver technology to increase data rates, enhance signal margins, enhance cable lengths, and increase device counts; 2) enhancements to the physical layer to reduce power consumption and to address emerging market for lower voltage devices; 3) Maintenance of the SCSI physical level standard that may result from further implementation of the SPI standard.
   d. Publications During Past Year: none.
   e. Statement of Progress or Accomplishments During Year: Project completed.
   f. Statement of Status as of This Report: Published.
   g. Future Plans: SPI-3 (Project 1302-D) is near the end of development phase and SPI-4 (Project 1365-D) is starting development phase.
   h. Reasons for Delay: none.

   a. Project 1143-D, SCSI Enhanced Parallel Interface Technical Report (EPI)
   b. Target date for dpANS to NCITS:
      Original target date: 
      Previous target date: 
      Current target date: Maintenance Phase -- 5yr review due in 2004.
   c. Project Description: This technical report will address complex physical configurations of parallel SCSI having one or more of the following features: a) mixed single-ended and differential devices on separate segments of the same logical bus; b) higher device count (e.g. > 16 devices); c) physical bus segments with branches to improve transmission line effects; d) extended physical bus segment lengths allowed by the propagation delay assumptions already built into the parallel SCSI protocol; e) removal and replacement of devices on active buses; f) removal, replacement, and addition of physical bus segments in active systems; g) mixed power conditions in active systems.
   d. Publications During Past Year: none.
   e. Statement of Progress or Accomplishments During Year: Project completed.
f. Statement of Status as of This Report: Published.

g. Future Plans: none.

h. Reasons for Delay: Priority was given to SPI-2 development.


a. Project 1144-D, SCSI Fibre Channel Protocol - 2 (FCP-2)

b. Target date for dpANS to NCITS:
   - Original target date: November 1997
   - Previous target date: November 1999
   - Current target date: May 2000

c. Project Description: The FCP-2 standard will define a mapping layer for the execution of SCSI operations as defined by the SCSI-3 Architectural Model, ANSI X3.270-199X on the Fibre Channel - Physical and Signaling Interface as defined by ANSI X3.230-1994. It will maintain a high degree of compatibility with the present FCP standard. Candidates for inclusion in the FCP-2 draft standard include defining an optional response confirmation protocol for certain Fibre Channel Class 3 environments.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: This draft standard is nearing completion.

f. Statement of Status as of This Report: In development.

g. Future Plans: none.

h. Reasons for Delay: Dealing with error handling in an environment where out-of-order delivery may occur took longer than expected.

25. **Serial Storage Architecture - Physical Layer - 1 (SSA-PH1)**

a. Project 1145-D, Serial Storage Architecture - Physical Layer (SSA-PH1)

b. Target date for dpANS to NCITS:
   - Original target date: Maintenance Phase -- 5yr review due in 2001.

   Previous target date: November 1999
   - Current target date: May 2000

c. Project Description: The SSA-PH1 standard will define a physical layer that will support the SSA transport layer and the protocol above it. The goals of SSA-PH1 are: a) minimize gate count; b) copper cable operation at 20MB/sec.; c) full duplex operation to achieve an aggregate 40MB/sec between two ports; d) connectors and cables sized for small form factor devices.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Published.

g. Future Plans: SSA-PH2 (Project 1146-D) was completed. No further SSA work is planned.
26. **Serial Storage Architecture - Physical Layer - 2 (SSA-PH2)**

   a. Project 1146-D, Serial Storage Architecture - Physical Layer (SSA-PH2)

   b. Target date for dpANS to NCITS:
      - Original target date: 
      - Previous target date: 
      - Current target date: Maintenance Phase -- 5yr review due in 2003.

   c. Project Description: The SSA-PH2 standard will define a physical layer that will support the SSA transport layer and the protocol above it. The goals of SSA-PH2 are: a) extend the cable distance; b) copper cable operation at 40MB/sec or greater; c) full duplex operation to achieve an aggregate 80MB/sec between two ports; and d) consider an optical transmission option.

   d. Publications During Past Year: none.

   e. Statement of Progress or Accomplishments During Year: none.

   f. Statement of Status as of This Report: Published.

   g. Future Plans: none.

   h. Reasons for Delay: none.

27. **Serial Storage Architecture - Transport Layer - 2 (SSA-TL2)**

   a. Project 1147-D, Serial Storage Architecture - Transport Layer (SSA-TL2)

   b. Target date for dpANS to NCITS:
      - Original target date: 
      - Previous target date: 
      - Current target date: Maintenance Phase -- 5yr review due in 2003.

   c. Project Description: The SSA-TL2 standard will define a transport layer that uses the SSA physical layer to support the protocol above it. The goals of SSA-TL2 are: a) provide support for an extended distance option in the physical layer; b) provide support for higher data rates in the physical layer; and c) enhance packet formats and addressing methods.

   d. Publications During Past Year: none.

   e. Statement of Progress or Accomplishments During Year: none.

   f. Statement of Status as of This Report: Published.

   g. Future Plans: none.

   h. Reasons for Delay: none.


   a. Project 1155-D, SCSI Serial Bus Protocol 2 (SBP-2)
b. Target date for dpANS to NCITS:
   Original target date:
   Previous target date:
   Current target date: Maintenance Phase -- 5yr review due in 2002.

c. Project Description: The SBP-2 standard will define transport layer protocols to take
   Candidates for inclusion in the SBP-2 draft standard are: a) define a transport protocol that is
   independent of the command set, b) develop functional specifications for SBP-2 high-availability
   factors, possibly in connection with yet to be defined extensions to High Performance Serial Bus
   transport media, c) provide functionality to incorporate the anticipated inclusion of gigabit and
   greater transfer rates by High Performance Serial Bus, d) insure SBP-2 compatibility for operations
   within a group of High Performance Serial Buses connected by bridges, e) provision of facilities to
   take advantage of the isochronous data transfer capabilities of High Performance Serial Bus, and f)
   other capabilities which fit within the general application scope of High Performance Serial Bus that
   may be proposed during the development phase by the participants in the project.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: Project completed.

f. Statement of Status as of This Report: Published.

g. Future Plans: There may be a need for an SBP-3 project; there has been some discussion over
   whether such a project should be proposed for T10 or IEEE.

h. Reasons for Delay: none.

29. **SCSI Architecture Model - 2 (SAM-2)**

   a. Project 1157-D, SCSI Architecture Model - 2 (SAM-2)

   b. Target date for dpANS to NCITS:
      Original target date: November 1997
      Previous target date: November 1999
      Current target date: May 2000

   c. Project Description: The SAM-2 standard will define an abstract layered model specifying
      those common characteristics of an SCSI I/O subsystem that must be exhibited by all SCSI
      protocols and implementations to insure compatibility with device drivers and applications
      regardless of underlying interconnect technology. SAM-2 will maintain a high degree of compatibility
      with the present SAM standard. Candidates for inclusion in the SAM-2 draft standard include
      extensions to support high availability requirements.

   d. Publications During Past Year: none.

   e. Statement of Progress or Accomplishments During Year: Project is nearing completion.

   f. Statement of Status as of This Report: In development.

   g. Future Plans: It is likely that a SAM-3 project will be proposed.

   h. Reasons for Delay: none.
30. **SCSI Enclosure Services (SES)**
   
a. Project 1212-D, SCSI Enclosure Services (SES)
   
b. Target date for dpANS to NCITS:
   
   Original target date:
   
   Previous target date:
   
   Current target date: Maintenance Phase -- 5yr review due in 2003.
   
c. Project Description: The SES standard will define a model for a SCSI Enclosure Services device type. The command set and command set usage will be described. Formats for providing different classes of information will be defined. Formats for providing status and control information for each element and type of element in an enclosure are defined. The SES standard will use commands defined in the SPC standard to transfer these formats. Additional formats are provided for other enclosure related information. If the committee requests and approves appropriate text, the SES standard may include additional enclosure related information, including MIB/MIFs for enclosure information.
   
d. Publications During Past Year: none.
   
e. Statement of Progress or Accomplishments During Year: none.
   
f. Statement of Status as of This Report: Published.
   
g. Future Plans: none.
   
h. Reasons for Delay: none.

31. **SCSI High Availability Profile (HAP)**

   
   b. Target date for dpANTR to NCITS:
   
   Original target date:
   
   Previous target date:
   
   Current target date: Project closed at T10's request.
   
   c. Project Description: Candidates for inclusion in the HAP technical report are: a) An interoperability profile for parallel SCSI components; b) System-level considerations for High Availability subsystems; c) Clarify the SCC description of the use of multi-LUN devices; d) Clarify device identification procedures in the case of multiple access paths; e) Identify characteristics needed for devices intended for use in high availability systems, including hardware, software, microcode, and device models; f) Document the process and procedures used to remove and replace device and host enclosures on a SCSI bus; g) Document the proper usage of "Y" cables, location of bus terminators, etc. as applicable to high availability systems; h) Document methods to avoid bus glitches on power cycles; i) Document when a bus reset may be used, when bus options are renegotiated, how to handle incoming bus resets, and the handling of message and command traffic when a host is running its boot or console code; j) Document when certain data needs to be maintained on a per-LUN or a per-host basis in a device; k) Establish guidelines for hosts in a multi-host environment to coordinate the use of mode pages, bus IDs, bus resets, and reservations; l) Clarification of the use of multiple internal controllers inside a SCSI device to provide internal protection against device failure.
   
   d. Publications During Past Year: none.
e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Project closed.

g. Future Plans: none.

h. Reasons for Delay: none.

32. **SCSI Controller Commands - 2 (SCC-2)**

a. Project 1225-D, SCSI Controller Commands (SCC-2)

b. Target date for dpANS to NCITS:
   - Original target date: 
   - Previous target date: 
   - Current target date: Maintenance Phase -- 5yr review due in 2003.

c. Project Description: The SCSI Controller Commands-2 standard is intended to provide a complete set of commands to complement the SCSI-3 Primary Command Set, and be applicable to devices which act as subsystem controllers, such as a disk array controllers. Functions which will be considered for incorporation include: a) Transfer commands unique to SCC-2 devices; b) Control commands to manage the operation of an SCC-2 device; c) Optional device mapping and pass-through support; d) Other capabilities which fit within the general scope of implementing the SCSI Controller Commands-2 on a broad range of applications, and other capabilities that may be proposed during the development phase by the participants in the project.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: Project completed.

f. Statement of Status as of This Report: Published.

g. Future Plans: none.

h. Reasons for Delay: none.

33. **Multi-Media Commands - 2 (MMC-2)**

a. Project 1228-D, Multi-Media Commands - 2 (MMC-2)

b. Target date for dpANS to NCITS:
   - Original target date: March 1998
   - Previous target date: November 1999
   - Current target date: July 1999

c. Project Description: The SCSI Multi-Media Commands-2 standard is intended to provide additional commands to existing Multi-Media Command Set, and be applicable to new devices being developed. Functions which will be considered for incorporation include: a) New Format commands unique to DVD, PD devices; b) Other capabilities which fit within the general scope of implementing the SCSI Multi-Media Commands-2 on a broad range of applications, and other capabilities that may be proposed during the development phase by the participants in the project.

d. Publications During Past Year: none.
Statement of Progress or Accomplishments During Year: Development completed; in approval phase.

Statement of Status as of This Report: At first public review (closes 24 Oct 1999).

Future Plans: An MMC-3 (Project 1363-D) has been started.

Reasons for Delay: none.

34. **SCSI Primary Commands - 2 (SPC-2)**

- Project 1236-D, SCSI Primary Commands - 2 (SPC-2)

- Target date for dpANS to NCITS:
  - Original target date: July 1998
  - Previous target date: November 1999
  - Current target date: May 2000

- Project Description: The SCSI Primary Commands-2 standard is intended to include additional commands as well as existing SCSI-3 Primary Commands, and be applicable to both existing and new SCSI device types being developed. The participants in the project may decide to move some information in SCSI-3 Primary Commands to another standard or to make some information in SCSI-3 Primary Commands obsolete in SCSI Primary Commands-2. In addition to the information currently in SCSI-3 Primary Commands, information that will be considered for incorporation include: a) New additional sense code values; b) New mode page definitions or new fields in existing mode pages; c) New fields in the parameter data returned by the INQUIRY and REQUEST SENSE commands; d) New vital product data pages; e) New commands appropriate for all SCSI device types; f) Changes to the processor device type model; and g) Other capabilities that fit within the general scope of implementing the SPC-2 on a broad range of applications, and other capabilities that may be proposed during the development phase by the participants in the project.

- Publications During Past Year: none.

- Statement of Progress or Accomplishments During Year: Project nearing completion.

- Statement of Status as of This Report: In development.

- Future Plans: An SPC-3 project will probably be proposed in order to forward SPC-2.

- Reasons for Delay: It seems necessary to keep an SPC-n project open as long as other command sets projects are open.

35. **Reduced Block Commands (RBC)**

- Project 1240-D, Reduced Block Commands (RBC)

- Target date for dpANS to NCITS:
  - Original target date: May 1999
  - Previous target date: March 1999

- Project Description: This standard will define for hard disk drive and removable disk drive devices: the commands to be utilized; the device operation; the subset of the SBP-2 protocol to be utilized; the security requirements on 1394; the configuration ROM and CSR requirements on 1394.
Publications During Past Year: none.

Statement of Progress or Accomplishments During Year: Development phase completed and approval phase nearly complete.

Statement of Status as of This Report: At second public review (closes 24 Oct 1999).

Future Plans: none.

Reasons for Delay: RBC was forwarded ahead of schedule.

**36. SCSI Socket/SSL Services Command Set (SSS)**

- Project 1246-D, SCSI Socket/SSL Services Command Set (SSS)
- Target date for dpANS to NCITS:
  - Original target date:  
  - Previous target date:  
  - Current target date: Project closed at T10's request.
- Project Description: The SSS standard will: a) define a platform and device independent method of communication between processor devices and communication devices or other devices; b) be optimized for platform/device independence; c) provide other capabilities which fit within the general application scope of the SCSI Socket/SSL Service that may be proposed during the development phase by the participants in the project.
- Publications During Past Year: none.
- Statement of Progress or Accomplishments During Year: none.
- Statement of Status as of This Report: Project closed.
- Future Plans: none.
- Reasons for Delay: none.

**37. SCSI Parallel Interface - 3 (SPI-3)**

- Project 1302-D, SCSI Parallel Interface - 3 (SPI-3)
- Target date for dpANS to NCITS:
  - Original target date: May 1999
  - Previous target date: May 1999
  - Current target date: January 2000
- Project Description: The SPI-3, based on low-voltage differential (LVD) technology, will allow a 32-bit dual-channel host adapter to attain greater than 5 Gbits per second data rate, doubling the data rate of current technology. This will permit peripherals to meet the data I/O needs of the next generation 64-bit processors being delivered by the end of the millennium. In addition to improving the fundamental data rate to 80 mega-transfers per second, SPI-3 will consider reducing the overhead of parallel SCSI with the adoption of protocol enhancements allowing a host adapter achievement of greater than 100,000 I/O's per second. The enhancements may include an error detection scheme along with packetization to increase the data integrity and provide unrestricted hot plugging for parallel SCSI. The SPI-3 project will consider the advancing developments in silicon technology related to power management and voltage-reduction.
d. Publications During Past Year: none.
e. Statement of Progress or Accomplishments During Year: Development phase nearly complete.
f. Statement of Status as of This Report: In development.
g. Future Plans: SPI-4 (Project 1365-D) has been started.
h. Reasons for Delay: none.

38. SCSI Enclosure Profile (SEP) Technical Report

a. Project 1303-D, SCSI Enclosure Profile (SEP)
b. Target date for dpANS to NCITS:
   Original target date: May 1999
   Previous target date: 
   Current target date: Project closed at T10's request.

c. Project Description: With the near completion of SES (SCSI Enclosure Services) and SCC-2 (SCSI Controller Commands -2) standards, product developers need guidance for cost-effective engineering of RAID (Redundant Arrays of Independent Disks) products based on these new standards. It is important that both host software and RAID controller firmware be developed at minimum time and cost so that initial industry acceptance of these new standards can be achieved. A usage profile describing a minimum usage definition based on the standards and agreed to using the NCITS consensus process is proposed to meet these needs.

d. Publications During Past Year: none.
e. Statement of Progress or Accomplishments During Year: none.
f. Statement of Status as of This Report: Project closed.
g. Future Plans: none.
h. Reasons for Delay: none.

39. Multi-Media Commands - 3 (MMC-3)

a. Project 1363-D, Multi-Media Commands - 3 (MMC-3)

b. Target date for dpANS to NCITS:
   Original target date: November 2000
   Previous target date: November 2000
   Current target date: November 2000

c. Project Description: The MultiMedia Command set version 3 is based on MultiMedia Command set version 2 that provides for commands to implement CD-R, CD-RW, DVD-ROM, DVD-R, DVD+RW DVD-RAM, DVD-RW, AS-MO, and earlier devices. This command set may be implemented on multiple interfaces such as SCSI, ATA/ATAPI, SBP-2 (1394), and FC-P. The following items should be considered for inclusion in MMC-3: 1) options for improving operation with serial interconnects; 2) extensions to DVD product commands; 3) extensions to CD product commands; 4) other capabilities that may fit within the general application scope of this project.

d. Publications During Past Year: none.
40. Reduced MultiMedia Command Set (RMC)
   a. Project 1364-D, Reduced MultiMedia Command Set (RMC)
   b. Target date for dpANS to NCITS:
      Original target date: November 2000
      Previous target date: November 2000
      Current target date: November 2000
   c. Project Description: The Reduced MultiMedia command set is based on MultiMedia Command Set – 2 (MMC-2), and the Reduced Block Command (RBC) Set for SBP-2. It will provide for a reduced number of commands that will ensure all functions of the CD/DVD will be implemented. The following items should be considered for inclusion in RMC: 1) extensions for minimal command implementation; 2) ensure that the latest CD/DVD products have valid commands; 3) other capabilities that may fit within the general application scope of the this project.
   d. Publications During Past Year: none.
   e. Statement of Progress or Accomplishments During Year: Development phase just started.
   f. Statement of Status as of This Report: In development.
   g. Future Plans: none.
   h. Reasons for Delay: none.

41. SCSI Parallel Interface - 4 (SPI-4)
   a. Project 1365-D, SCSI Parallel Interface - 4 (SPI-4)
   b. Target date for dpANS to NCITS:
      Original target date: November 2000
      Previous target date: November 2000
      Current target date: November 2000
   c. Project Description: The SCSI Parallel Interface - 4 (SPI-4), is based on low-voltage differential (LVD) technology and is designed to provide a 320 MB/sec data rate and lay the groundwork for the next data rate, 640 MB/sec. In addition to doubling the existing data rate of SPI-3, the following items may be considered for inclusion in SPI-4: 1) extended addressing for multi-segment domains; 2) options for expander design; 3) options for improving operation with host interconnects; 4) extensions to domain validation; 5) skew management schemes; 6) improvements for physical layer signal integrity; 7) power management; 8) voltage reduction options; 9) other capabilities that may fit within the general application scope of the this project.
   d. Publications During Past Year: none.
   e. Statement of Progress or Accomplishments During Year: Development phase just started.
g. Statement of Status as of This Report: In development.

h. Future Plans: It seems likely that a SPI-5 will be proposed.

i. Reasons for Delay: none.

42. **SCSI Domain Validation Technical Report (SDV)**
   
a. Project _____-D, SCSI Domain Validation Technical Report (SDV)

b. Target date for dpANS to NCITS:
   - Original target date: November 2001
   - Previous target date: November 2001
   - Current target date: November 2001

c. Project Description: The SCSI Domain Validation (SDV), is an application of testing techniques to validate the communication capability of the SCSI physical layer. Some of the techniques require cooperation between host and target devices. The following items should be considered for inclusion in SDV: 1) definition of domain validation levels; 2) interoperability parameters for techniques; 3) communication methods for expanders; 4) other capabilities that may fit within the general application scope of the this project.

d. Publications During Past Year: none.

e. Statement of Progress or Accomplishments During Year: none.

f. Statement of Status as of This Report: Proposed project.

g. Future Plans: none.

h. Reasons for Delay: none.

III. **Committee Activities**

a. Previous Year's Meetings:
   - September 17, 1998; St. Petersburg Beach, FL
   - November 5, 1998; Palm Springs, CA
   - March 11, 1999; Harrisburg, PA
   - May 6, 1999; Manchester, NH
   - July 15, 1999; Colorado Springs, CO
   - September 16, 1999; Huntington Beach, CA

b. Current Year's Planned Meetings T10:
   - November 4, 1999; Monterey, CA
   - January 13, 2000; Brisbane, Australia
   - March 9, 2000; Dallas, TX
   - May 18, 2000; Nashua, NH
   - July 13, 2000; Colorado Springs, CO
   - September 14, 2000, Huntington Beach, CA
   - November 2, 2000, Monterey, CA
c. Officers: T10
   Chair: John B. Lohmeyer
   Vicechair: George O. Penokie
   Secretary: Ralph O. Weber

d. Membership: The current T10 membership list is attached.
f. Administrative Matters of Note: none.
g. Procedural Matters of Note: none.
h. Recommendations: none.

IV. Anticipated Projects

It is anticipated that one or more projects will be needed for next-generation versions of current T10 projects as these projects near completion.

V. Future Trends in this Technical Area

The physical layer of parallel SCSI continues to take advantage of advances in silicon technology boosting transfer rates and connectivity. SCSI expanders and switches have recently emerged as methods to enhance SCSI connectivity. The trend to smaller connectors to accommodate wider data paths and smaller devices also impacts the physical layer.

The protocol layers for SCSI command sets continue to expand to encompass new physical layers being developed in other organizations. In addition, advances in parallel protocol are expected to occur in order to reduce overhead.

The SCSI command sets are evolving to encompass the latest developments in storage (e.g., digital video disks, CD recordable devices, and array controllers) as well as refinements to deal with current issues (e.g., power management and security).

Attachment 1: Committee Projects: SD-4 Data

The NCITS Secretariat provided the following data:

<table>
<thead>
<tr>
<th>T10/SC25/WG4</th>
<th>Lower Level Interface</th>
</tr>
</thead>
<tbody>
<tr>
<td>(Revised 08/19/99 )NCITS Project:</td>
<td>52 - M</td>
</tr>
</tbody>
</table>

Standard Designation: A/I 9315:1989  [1994] **Title:** Interfaces between flexible disk cartridges drives and their host controllers

**Related International Development**

ISO/IEC Doc.: IS 9315:1989 JTC 1 Project: 25.13.10.01

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NCITS Project: 53 - M

Standard Designation: X3.91M:1987  [R1997] **Title:** Storage Module Interfaces (SMD-E)

**Related International Development**

ISO/IEC Doc.: IS 9324 JTC 1 Project: 25.13.10.03

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NCITS Project: 375 - M

Standard Designation: X3.131:1994  [] **Title:** Small Computer System Interface - 2 (SCSI-2)
Related International Development


Technical Information Bulletin 1

Related International Development

ISO/IEC Doc.: N/A JTC 1 Project: __

Technical Information Bulletin 2

Related International Development

ISO/IEC Doc.: N/A JTC 1 Project: __

-NCITS Project: 792 - M

Related International Development

ISO/IEC Doc.: DIS 15842 JTC 1 Project: 25.13.11.02

NCITS Project: 855 - D

Related International Development

ISO/IEC Doc.: JTC 1 Project: __

Standard Designation: X3.253:1995  Title: SCSI-3 Parallel Interface (SPI)

Related International Development

ISO/IEC Doc.: JTC1 N 3913 JTC 1 Project: 25.13.11.05

-NCITS Project: 856 - D
Standard Designation: X3.292:1997  Title: SCSI-3 Interlocked Protocol (SIP)

Related International Development

ISO/IEC Doc.: CD 14766-211 JTC 1 Project: 25.13.11.04

NCITS Project: 989 - D
Standard Designation: X3.295:1996  Title: Serial Storage Architecture - Transport Layer (SSA-TP1)

Related International Development

ISO/IEC Doc.: N/A JTC 1 Project: N/A

-NCITS Project: 990 - D
Standard Designation:  Title: Common Access Method-3 (CAM-3)

Related International Development

ISO/IEC Doc.: N/A JTC 1 Project: N/A

-NCITS Project: 991 - DT

Related International Development
ISO/IEC Doc.: N/A JTC 1 Project: 25.13.11.06

NCITS Project: 993 - M
Related International Development
ISO/IEC Doc.: JTC1 N 3917 JTC 1 Project: 25.13.13.02

NCITS Project: 994 - M
Standard Designation: X3.270:1996  [ ] Title: SCSI-3 Architecture Model (SAM)  
Related International Development
ISO/IEC Doc.: JTC1 N 3929 JTC 1 Project: N/A

NCITS Project: 995 - D
Standard Designation: X3.301:1997  [ ] Title: SCSI-3 Primary Commands (SPC)  
Related International Development
ISO/IEC Doc.: N/A JTC 1 Project: N/A

NCITS Project: 996 - D
Standard Designation: NCITS 306:1998  [ ] Title: SCSI-3 Block Commands (SBC)  
Related International Development
ISO/IEC Doc.: CD 14766-321 JTC 1 Project: N/A

NCITS Project: 997 - D
Standard Designation:  [ ] Title: SCSI-3 Stream Commands (SSC)  
Related International Development
ISO/IEC Doc.: N/A JTC 1 Project: N/A

NCITS Project: 999 - D
Standard Designation: NCITS 314:1998  [ ] Title: SCSI-3 Medium Changer Commands (SMC)  
Related International Development
ISO/IEC Doc.: CD 14776-351 JTC 1 Project: N/A

NCITS Project: 1047 - D
Standard Designation: X3.276:1997  [ ] Title: SCSI-3 Controller Commands (SCC)  
Related International Development
ISO/IEC Doc.: JTC1 N 3916 JTC 1 Project: N/A

NCITS Project: 1048 - D
Related International Development
ISO/IEC Doc.: CD 14766-361 JTC 1 Project: N/A

NCITS Project: 1051 - D
Related International Development
ISO/IEC Doc.: N/A JTC 1 Project: N/A
NCITS Project: 10/1 - M
Standard Designation: X3.277:1996   [] Title: SCSI-3 Fast-20 Parallel Interface (Fast-20)
Related International Development
ISO/IEC Doc.: JTC1 N 3915 JTC 1 Project: N/A

-NCITS Project: 1121 - D
Related International Development
ISO/IEC Doc.: N/A JTC 1 Project: N/A

-NCITS Project: 1142 - D
Standard Designation: X3.302:1998   [] Title: SCSI-3 Parallel Interface - 2 (SPI-2)
Related International Development
ISO/IEC Doc.: CD 14776-112 JTC 1 Project:

NCITS Project: 1143 - TR
Related International Development
ISO/IEC Doc.: JTC 1 Project:

-NCITS Project: 1144 - D
- Standard Designation:   [] Title: SCSI Fibre Channel Protocol - 2 (FCP-2)
Related International Development
ISO/IEC Doc.: JTC 1 Project:

-NCITS Project: 1145 - D
Standard Designation: X3.293:1996   [] Title: Serial Storage Architecture - Physical Layer 1 (SSA-PH1)
Related International Development
ISO/IEC Doc.: JTC 1 Project:

NCITS Project: 1146 - D
Standard Designation: NCITS 307:1997   [] Title: Serial Storage Architecture - Physical Layer 2 (SSA-PH2)
Related International Development
ISO/IEC Doc.: JTC 1 Project:

-NCITS Project: 1147 - D
- Standard Designation: NCITS 308:1997   [] Title: Serial Storage Architecture - Transport Layer 2 (SSA-TL2)
Related International Development
ISO/IEC Doc.: JTC 1 Project:

-NCITS Project: 1155 - D
Standard Designation: NCITS 325:1998   [] Title: SCSI-3 Serial Bus Protocol 2 (SBP-2)
Related International Development

ISO/IEC Doc.: JTC 1 Project:

NCITS Project: 1157 - D
Standard Designation: :   [] Title: SCSI Architecture Model-2 (SAM-2)

Related International Development

ISO/IEC Doc.: JTC 1 Project:

NCITS Project: 1212 - D
Standard Designation: NCITS 305:1998   [] Title: SCSI Enclosure Services (SES)

Related International Development

ISO/IEC Doc.: CD 14776-371 JTC 1 Project:

NCITS Project: 1225 - D

Related International Development

ISO/IEC Doc.: CD 14776-342 JTC 1 Project:

NCITS Project: 1228 - D
Standard Designation: NCITS 333:   [] Title: SCSI Multi-Media Commands - 2 (MMC-2)

Related International Development

ISO/IEC Doc.: JTC 1 Project:

NCITS Project: 1236 - D
Standard Designation: :   [] Title: SCSI Primary Commands - 2 (SPC-2)

Related International Development

ISO/IEC Doc.: CD 14766-311 JTC 1 Project:

NCITS Project: 1240 - D
Standard Designation: NCITS 330:   [] Title: Reduced Block Commands (RBC)

Related International Development

ISO/IEC Doc.: JTC 1 Project:

NCITS Project: 1302 - D
Standard Designation: :   [] Title: SCSI Parallel Interface-3 (SPI-3)

Related International Development

ISO/IEC Doc.: JTC 1 Project:

NCITS Project: 1303 - DT
Standard Designation: :   [] Title: SCSI Enclosure Profile

Related International Development

ISO/IEC Doc.: JTC 1 Project:

NCITS Project: 1363 - D
Standard Designation: :   [] Title: MultiMedia Command Set-3 (MMC-3)

Related International Development

ISO/IEC Doc.: JTC 1 Project:
<table>
<thead>
<tr>
<th>Project</th>
<th>Standard Designation</th>
<th>Title</th>
<th>Related International Development</th>
</tr>
</thead>
<tbody>
<tr>
<td>1364 - D</td>
<td>[]</td>
<td>Reduced MultiMedia Command Set (RMC)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ISO/IEC Doc.: JTC 1 Project:</td>
<td></td>
</tr>
<tr>
<td>1365 - D</td>
<td>[]</td>
<td>SCSI Parallel Interface-4 (SPI-4)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ISO/IEC Doc.: JTC 1 Project:</td>
<td></td>
</tr>
</tbody>
</table>
Attachment 2: Internal Procedures

The T10 internal procedures are as follows:


The above procedures have been approved by PPC. T10 is in the process of revising the T10 Electronic Procedures to reflect the new T10 FTP and web site (ftp.t10.org and www.t10.org). The revised procedure will likely be forwarded for approval in November 1999.
Attachment 3: T10 Current Membership List

This file contains the T10 attendance database in ASCII format. It is not the "official" T10 membership list that is maintained by the NCITS Secretariat. The NCITS list includes people who have never attended a T10 meeting, but have paid their fee(s) and are in "good standing". This list is used for keeping track of attendance since there are minimum attendance requirements to maintain voting rights.

The code in parentheses following people's names indicates their membership status:

- P  - the Principal member for an organization
- A  - the first Alternate member for an organization
- A# - an additional Alternate member for an organization
- O  - an Observer member of T10
- L  - a Liaison member of T10 (usually a member of another standards group)
- XO - Ex Officio member of T10 (several NCITS officers are Ex Officio members of T10)

(This report was generated 1999/09/14 at 09:56:59.)

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