

Document number: 99-226r2.pdf
Date: September 24,1999
To: T10/T11 Committees
From: Jim Coomes, Seagate Technology
Subject: Mode Page 19h DSA bit Clarification

Based on e-mail input on the rev 1 (99-226r1.pdf) update of the Disable Soft Address function in the FC Control mode page, the definition is updated below.

Summary of changes:

1st sentence: 'shall attempt' to 'shall only attempt'
1st sentence: 'the hard address' to 'its hard address'
2nd sentence: added to clarify no soft address

New Text:

9.1.3.4 Require Hard Address (RHA)

Targets not attached to an FC-AL loop shall ignore this bit.

A Require Hard Address (RHA) bit of one indicates that a target attached to an FC-AL loop shall only attempt to obtain its hard address available in the SCA-2 SFF- 8067 connector or device address jumpers during loop initialization. The target shall not attempt to obtain an address during the LISA phase of initialization. If there is a conflict for the hard address selection during loop initialization or the target does not have a valid hard address available, the target shall enter the nonparticipating state. If the target detects loop initialization while in the nonparticipating state, the target shall again attempt to get its hard address. If the hard address has not changed from the address obtained in a previous successful loop initialization, the target shall attempt to obtain the address in the LIFA phase if a valid Fabric login exists or LIPA phase of loop initialization. If the hard address has changed, the target shall attempt to obtain the new address in the LIHA phase. When the RHA bit is zero, the target follows the normal initialization procedure, including the possibility of obtaining a soft address during the loop initialization process.

Additionally:

LIFA, LIPA, LIHA, and LISA are added to the abbreviation section.