

Document number: 99-226r0.pdf  
Date: August 4,1999  
To: T10/T11 Committees  
From: Jim Coomes, Seagate Technology  
Subj: Mode Page 19h DSA bit Clarification

This proposal clarifies the definition of the Disable Soft Address bit in the Fibre Channel Control Mode page (19h). This clarification is to support the customer requested and implemented behavior for this function. The clarification limits the target to only select an address in the hard address phase of loop initialization.

**New Text:**

**9.1.3.4 Disable Soft Address (DSA)**

Targets not attached to an FC-AL loop shall ignore this bit.

A Disable Soft Address (DSA) bit of one indicates that a target attached to an FC-AL loop shall not select an address in the LIFA, LIPA, or LISA phases of loop initialization. If there is a conflict for the hard address selection during loop initialization, the target shall enter the nonparticipating state. If the target detects loop initialization while in the nonparticipating state, the target will again attempt to get its hard address. When the DSA bit is zero, the target follows the normal initialization procedure, including the possibility of obtaining a soft address during the loop initialization process.

**Additionally:**

LIFA, LIPA, and LISA are added to the abbreviation section.