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To: T10 Technical Committee
From: John Lohmeyer, LSI Logic Principal Member of T10
Subj: Proposal to change MSE high-impedance current specification in SPI-3

The July '99 SPI-3 working group reviewed 99-187r1 and requested that I simplify the proposal to just not specify leakage currents above 3.01 volts. This avoids the need for the figure. The proposal now is to make the following changes to SPI-3 rev 7:

Add a second sentence to the paragraph immediately above Table 15:

In addition to the device electrical requirements defined in the remaining subclauses of this clause [SCSI](#) devices shall meet the requirements specified in table 15 and table 16 at the device connector. [The requirements in table 16 shall apply to both powered and unpowered devices.](#)

Modify table 16 as shown:

Table 16 - Input current requirements at the device connector for lines not being driven by the device

Value	Maximum	Notes
MSE current magnitude	$\pm 20 \mu\text{A D.C.}$	Measured from + or - signal $0 < V_{\text{IN}} < 3,01 \text{ V}$ to local ground for each signal pin.
LVD current magnitude	$\pm 20 \mu\text{A D.C.}$	Measured from + or - signal $0 \leq V_{\text{IN}} < 2,5 \text{ V}$ to local ground for each signal pin.

Note: I added the "0 <" to the second row above. It appears that this was inadvertently left out.