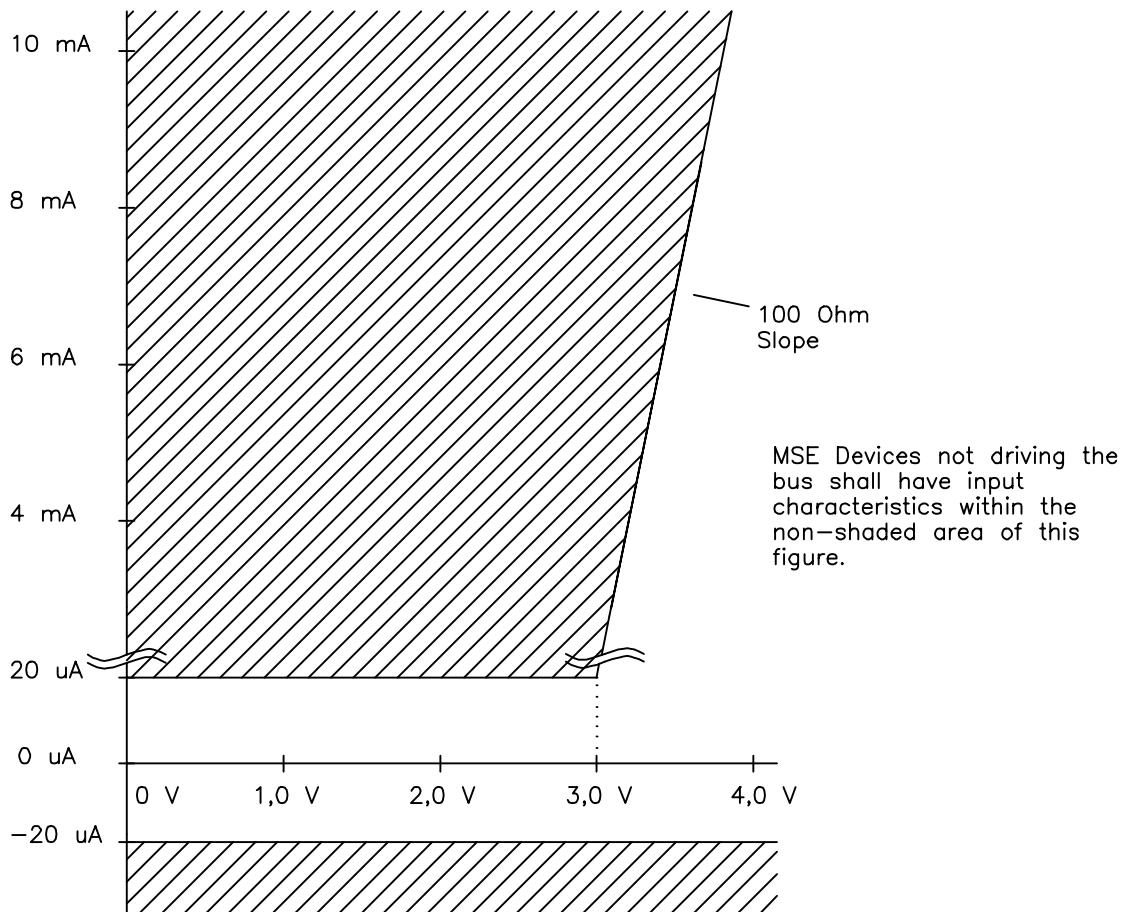LSI LOGIC®

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May 10, 1999

To: T10 Technical Committee
From: John Lohmeyer, LSI Logic Principal Member of T10
Subj: Proposal to change MSE High-Z Current Specification in SPI-3

The May '99 SPI-3 working group requested that I develop a written proposal regarding relaxing the High Z input characteristics of MSE devices (i.e., their input characteristics when they are not driving the bus). I propose that the below figure replace the first line of Table 16 (SPI-3, rev 5) as the definition of MSE input characteristics when not driving the SCSI bus.

Annex A, Table A.1: Change the device leakage line as follows: Minimum: -20 (no change); Maximum: see figure <new>; Cross-reference: figure <new>.

Re-word the first paragraph in A.4.1 as follows:

"The requirements in this clause apply as a test method to ensure compliance with table 15, table 16, and figure <new>. With the transceiver in an off condition (i.e., not transmitting) and the + and - signals connected to a variable voltage source, $V_{applied}$, the output high-Z currents I_{I-L} and I_{I+L} shall not exceed the applicable values in table 16 and figure <new> over the range $0,00 V \leq V_{applied} \leq 3,6 V$ (see figure A.11)."