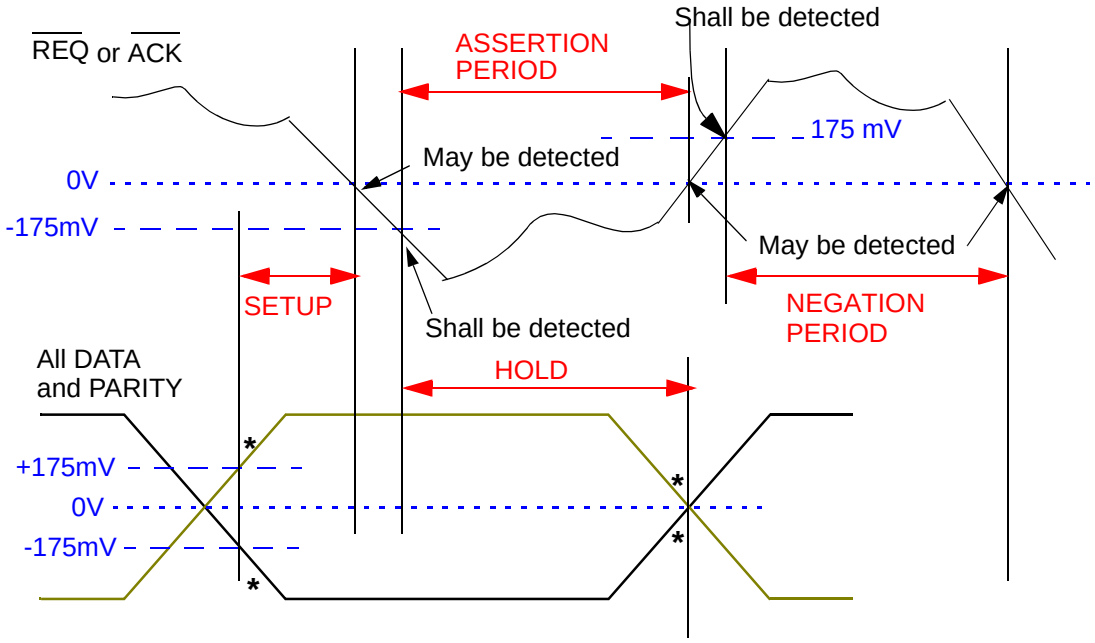

* Use the crossing that yields the shorter SETUP and HOLD time

Differential voltage signals in all cases

Notes:
1 Negative going signals shall maintain negative slope after crossing the 0 mV level and before crossing the -175 mV level and should be linear within 20% in this range.
2 Positive going signals shall maintain positive slope after crossing the 0 mV level and before crossing the 175 mV level and should be linear within 20% in this range.
3. Signals shall not cross the 0 mV level without completing the transition to the + or - 175 mV level
4. Signal properties are not defined except between 0 to -175 mV for negative going signals and between 0 and 175 mV for positive going signals
5. Data and parity signals are shown idealized for convenience but may have structure similar to that shown for the REQ and ACK signal

Figure 1 - LVD timing measurement points

Figure 1 illustrates that receivers shall switch detected signal states when the magnitude of the signal reaches 175mV (after crossing 0 immediately preceding) regardless of the intensity of assertion or negation that preceded the transition. Signals between the transition regions shall not exceed the maximum level allowed for input to the receivers and may approach the 0 level as long as they do not cross. There are no other requirements on the signals between the transition regions.