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To: T10 Technical Committee
From: Bill Galloway
Subj: Timing for the SCSI RST line

The RST line is a wired-or signal and is only passively negated. In LVD mode, this negation is weak. Systems in the lab have been observed to reset due to cross talk on the RST line. The Reset Hold Time is 25us and up to a Bus Clear Delay is allowed for filtering the RST line. A minimum filter time of 200ns should be added to SPI-3 to ensure robust operation.

Add to Table 30:

"Reset Delay" of 200ns

Add paragraph:

9.2.x Reset delay time
The minimum time that the RST signal must be continuously asserted before the SCSI device shall reset.

Change paragraph:

NOTE 33 - Environmental conditions (e.g. static discharge) may generate brief glitches on the RST signal. ~~It is recommended that SCSI devices not react to these glitches~~. <u>SCSI devices shall not react to glitches less than a Reset Delay time.</u> The manner of rejecting glitches is vendor-specific. The bus clear delay following a RST signal transition to true is measured from the original transition of the RST signal, not from the time that the signal has been confirmed. This limits the time to confirm the RST signal to a maximum of a bus clear delay.