BREA Technologies, Inc.
14902 Mesita Drive
Houston, TX 77083-3209
P: (281) 530-3063
F: (281) 988-0358
BillG@breatech.com

January 22, 1999

To: T10 Technical Committee
From: Bill Galloway
Subj: LVD Receiver offset voltage

Figures 44 and 45 are not consistent with table A.5 in normative Annex A. Table A.5 states that LVD receivers must have no more than +/- 30mv of DC offset. Figures 44 and 45 allow LVD receivers up to +/- 60mv of AC offset. The AC offset should be less than or equal to the DC offset (not more than).

9.2.3 LVD (ST data transfers)
Change one occurrence in second paragraph of 60mv to 30mv.

Figure 44 - LVD timing measurement points for ST data transfers
Change all occurrences of 60mv to 30mv.

9.2.4 LVD (DT data transfers)
Change one occurrence in second paragraph of 60mv to 30mv.

Figure 45 - LVD timing measurement points for DT data transfers
Change all occurrences of 60mv to 30mv.