T10/98-246r1

To: T10 Technical Committee
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Date: 8 February 1999
Subject: "May not"/"Cannot" clarifications in SPI-3

NOTE: Items 1, 2, and 3 were accepted by the January 1999 SPI-3 Working Group. Item 4 was found during a SPI-3 editing session after that meeting.

There are a few "may not" phrases in SPI-3 (carried over from SPI-2) that should probably be changed to "shall not" phrases in SPI-3. It might be helpful to add "may not" to the glossary to clarify its meaning.

1. Change "may not" to "shall not":
Section: SPI-2 11.5.1.1, SPI-3 12.7.1.1 Message protocol rules
"One or more messages may be sent during a single MESSAGE phase, but a message may not be split between multiple MESSAGE phases."
[accepted by January 1999 SPI-3 Working Group]

2. Change "may not" to "shall not":
Section: SPI-2 11.5.2.3, SPI-3 12.7.2.3 IDENTIFY
"Thus an initiator may change the DISCPRIV bit, but may not attempt to switch to another task."
[accepted by January 1999 SPI-3 Working Group]

3. Add a definition of "may not" to the glossary keywords section.

3.3.4 may: A keyword that indicates flexibility of choice with no implied preference (equivalent to "may or may not").
3.3.x may not: A phrase that indicates flexibility of choice with no implied preference (equivalent to "may or may not").
[accepted by January 1999 SPI-3 Working Group]

4. Change "cannot" to "shall not" near the end of section D.1.1 (SCA-2 pinout annex):
      "c) The logic level grounds, GROUND (5V/3,3V) are shared between the currents provided by the 5 V D.C. and 3,3 V D.C. signals. The sum of the 5 V D.C. and 3,3 V D.C. currents cannot exceed 3 Amps."