

Date: July 15, 1998

To: T10 Committee (SCSI)

From: George Penokie (IBM)

Subject: Changes to SPI-2 Rev 20a

Made all the page numbers so they are on the outside of the page rather than the inside.

Changed "holder's" to "holders" in the patent statement.

In figure 27 the lower right S was labeled S_1 when it should have been S_2 . This was correct in rev 11 and has been corrected in rev 20b..

6 (pg 47) "At lease" should be "At least"

Table 20 (pg 54) extra space before period - "ground ."

Table 22 17,25 V not 17.25 V

Table 36 (pg 80) note 2 repeated twice

1 (pg 3) missing space after 2 in "Layer 2[NCITC"

1 (pg 3) extra comma ", is referred to herein"

1 (pg 3) unnecessary capitalization in Approved ": Approved ANSI"

1 (pg 3) missing period after last word SCSI-2

2.2 (pg 4) missing comma before East Englewood

2.3 (pg 4) CA should be capitalized

2.3 (pg 4) replace "from a FAX access at xxx" with "xxx (fax)" like in 2.2

3.4 (pg 10) missing space after comma "(comma(e.g.)"

11.1.3 and 11.1.5.1 "and the appropriate parity bit" should be "bits"

11.1.5.2 reference to 6.6.21 is incorrect - such a section doesn't exist

11.1.5.2 The sentence is broken: "For successful completion of the data phase is that the number..." changed to "For successful completion of the data phase the number..."

11.5.1.1 the quote "'Negate ATN Before Dropping ACK.'" should be "'Negate ATN before last ACK.'" to match the actual column headers.

11.1.5.2 Change the following paragraphs as indicated below:

The target shall assert the REQx signal for a minimum of an transmit assertion period. The target shall then wait at least the greater of a transfer period from the last transition of the REQx signal to true or a minimum of a transmit negation period from the last transition of the REQx signal to false before again asserting the REQx signal.

The initiator shall assert the ACKx signal for each REQx assertion received. The ACKx signal may be

asserted as soon as the corresponding REQx assertion has been received. The initiator shall assert the ACKx signal for a minimum of an transmit assertion period. The initiator shall wait at least the greater of a transfer period from the last transition of the ACKx signal to true or for a minimum of a transmit negation period from the last transition of the ACKx signal to false before asserting the ACKx signal.

If the I/O signal is true (transfer to the initiator), the target shall first drive the DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,P3) signals to their desired values, wait at least one transmit setup time ~~system deskew delay plus one cable skew~~, then assert the REQx signal. The DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,P3) signals shall be held valid for a minimum of ~~one system deskew delay plus one cable skew~~ plus one transmit hold time after the assertion of the REQx signal. The target shall assert the REQx signal for a minimum of an assertion period. The target may then negate the REQx signal and change or release the DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,P3) signals. The initiator shall read the value on the DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,P3) signals within one receive hold time of the transition of the REQx signal to true. The initiator shall then respond with an ACKx assertion.

If the I/O signal is false (transfer to the target), the initiator shall transfer one byte for each REQx assertion received. After detecting a REQx assertion, the initiator shall first drive the DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,P3) signals to their desired values, delay at least one transmit setup time ~~system deskew delay plus one cable skew~~, then assert the ACKx signal. The initiator shall hold the DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,hP3) signals valid for at least one ~~system deskew delay plus one cable skew~~ plus one transmit hold time after the assertion of the ACKx signal. The initiator shall assert the ACKx signal for a minimum of an transmit assertion period. The initiator may then negate the ACKx signal and may change or release the DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,P3) signals. The target shall read the value of the DB(7-0,P), DB(15-0,P,P1), or DB(31-0,P,P1,P2,P3) signals within one receive hold time of the transition of the ACKx signal to true.