To: T10 Membership
Subject: QAS timing
Date: 4/4/98

If B wins (best case timing).

Time	A Action	Time	B Action	Time	C Action
0	Release IO,Msg,CD	0	Sees IO,Msg, CD Released	0	
		90	May assert ID	400	Sees IO,Msg, CD Released
		200	Must assert ID	490	May assert ID
				600	Must Assert ID
1000	IDs Stable across bus	1000	IDs stable across bus	1000	IDs stable across bus
1000	Sees SEL asserted	1000	Win, assert SEL		
1200	Deassert BSY 200 nsec after seeing SEL			1400	Lose, monitor SEL & BSY
1400	SEL stable across bus	1400	SEL stable across bus	1400	SEL stable across bus
				1500	May deassert ID
				1600	Must have IDs deasserted
2000	IDs stable across bus	2000	IDs stable across bus	2000	IDs stable across bus
		2000	Start selection phase		

The above configuration assumes device A and B are at one end of the bus and device C is at the other end.