To: T10 Membership

Subject: 1st Pulse Measurements

Date: 4/17/97

Board #5, Asymmetrical Va & Vn, 431mV & 540mV (Fig. 27 edge point)

Board #6, Asymmetrical Va & Vn, 630mV & 700mV (Not in shaded area)

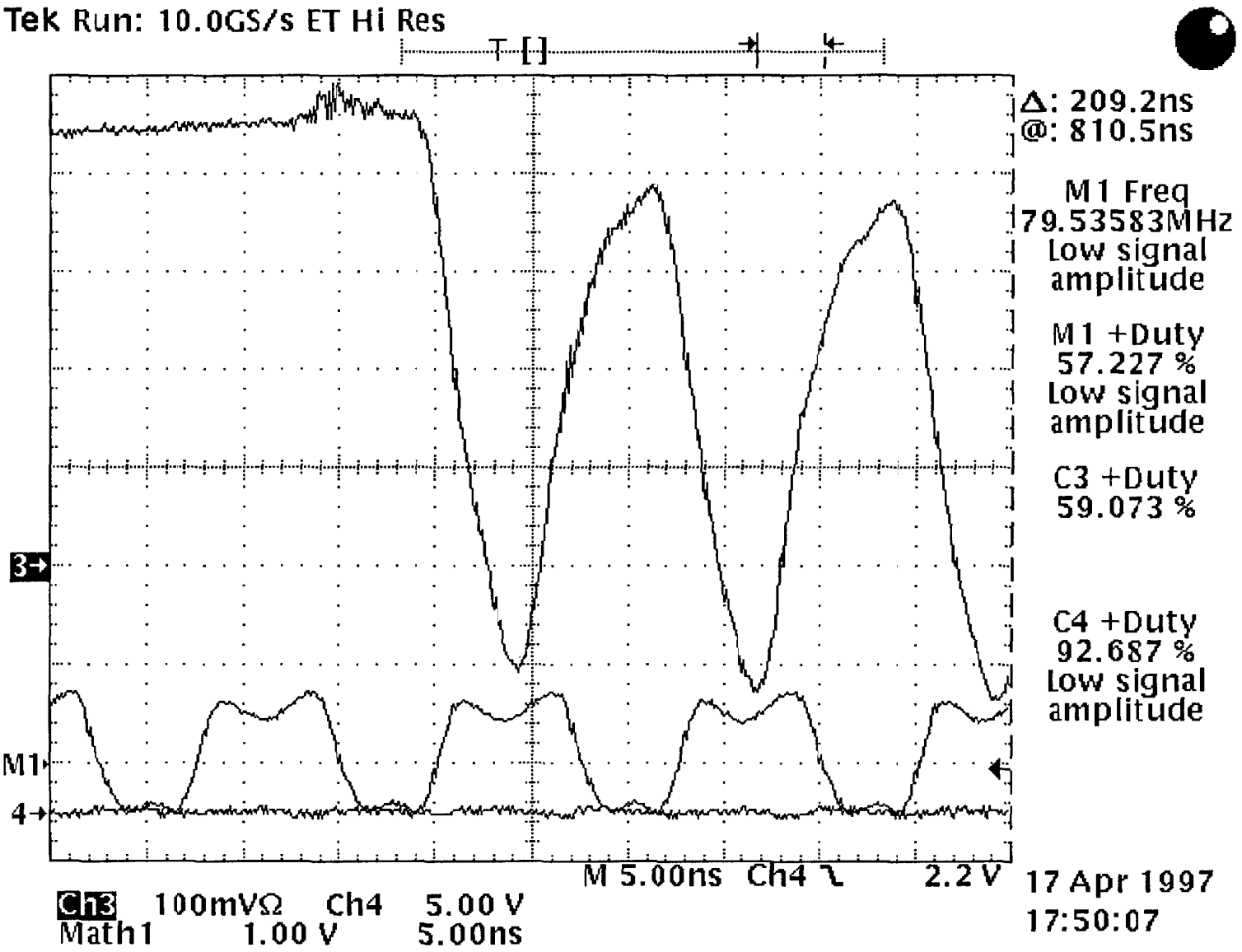
Board #5, Symmetrical Va & Vn, 413mV & 406mV

Board #6, Symmetrical Va & Vn, 602mV & 579mV

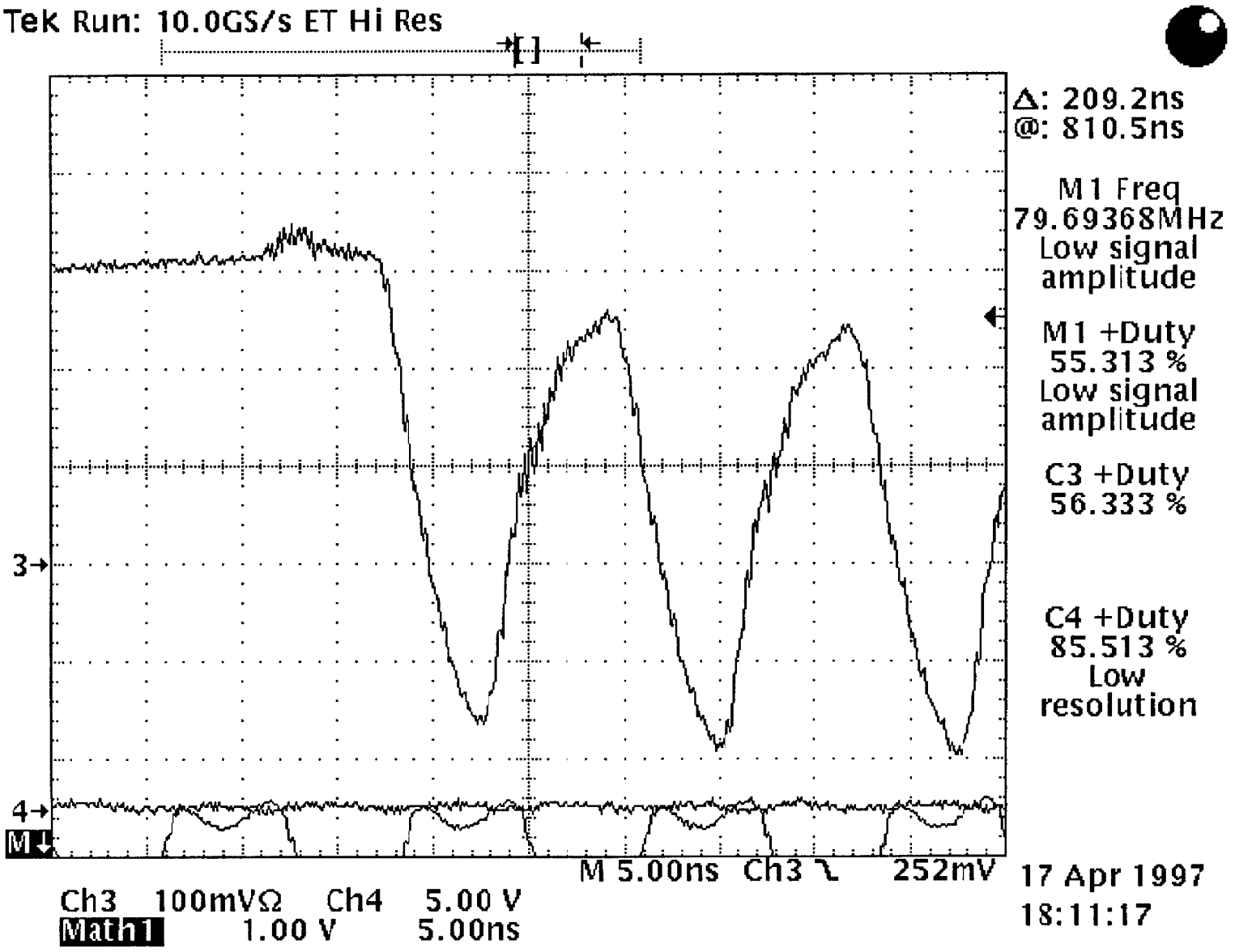
Table 1

Board # & Type	Amplitude	Over/Under Drive Ratio
Board 5 Asym	100mV	~1:4
Board 5 Sym	160mV	~1:1.5 (160/260)
Board 6 Asym	130mV	~1:4 (130/500)
Board 6 Sym	180mV	~1:2

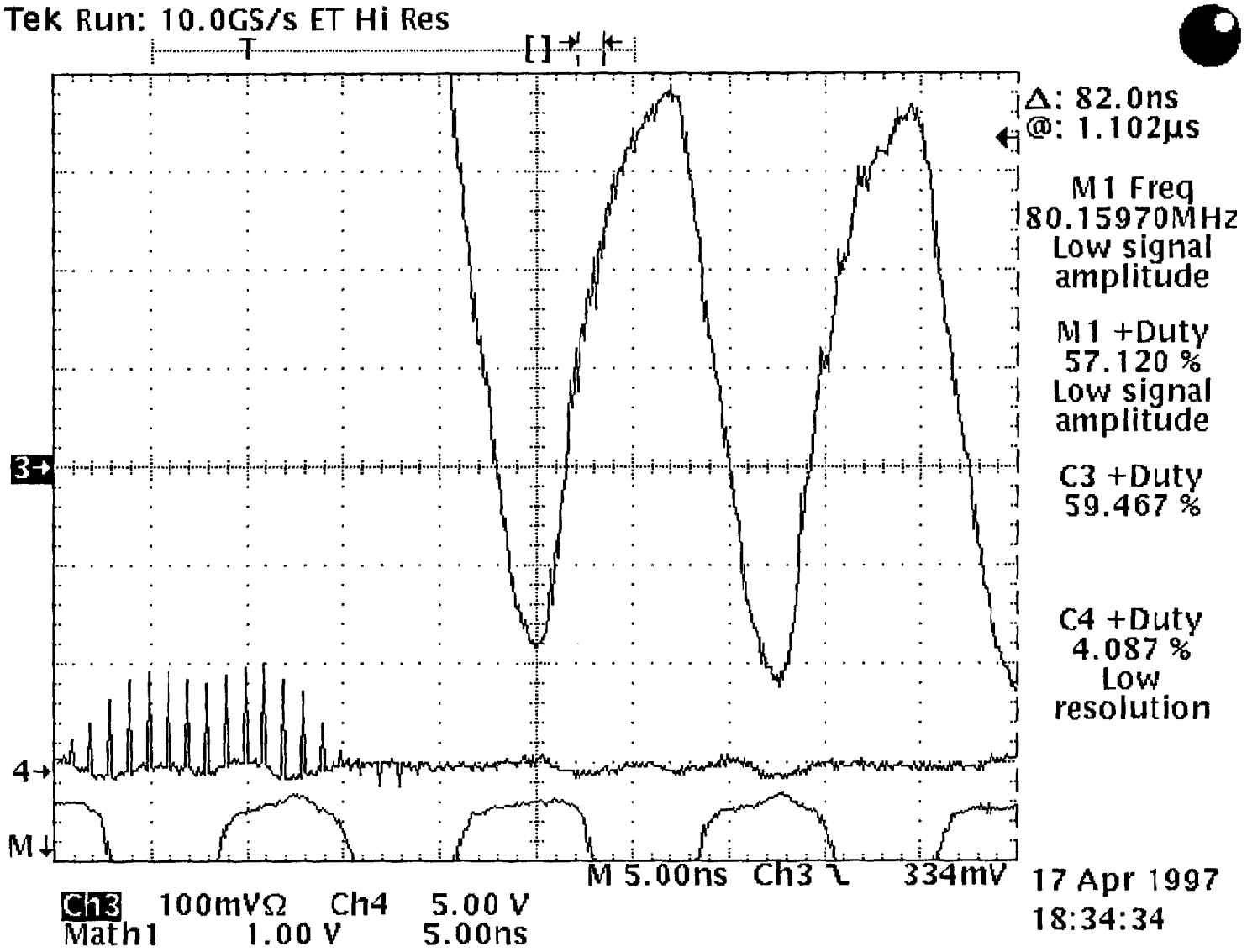
1st pulse Agilent Bit #5
80mhz 25 meter Cable



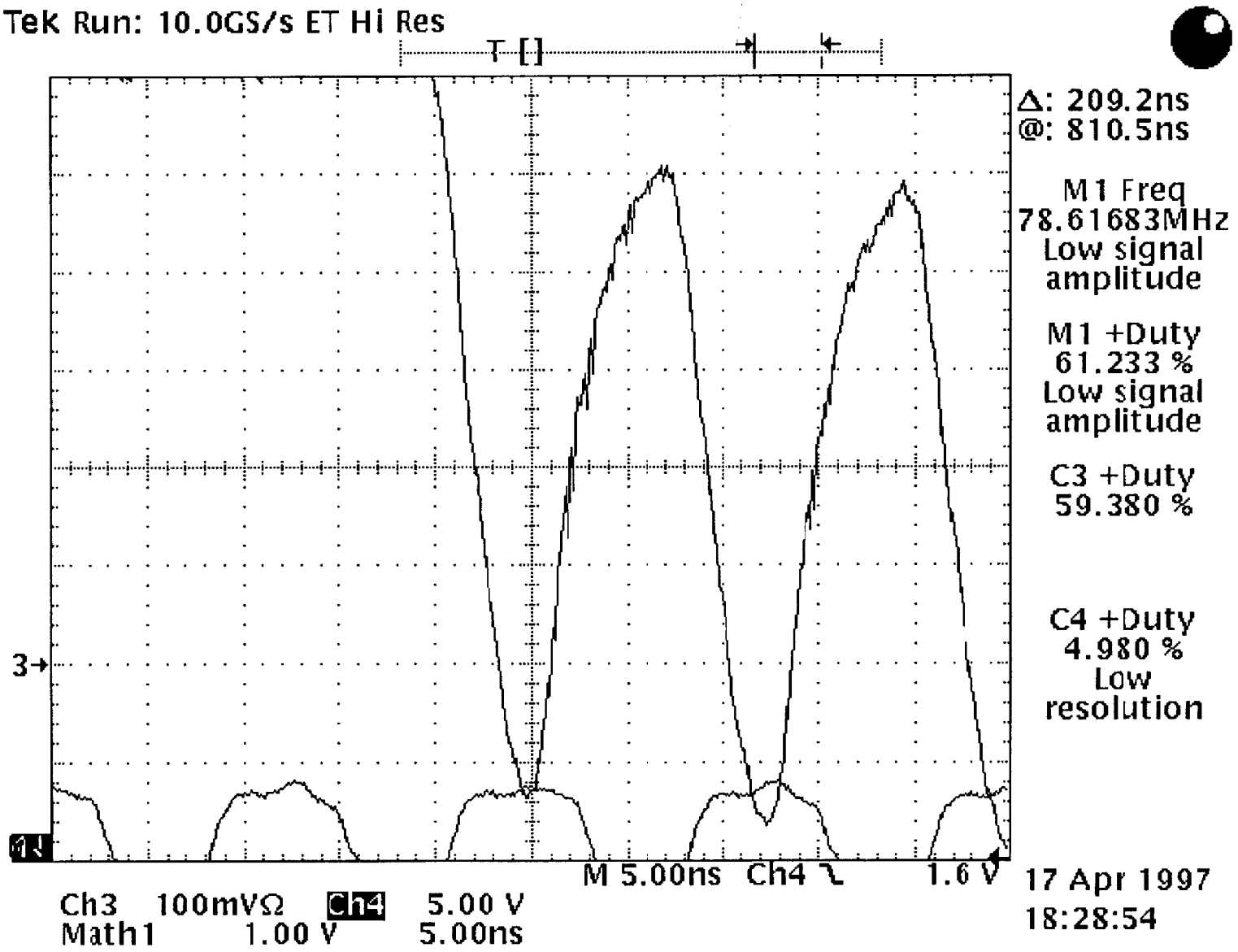
1st pulse Sym No Bias Point #5
80 mhz 25 meter Cable

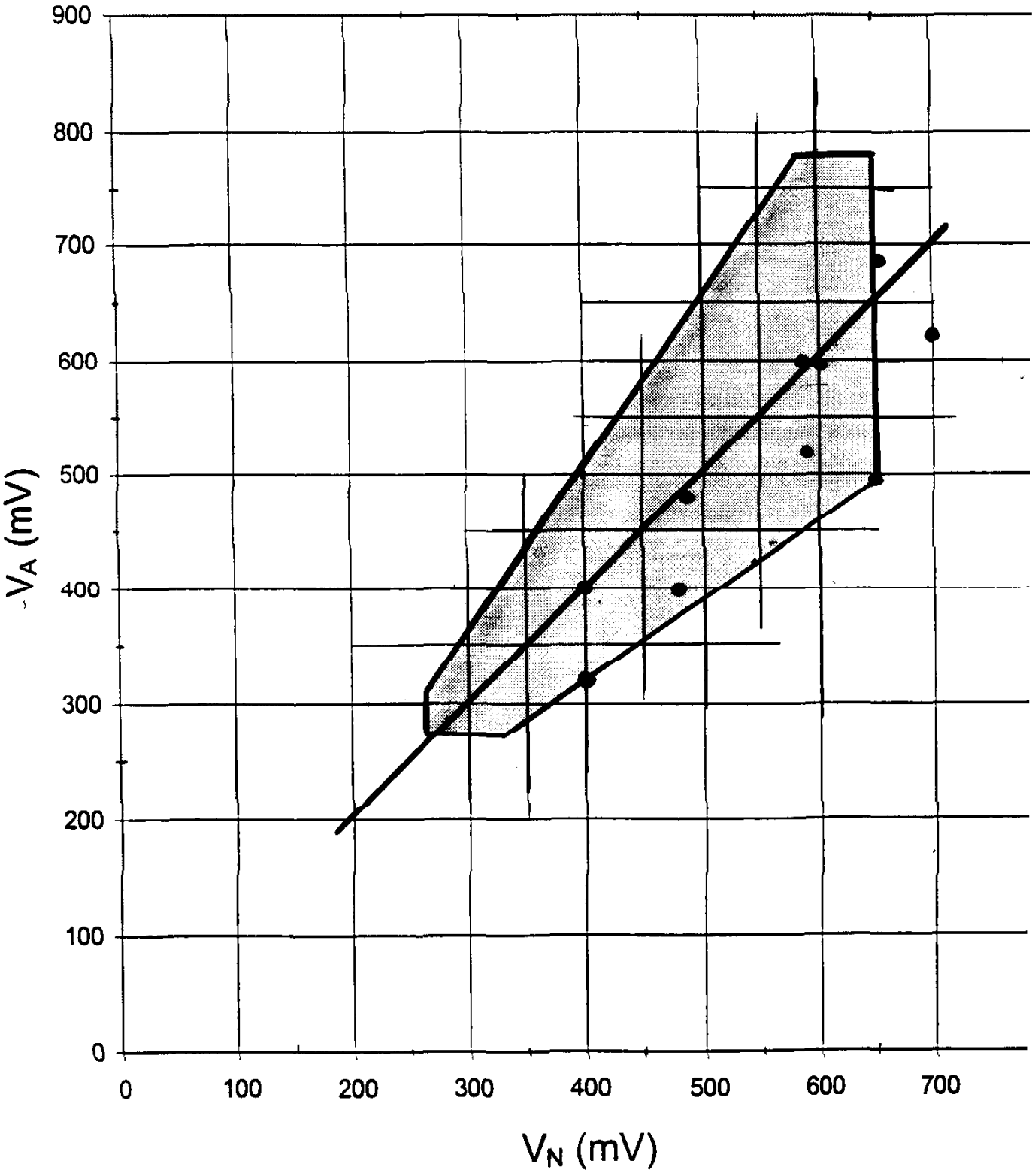


1st pulse Sym NO Bias
Board #6 25 meter cable



1st Pulse Asym Bias Board #6
80 mHz 25 meter cable





Domain for Driver Assertion and Negation Levels