

Back to Reality

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Issues

First Pulse Magnitude

Whose Fault is it?

Asymmetric/Symmetric Current Drive

Which is Better?

Tolerances of Current Sources

Can they Work?

Active Bias Terminators

Why we Need Them?



Do you Remember These?

Picture by Bill Ham, SPI-2 Meeting, June 6, 1996

First Pulse Distortion

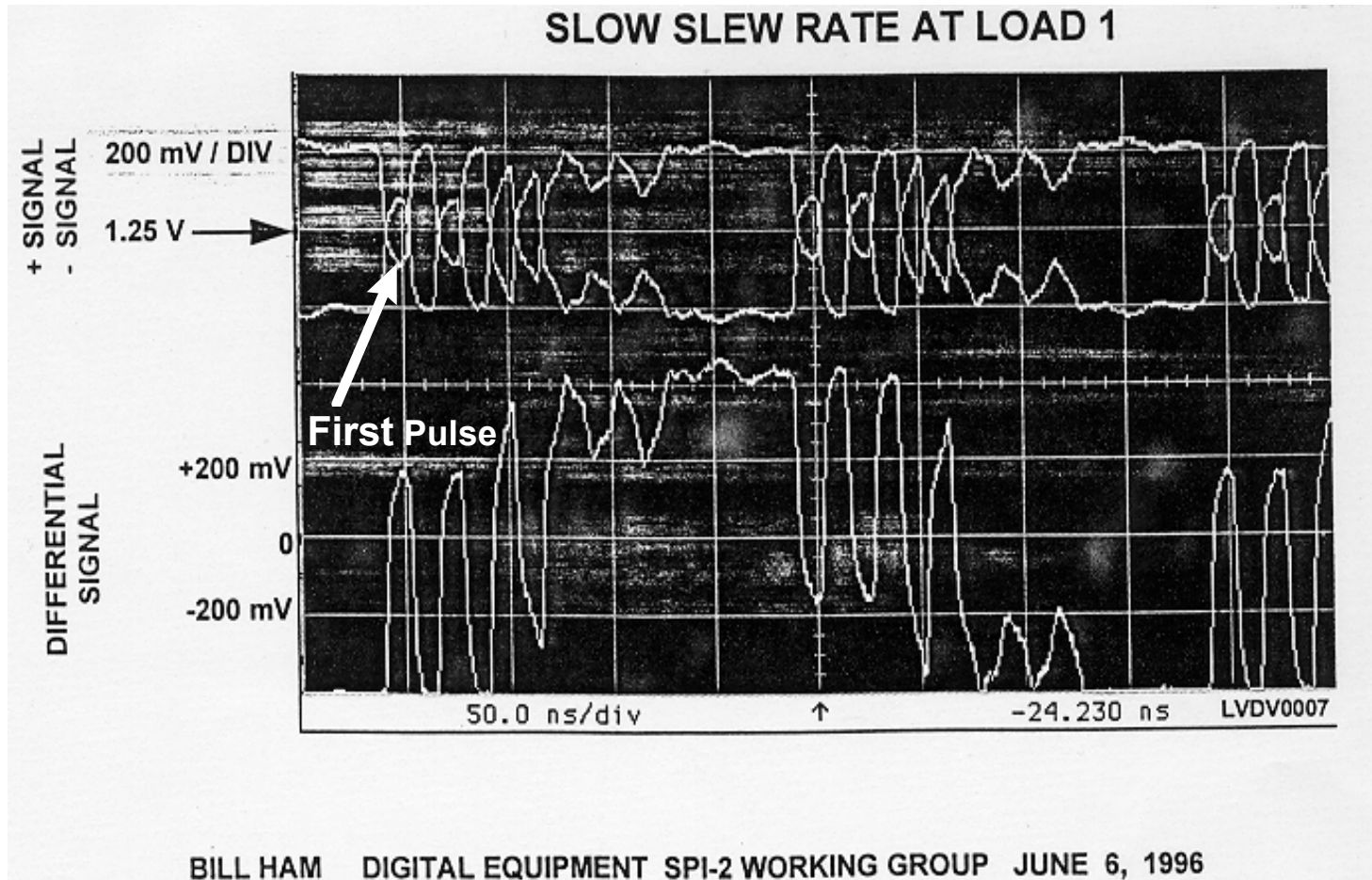
Symmetric Driver, 150W Symmetric Terminator

Fully Loaded Cable

Advanced Warning



Bill Ham's Measurement



Whose Fault is it?

¿ *Current Source Drive ?*

¿ *Active Terminator ?*

¿ *Asymmetric Driver ?*

¿ *Symmetric Driver ?*



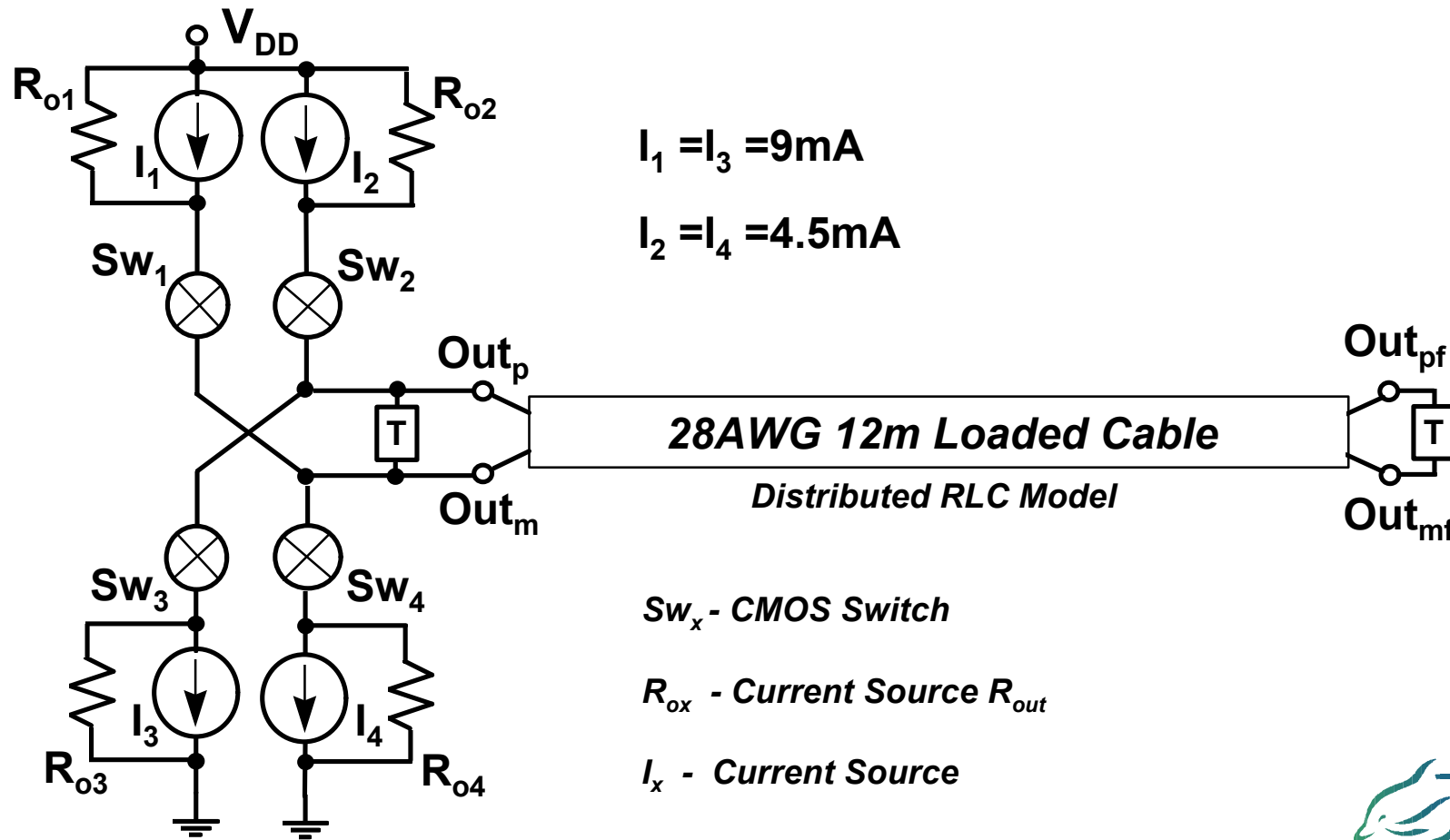
Whose Fault is it?

(None of the Above)

It s the TRANSMISSION LINE



How to Prove it ?



$$I_1 = I_3 = 9\text{mA}$$

$$I_2 = I_4 = 4.5\text{mA}$$

Sw_x - CMOS Switch

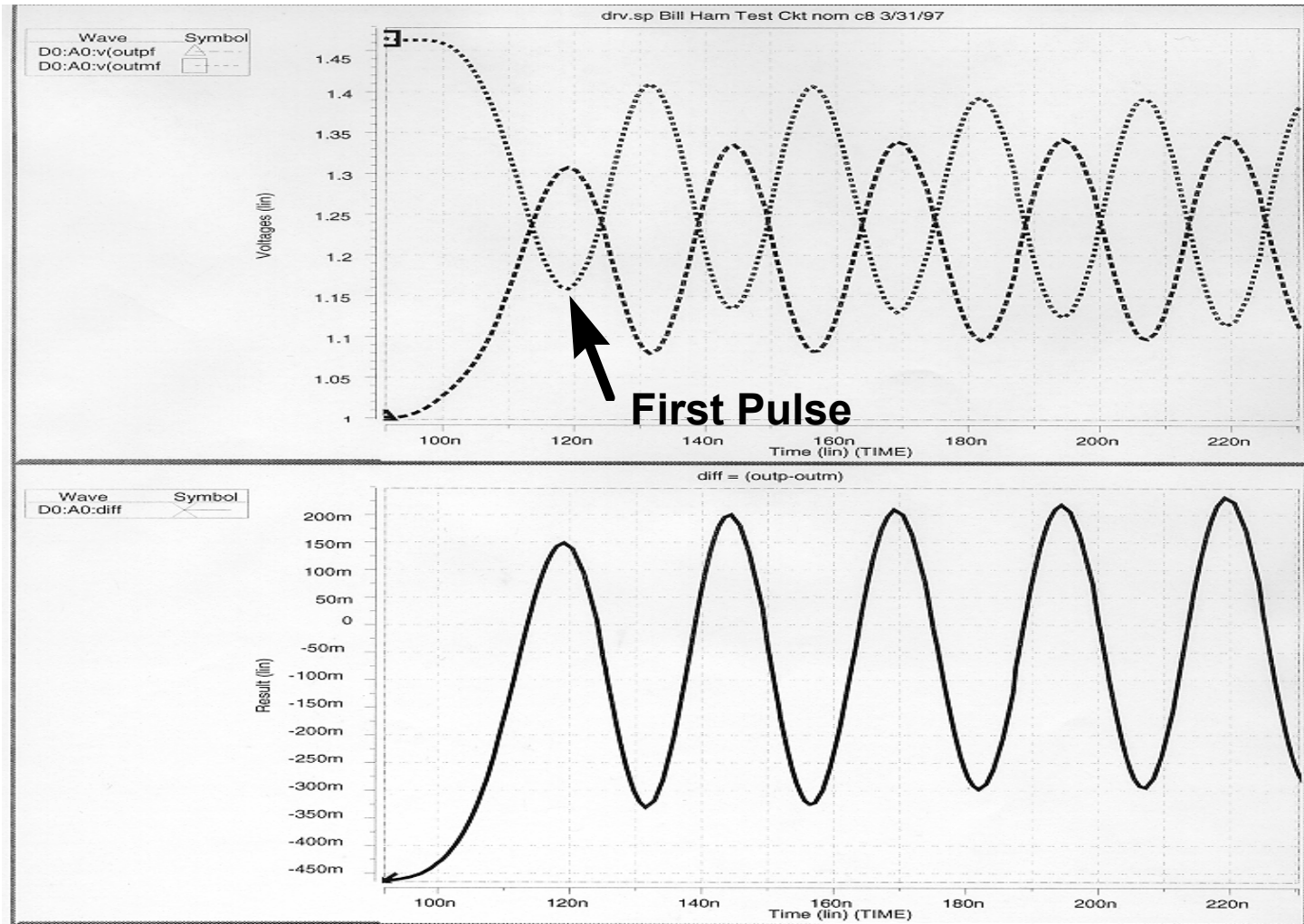
R_{ox} - Current Source R_{out}

I_x - Current Source

T - Balanced Terminator



Bill Ham's Test Ckt



Hspice Simulation of First Pulse



Which is Better?

Asymmetric Current Driver

or

Symmetric Current Driver ?

*Measured data - verified by simulation results
proves that symmetric drive offers no advantage
over present asymmetric drive*



Active Terminator *Benefits*

Built-in Bias for Fail-Safe Operation

Common-Mode Drive / Sink Current

Controlled Impedance

Low Capacitance

Simpler Receiver Circuitry

Even without Bias, Active Terminator Needed



Why Terminator Bias?

Provides Fail-Safe Operation

Symmetric Signal is Achievable

Balanced Receiver - Low Skew

No Complex Dual Receiver



Receiver Sensitivity

Typical $V_{in\ DIFF} = 400mV$ (No Load)

Worst Case $V_{in\ DIFF} = 60mV$ (Loaded Cable)

Worst Case Process, Temp & Supply Voltage

Low Skew Critical



Current Source Drivers *Issues*

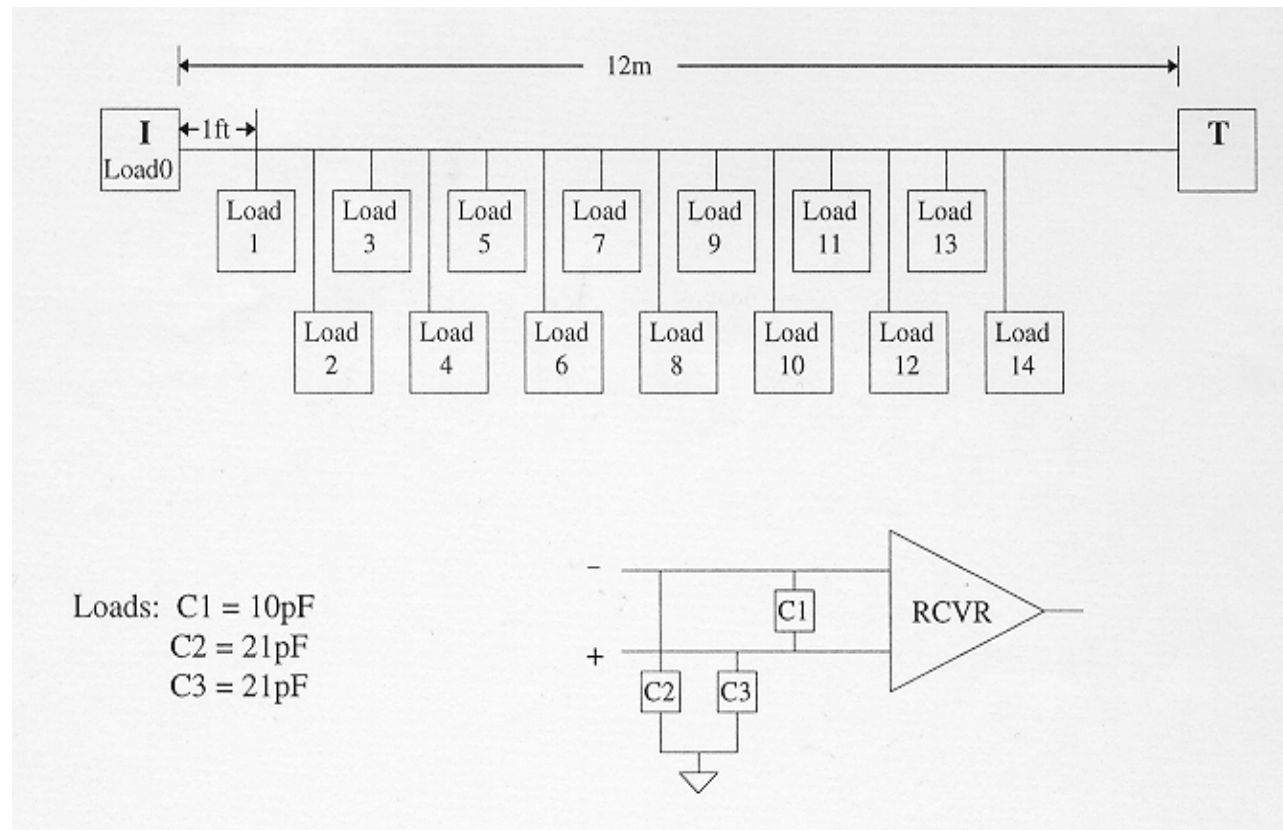
Current Mirror Match

I_{DP} versus I_{DN} Match

**Identical Challenge for Symmetric or
Asymmetric Drivers**

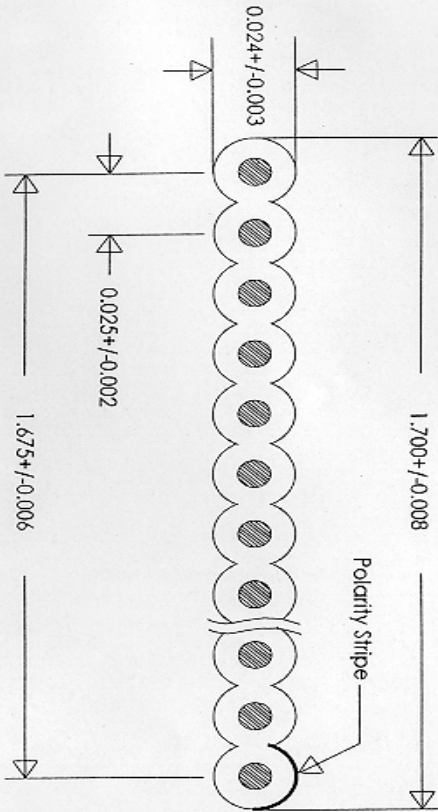


Test Setup



Cable Specification

REVISION RECORD			
REV	DESCRIPTION	DFIM	DATE
1	Initial Release	KP	7/12/93
3	Added Differential Impedance	KP	3/15/96



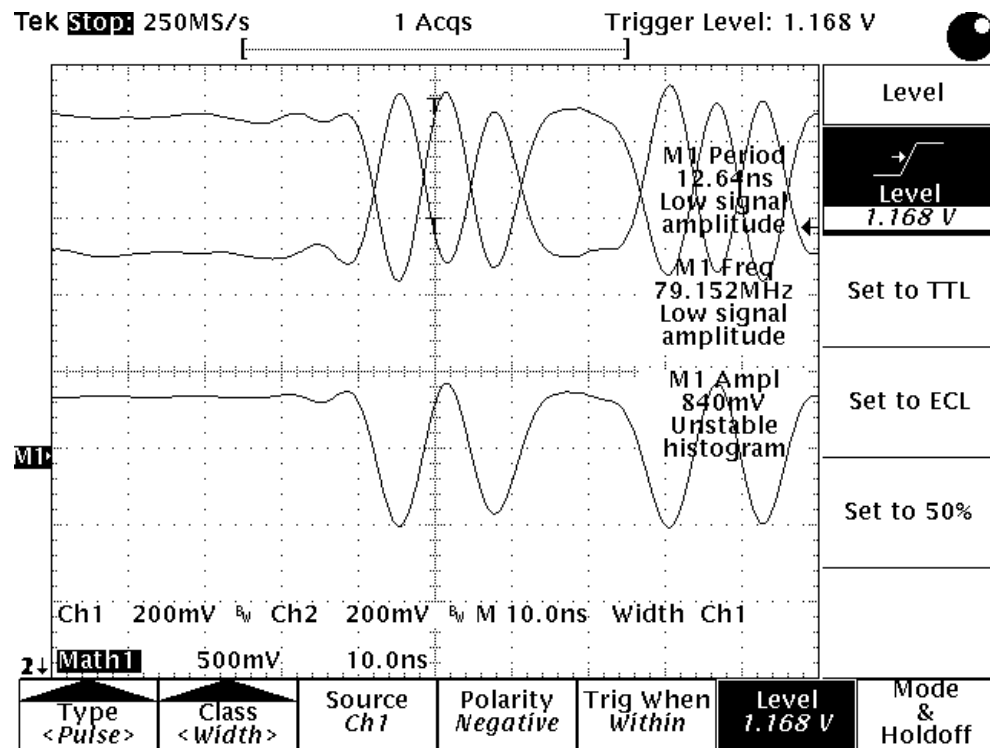
NOTES

- Insulation: FEP, Clear, Blue Stripe
- Conductor: 30 AWG, Solid, Silver Plated Copper, 0.010" Diameter
- Impedance: 90+/-6 ohms (Single End)
130+/-10 ohms (Differential)
- Capacitance: 14.7 pF/ft (Single End)
10.0 pF/ft (Differential)
- Inductance: 0.12 uH/ft (Single End)
0.17 uH/ft (Differential)
- Propagation Delay: 1.31 ns/ft
- Temperature Rating: -55C to 150C
- Voltage Rating: 150 V
- U.L. Style: 20726

TOLERANCES		TEMP-FLEX CABLE, INC.		CAGE CODE: 0CCG17	
INSEE CHANGE NO: 2 PL DEC +/-0.01		11 Depot Street South Orono, MA 01560 Phone: 508-839-5987 Fax: 508-839-4128		PART No:	
DFIM: KP	DATE: 3/15/96	TITLE: 30 AWG Solid 28 Conductor		F3001S-68-025-85	

Fast-80 Transfer

@ Driver REQ - No Load

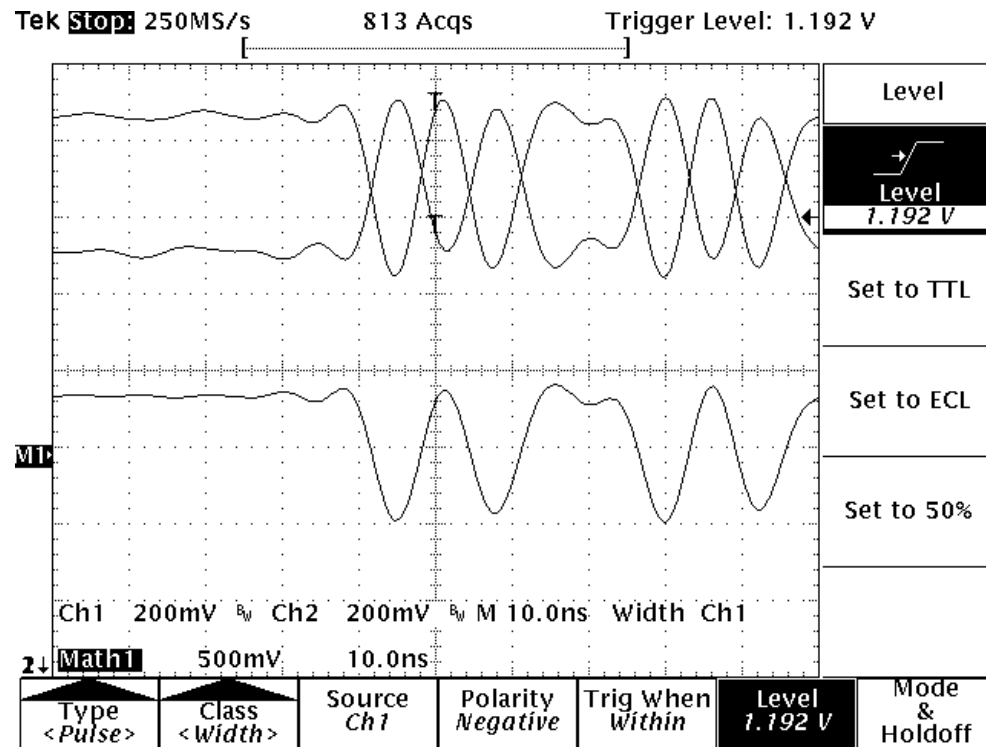


$$I_{\text{ASSERT}} / I_{\text{NEGATE}} = 9\text{mA} / 4.5\text{mA}$$

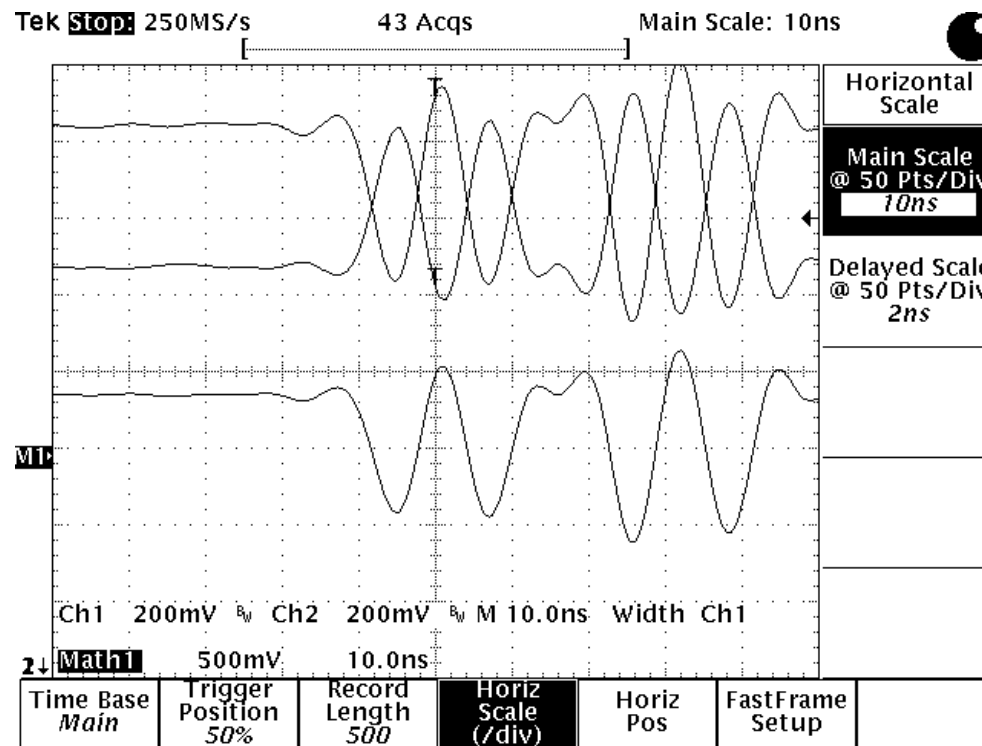


Fast-80 Transfer

@ Driver REQ - 15 Loads

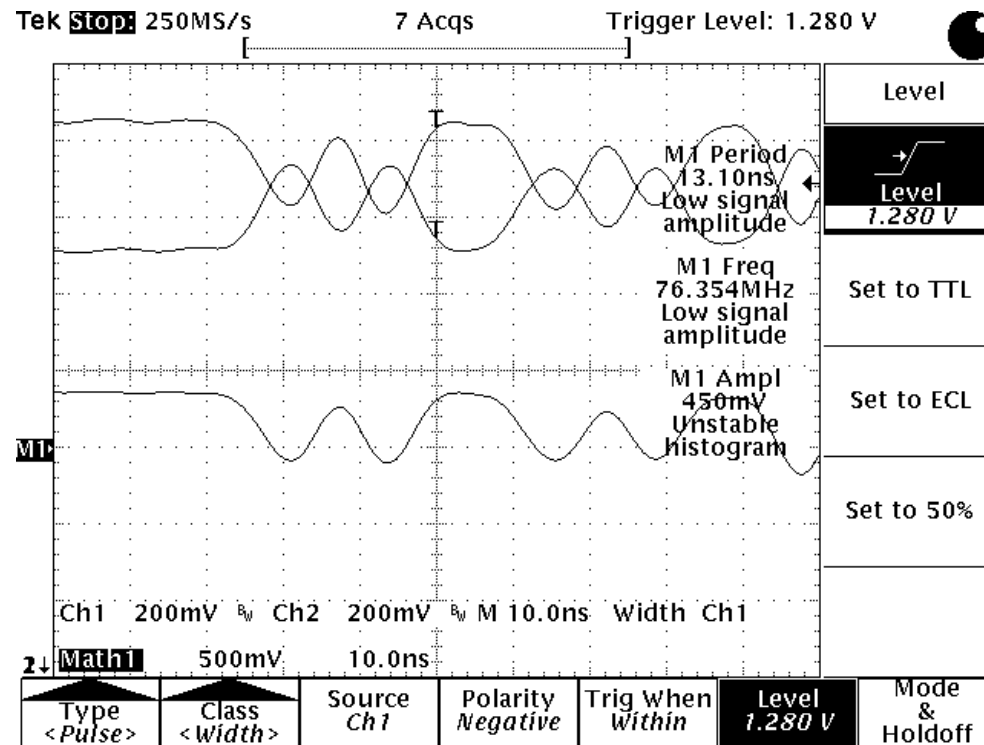


Fast-80 Transfer @ Driver ACK - 15 Loads



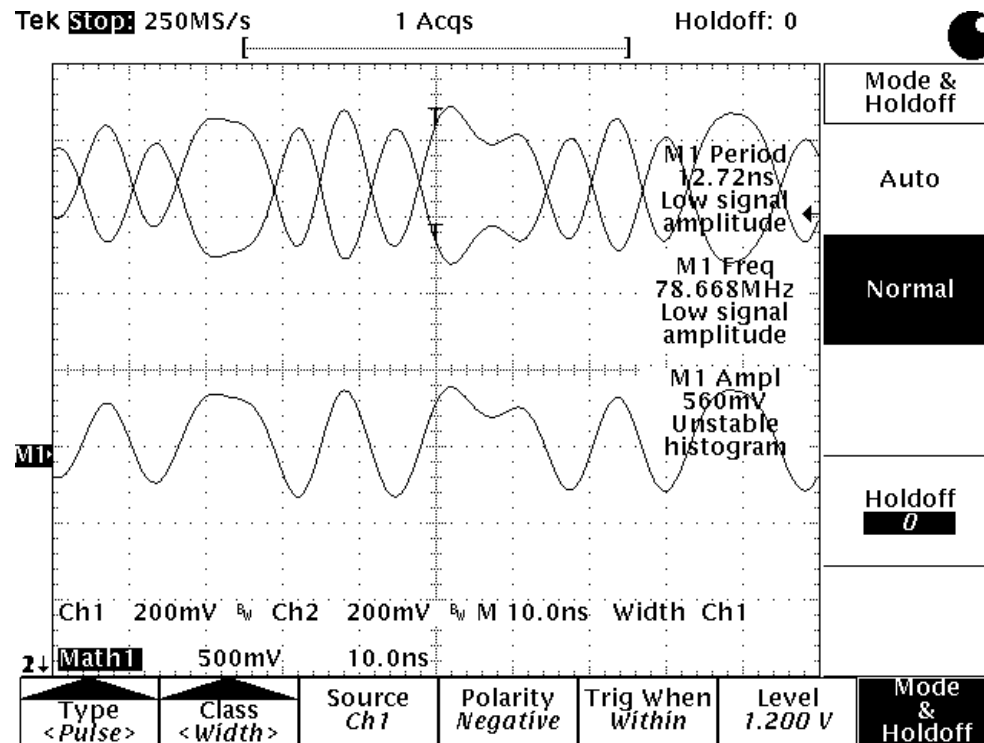
Fast-80 Transfer

REQ @ Load 0



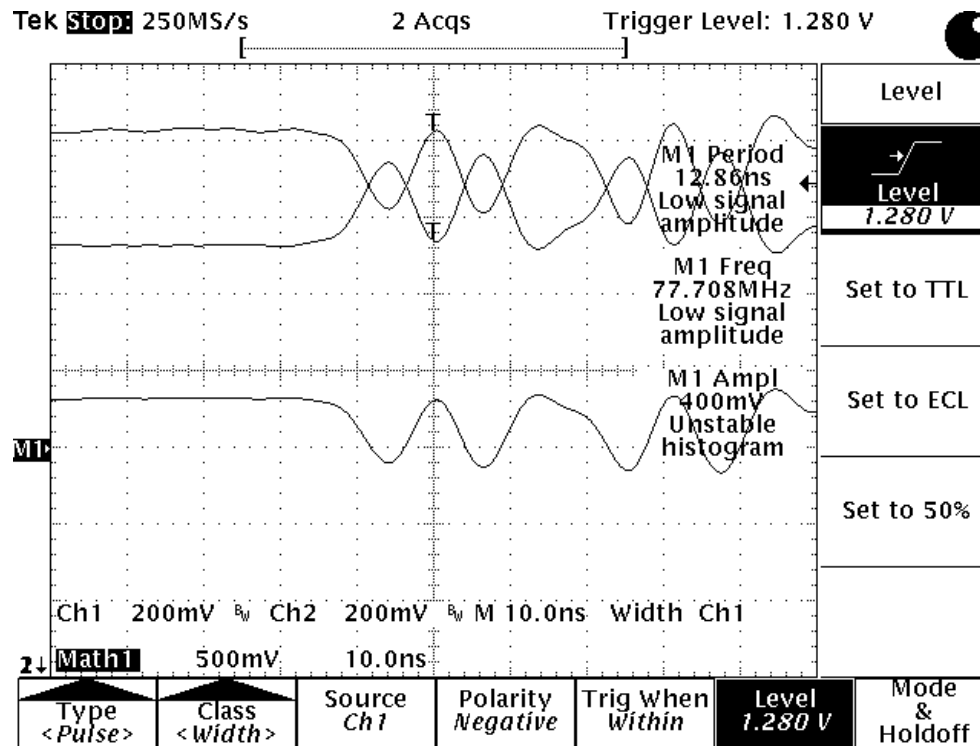
Fast-80 Transfer

REQ @ Load 12



Fast-80 Transfer

$$I_{AS}/I_{NEG} = 8mA/4mA$$

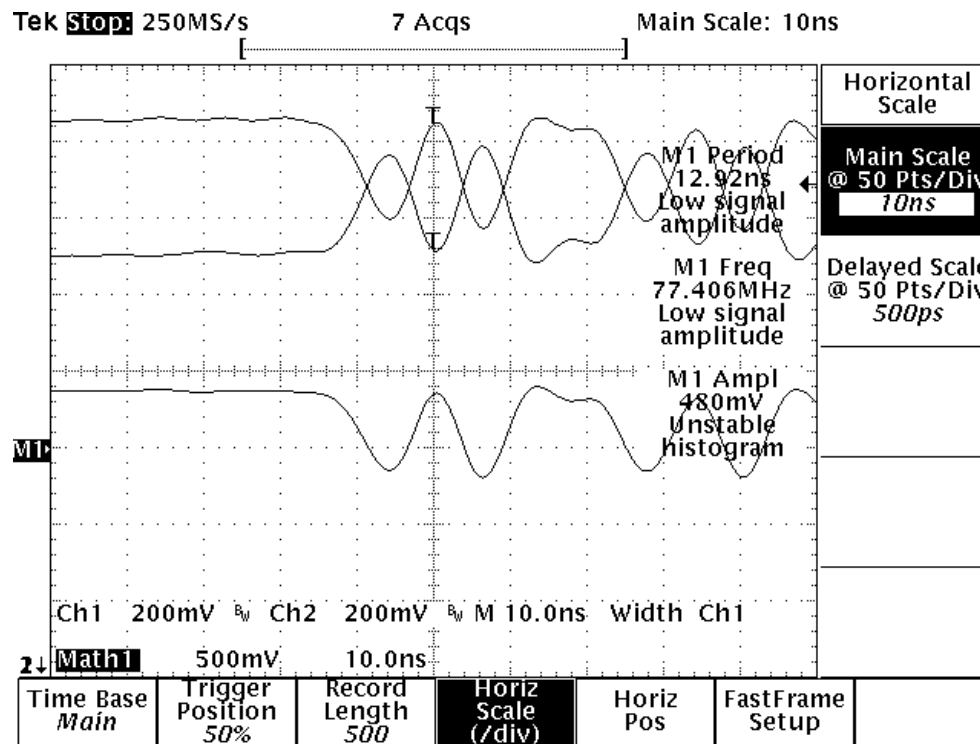


REQ @ Load 1



Fast-80 Transfer

$$I_{AS}/I_{NEG} = 10\text{mA}/5\text{mA}$$



REQ @ Load 1



LVD Power

Asymmetric Drive: $V_{\text{diff}}=400\text{mV}$, $Z_o=120\Omega$
 $V_{\text{BIAS}}=120\text{mV}$

lassert = 8.667mA

$$\Rightarrow P_D = (3.3-0.4)\text{V} * 8.667\text{mA} = 25.13\text{mW}$$

Inegate = 4.667mA

$$\Rightarrow P_D = (3.3-0.4)\text{V} * 4.667\text{mA} = 13.53\text{mW}$$

Total Average Power = 19.33mW



LVD Power

Symmetric Drive: $V_{\text{diff}}=400\text{mV}$, $Z_o=120\Omega$

lassert = 6.67mA

$$\Rightarrow P_D = (3.3-0.4)\text{V} * 6.67\text{mA} = 19.33\text{mW}$$

Inegate = 6.67mA

$$\Rightarrow P_D = (3.3-0.4)\text{V} * 6.67\text{mA} = 19.33\text{mW}$$

Total Average Power = 19.33mW



Symmetric Driver ?

No Advantage in Performance

No Fail-Safe Operation

Identical Power Dissipation

Increased Receiver Complexity



Beyond Fast-80

Cable Length

Number of Loads

Receiver Sensitivity

Cable Media

Protocol Issues

**If Symmetric Driver has Merit Beyond
Fast-80, the Transition Need Not to be
Disruptive**

