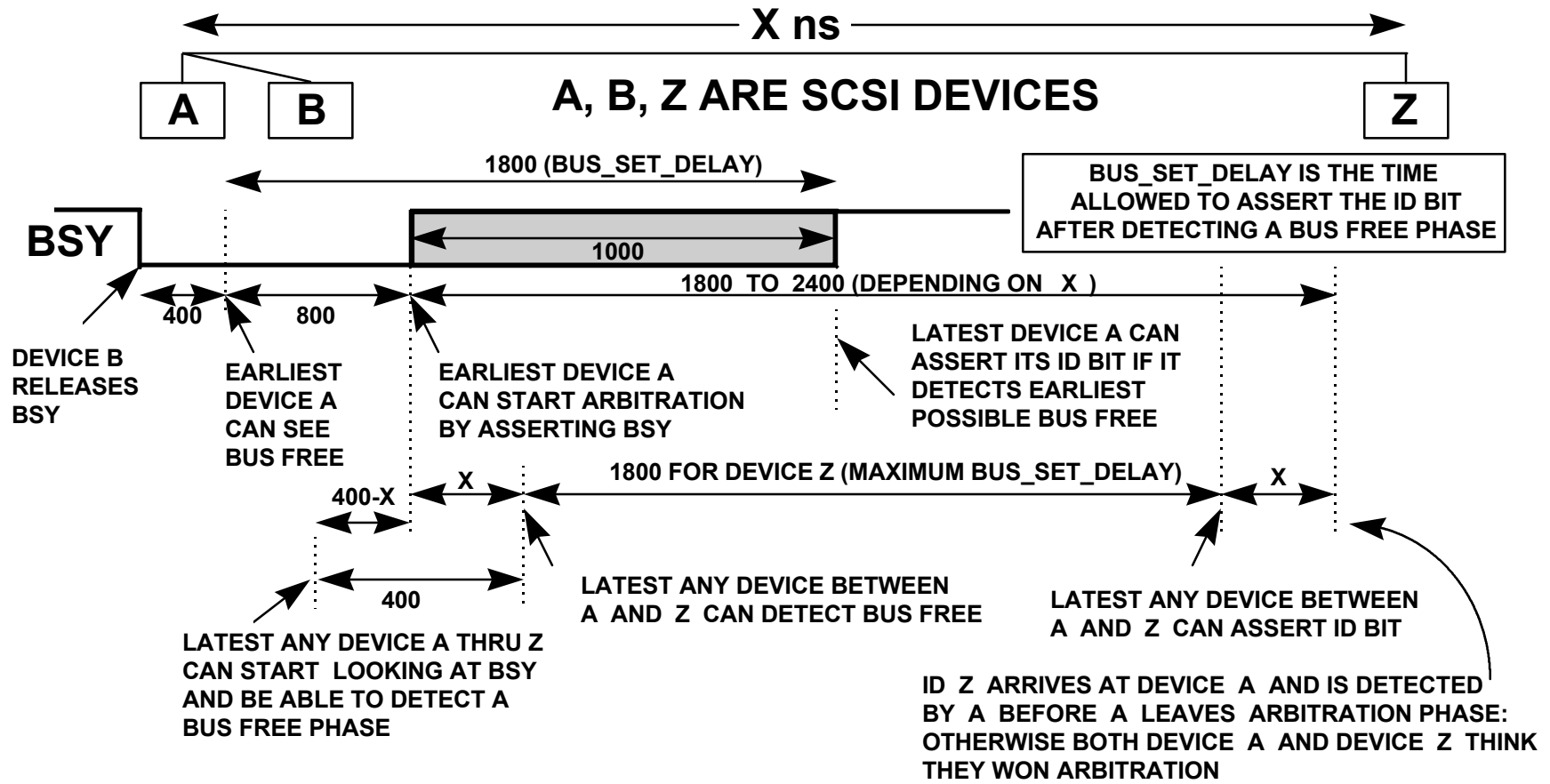


# EFFECT OF BUS\_SET\_DELAY ON MAXIMUM DOMAIN DELAY

T10/97-116r0



**WORST CASE UNDER ABOVE ASSUMPTIONS:  $2X + 1800 = 2400 \Rightarrow X = 300$  ns**

**NEED TO REDUCE MAXIMUM BUS\_SET\_DELAY TO 1600 ns OR LESS IN ORDER TO RECOVER THE FULL 400 ns PROPAGATION TIME ALLOWED BY THE BUS SETTLE DELAY**