

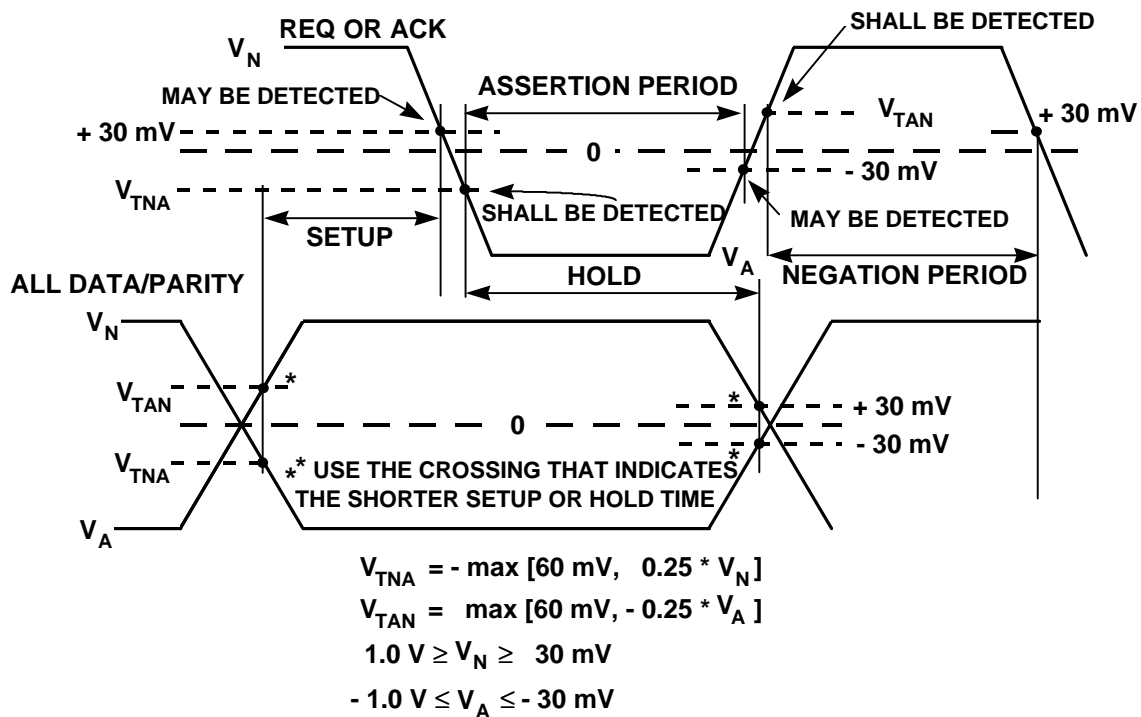
Date: 12/15/96

The SPI-2 editing session made the following recommendations:

Agree no change to 100 ms in rev 11. May have to add some wording to be clearer about how and when to sample DIFFSENS.

Hot plugging - Need to do something along the lines of 96-270. Add wording to force all drivers including ground drivers to a high impedance state for 100 milliseconds. Should we add a requirement for host adapters to generate a reset?

Figure 24 - LVD has no hysteresis; need to add may be detected and shall be detected to fig 24 and change to 30 mv. See revised figure



DIFFERENTIAL VOLTAGE SIGNALS IN ALL CASES