

To: X3T10 Committee (SCSI)

From: George Penokie (IBM)

Subject: Clarification of when parity is valid (SPI-2)

In SPI sections 10.11.3 Data path width the second sentence reads:

The DB(15-8,P1) signals are undefined and parity may not be valid.

That statement applies to signals being received, however, that is not clear from the statement itself. I propose the sentence be rewritten as follows:

At the receiver the DB(15-8,P1) signals are undefined and parity may not be valid.