

Date: Mar. 7, 1996
To: X3T10 Committee
From: Gerry Houlder, Seagate Technology
Subj: Add clarification to capacity retention requirement

The following text is from the set capacity proposal (X3T10/96-260 rev. 4):

If the device supports changing its capacity by changing the number of blocks field, then the number of blocks field is interpreted as follows:

- a) If the number of blocks is set to zero, the device shall be set to its maximum capacity. If the block size has not changed, the device shall not become format corrupted. This capacity setting shall be retained through reset events or power cycles.
- b) If the number of blocks is greater than zero and less than or equal to its maximum capacity, the device shall be set to that number of blocks. If the block size has not changed, the device shall not become format corrupted. This capacity setting shall be retained through reset events or power cycles.

Two sentences are shown in italics. It has been pointed out that the capacity retention requirement should be linked with the setting of the SP bit in the MODE SELECT command CDB, but this wording could be interpreted as saying the capacity shall be retained even if the SP bit is set to zero. Therefore I propose adding the underlined text so these two two sentences will read:

This capacity setting shall be retained through reset events or power cycles when the SP bit in the MODE SELECT command is set to one.