To: Membership of X3T10

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Subject: Software Write Protect Bit Mode Page Location for SCSI-3 SSC   X3T10/95-318r1.

The software write protect (SWP) bit was recently added to the read-write error recovery mode page in SSC version 3. This mode page pertains to error recovery.

I propose the bit should instead be located in the device configuration mode page at bit 2 of byte 10, since SWP is more related to configuration than recovery.