X3T10/95-175R0

CONGRUENT SOFTWARE, INC.
3998 Whittle Avenue
Oakland, CA 94602
(510) 531-5472

FROM: Peter Johansson

TO: Larry Lamers
           ASC X3T10 Technical Editor

DATE: February 10, 1995

RE: Editorial Correction to SPI Revision 15

CC: John Lohmeyer
           X3T10 Chair

Larry, there is a editorial error in Annex B, SCSI Configured Automatically (SCAM), in Revision 15 of X3T10/855D, SCSI-3 Parallel Interface.

On page 52, in section B.4.1.1 Transfer Cycles, the steps of a SCAM transfer cycle are enumerated. The underlined text shown below was inadvertently omitted in the transition from Ed Gardner's X3T9.2/93-109r5 to the current document and should be added to SPI before it finally goes to press.

   4. Read and latch data from the DB(4-0) signals. <u>All devices assert the DB(6) signal.</u>

I think it is safe to say that this is an editorial (rather than a technical) correction since the accompanying Figure B.5 Transfer Cycles on the following page clearly shows that DB(6) is to be asserted as part of step four.

Please add this request for correction to the agenda of the March X3T10 meeting in Newport Beach.

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Larry –

EDITORIAL NITS for SPI Rev 15 Annex B

The use of

"wired-or" vs "wired-OR"
"logical-or" vs "logical-OR"

is inconsistent. I think the right-hand version is preferable, but it should ~~be~~ (shall) be made uniform in either case (pun intended).

Peter

Also, pg. 51 "negtion" spelling error