FAST XX SCSI
NEW PROJECT PROPOSALS

• TWO NEW PROJECT PROPOSALS ARE NEEDED TO COMPLETE THE FAST XX SCSI SUITE:
  – A FAST 40 VERSION
  – A DIFFERENTIAL VERSION THAT CAN BE INTEGRATED INTO A SINGLE PROTOCOL CHIP

• THE FAST 40 VERSION WOULD BE SIMILAR TO THE FAST 20 VERSION
  – SHORTER SINGLE ENDED LENGTHS WOULD BE REQUIRED
  – DEVICE NUMBER/CAPACITANCES WOULD LIKELY CHANGE

• THE NEW DIFFERENTIAL VERSION WOULD CHANGE THE DIFFERENTIAL SIGNALS AND COMMON MODE LEVELS TO ALLOW EASY INTEGRATION WITH PROTOCOL FUNCTIONS ON THE SAME CHIP (THIS WOULD APPLY TO BOTH FAST 20 AND FAST 40)