

TO: ATA Local Bus Committee  
From: R. Kalish - Adaptec, Inc.  
RE: RESET on the Bus

From time to time an issue has come up that I think we finally need to address in the ATA-2 Document. The problem of how short a RESET signal on the cable should or should not be seen as a RESET by an ATA device needs to be covered in the standard. The standard currently states that the host shall hold the RESET for 25 micro-seconds, but there is no specification for how the drive should respond to a signal shorter than this. Should a drive see a 10 microsecond signal on the RESET signal as a RESET? How about a pulse of only 10 nano-seconds??

I propose that we add a statement to the ATA-2 specification in the RESET signal description section as shown below:

ATA devices shall not recognize a signal shorter than 20 nSec. on the RESET-signal ( pin 1 ) as a valid reset signal. Devices may respond to any signal greater than 20 nSec, and must recognize a signal greater than 25  $\mu$ Seconds

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