To: T10 SAS Protocol and Phy Working Groups

From: Brian Day and William Petty

Subject: SAS-2.1 / SPL: 08-439 An Optical OOB Method

Revision History

Revision 0 - Initial draft

Revision 1 - Changed the OOB Idle behavior for optical mode to address EMI concerns raised at the November working group meetings. Added paragraph to descibe negotiation idle in section 6.8.4.

Revision 2 - Editorial changes based on Jan working group feedback. Add new primitive, OOB_IDLE, to be used instead of ALIGN(2), for OOB idle to simplify idle detection in 3GBps environments.

Revision 3 - Reformatted based on the separated SAS-2.1 and SPL specs. Changed OOB idle amount of scrambled data from “up to 1024 dwords” to “up to 512 dwords”.

Related Documents

sas21-r00 : Serial Attached SCSI - 2.1, Draft revision 0

spl-r01 : SAS Protocol Layer, Draft revision 1

Overview

This proposal provides an alternative OOB method that can be used with commodity optical transceivers. Instead of allowing the differential lines to go to D.C. Idle, specific character sequences (OOB_IDLE and ALIGN(3)) are proposed to provide an “OOB Idle” condition, replacing the D.C. Idle condition.

The proposal has the following main goals:

a) no changes to existing SP state machine operation to significantly leverage the past interoperability testing.
b) be compatible with commodity optical modules/cables used in other interface technologies.
c) define the protocol behavior at the connector interface.

Proposed Changes to SAS-2.1

In section 3 definitions:

0.0.1 D.C. idle: A differential signal level that is nominally 0 V(P-P)~~, used during the idle time (see 3.1.42) and negation time (see 3.1.51) of an OOB signal (see 0.0.7).~~ See 5.7.4.

0.0.2 D.C. mode : A mode in which D.C. idle is used during the idle time and negation time of an OOB signal, (see x.x.x).

0.0.3 idle time: The part of an OOB signal (see 0.0.7) where ~~D.C.~~ OOB idle (see 3.1.14) is being transmitted. See 5.9.

0.0.4 negation time: The part of an OOB signal (see 0.0.7) during which ~~D.C.~~ OOB idle (see 3.1.14) is transmitted after the last OOB burst (see 3.1.52). See 5.9.

0.0.5 OOB burst: The transmission of signal transitions or ALIGN(3) primitives for a burst time (see 3.1.5). See 5.9.1.

0.0.6 OOB idle: The transmission of D.C. idle when D.C mode (see x.x.x) is enabled, or a defined sequence of dwords when optical mode is enabled (see x.x.x).

0.0.7 optical mode: A mode in which a defined sequence of dwords is used during the idle time and negation time of an OOB signal (see x.x.x).

In section 5:

5.7 Transmitter device and receiver device electrical characteristics

5.7.1 General electrical characteristics

Table 1 defines the general electrical characteristics, which apply to both transmitter devices and receiver devices.

Table 1 — General electrical characteristics

Characteristic	Units	1.5 Gbps (i.e., G1)	3 Gbps (i.e., G2)	6 Gbps (i.e., G3)
Physical link rate (nominal)	MBps	150	300	600
Unit interval (UI) (nominal) [a]	ps	$666.\overline{6}$	$333.\overline{3}$	$166.\overline{6}$
Baud rate (f_{baud}) (nominal)	Gigasymbols/s	1.5	3	6
Maximum A.C. coupling capacitor [b]	nF	12		
Maximum noise during OOB idle time [c] [d]	mV(P-P)	120		

[a] $666.\overline{6}$ equals 2000 / 3. $333.\overline{3}$ equals 1000 / 3. $166.\overline{6}$ equals 500 / 3.
[b] The coupling capacitor value for A.C. coupled transmit and receive pairs. See 5.7.4.2 for A.C. coupling requirements for transmitter devices. See 5.7.5.2 for A.C. coupling requirements for receiver devices. The equivalent series resistance at 3 GHz should be less than 1 ohm.
[c] With a measurement bandwidth of 1.5 × f_{baud} (e.g., 9 GHz for 6 Gbps), no signal level during the idle time shall exceed the specified maximum differential amplitude.
[d] This is not applicable when optical mode is enabled.

5.7.4.4 Transmitter device signal output characteristics for untrained 1.5 Gbps and 3 Gbps as measured with the zero-length test load

Table 22 specifies the signal output characteristics for the transmitter device for untrained 1.5 Gbps and 3 Gbps as measured with the zero-length test load (see 5.6.2) attached at a transmitter device compliance point (i.e., IT or CT). All specifications are based on differential measurements. See 5.7.4.6 for trained 1.5 Gbps, 3 Gbps, and 6 Gbps transmitter device signal output characteristics.

Table 2 — Transmitter device signal output characteristics for untrained 1.5 Gbps and 3 Gbps as measured with the zero-length test load at IT and CT

Signal characteristic [a]	Units	Untrained	
		1.5 Gbps	3 Gbps
Maximum intra-pair skew [b]	ps	20	15
Maximum transmitter device off voltage [c,f]	mV(P-P)	50	
Maximum (i.e., slowest) rise/fall time [d]	ps	273	137
Minimum (i.e., fastest) rise/fall time [d]	ps	67	
Maximum transmitter output imbalance [e]	%	10	

[a] All tests in this table shall be performed with zero-length test load (see 5.6.2).
[b] The intra-pair skew measurement shall be made at the midpoint of the transition with a repeating 01b or 10b pattern (e.g., D10.2 or D21.5)(see the phy test patterns in the Protocol-Specific diagnostic page in SPL) on the physical link. The same stable trigger, coherent to the data stream, shall be used for both the Tx+ and Tx- signals. Intra-pair skew is defined as the time difference between the means of the midpoint crossing times of the Tx+ signal and the Tx- signal.
[c] The transmitter device off voltage is the maximum A.C. voltage measured at compliance points IT and CT when the transmitter is unpowered or transmitting D.C. idle (e.g., during idle time of an OOB signal).
[d] Rise/fall times are measured from 20 % to 80 % of the transition with a repeating 01b or 10b pattern (e.g., D10.2 or D21.5)(see the phy test patterns in the Protocol-Specific diagnostic page in SPL) on the physical link.
[e] The maximum difference between the V+ and V- A.C. rms transmitter device amplitudes measured with CJTPAT (see Annex A) into the zero-length test load shown in figure 59 (see 5.6.2), as a percentage of the average of the V+ and V- A.C. rms amplitudes.
[f] This is not applicable when optical mode is enabled.

5.7.4.5 Transmitter device signal output characteristics for untrained 1.5 Gbps and 3 Gbps as measured with each test load

Table 3 specifies the signal output characteristics for the transmitter device for untrained 1.5 Gbps and 3 Gbps as measured with each test load (i.e., the zero-length test load (see 5.6.2) and either the TCTF test load (see 5.6.3) or the low-loss TCTF test load (see 5.6.4)) attached at a transmitter device compliance point (i.e., IT or CT). All specifications are based on differential measurements. See 5.7.4.6 for trained 1.5 Gbps, 3 Gbps, and 6 Gbps transmitter device signal output characteristics.

Table 3 — Transmitter device signal output characteristics for untrained 1.5 Gbps and 3 Gbps as measured with each test load at IT and CT

Signal characteristic	Units	IT, untrained		CT, untrained	
		1.5 Gbps	3 Gbps	1.5 Gbps	3 Gbps
Maximum voltage (non-operational)	mV(P-P)	2 000			
Maximum peak to peak voltage (i.e., 2 × Z2 in figure 75) if SATA is not supported	mV(P-P)	1 600			
Maximum peak to peak voltage (i.e., 2 × Z2 in figure 75) if SATA is supported	mV(P-P)	see SATA [a]		N/A	
Minimum eye opening (i.e., 2 × Z1 in figure 75), if SATA is not supported	mV(P-P)	325	275	275	
Minimum eye opening (i.e., 2 × Z1 in figure 75), if SATA is supported	mV(P-P)	see SATA [a]		N/A	
Maximum DJ [b, c, d]	UI	0.35			
Maximum half of TJ (i.e., X1 in figure 75) [b, c, d, e, f, g, h]	UI	0.275			
Center of bit time (i.e., X2 in figure 75)	UI	0.50			
Maximum intra-pair skew [i]	ps	80	75	80	75

[a] Amplitude measurement methodologies of SATA and this standard differ. Under conditions of maximum rise/fall time and jitter, eye diagram methodologies used in this standard may indicate less signal amplitude than the technique specified by SATA. Implementers of designs supporting SATA are required to ensure interoperability and should perform additional system characterization with an eye diagram methodology using SATA devices.

[b] All DJ and TJ values are level 1 (see MJSQ).

[c] The values for jitter in this table are measured at the average signal amplitude point.

[d] The DJ and TJ values in this table apply to jitter measured as described in 5.7.3.3. Values for DJ and TJ shall be calculated from the CDF for the jitter population using the calculation of level 1 jitter compliance levels method in MJSQ.

[e] TJ is specified at a CDF level of 10^{-12}.

[f] If TJ received at any point is less than the maximum allowed, then the jitter distribution of the signal is allowed to be asymmetric. The TJ plus the magnitude of the asymmetry shall not exceed the allowed maximum TJ. The numerical difference between the average of the peaks with a BER that is less than 10^{-12} and the average of the individual events is the measure of the asymmetry.

Jitter peak-to-peak measured < (maximum TJ - |Asymmetry|).

[g] The value for X1 applies at a TJ probability of 10^{-12}. At this level of probability, direct visual comparison between the mask and actual signals is not a valid method for determining compliance with the jitter requirements.

[h] The value for X1 is also half the value of TJ for maximum delivered jitter listed in table 33 (see 5.7.5.5). The test or analysis shall include the effects of the JTF (see 5.7.3.2).

[i] The intra-pair skew measurement shall be made at the midpoint of the transition with a repeating 01b or 10b pattern (e.g., D10.2 or D21.5)(see the phy test patterns in the Protocol-Specific diagnostic page in SPL) on the physical link. The same stable trigger, coherent to the data stream, shall be used for both the Tx+ and Tx- signals. Intra-pair skew is defined as the time difference between the means of the midpoint crossing times of the Tx+ signal and the Tx- signal at the probe points.

5.7.4.6 Transmitter device signal output characteristics for trained 1.5 Gbps, 3 Gbps, and 6 Gbps

5.7.4.6.1 Transmitter device signal output characteristics for trained 1.5 Gbps, 3 Gbps, and 6 Gbps overview

Table 24 specifies the signal output characteristics for the transmitter device for trained 1.5 Gbps, 3 Gbps, and 6 Gbps as measured with the zero-length test load (see 5.6.2), unless otherwise specified, attached at a transmitter device compliance point (i.e., IT or CT). All specifications are based on differential measurements.

Table 4 — Transmitter device signal output characteristics for trained 1.5 Gbps, 3 Gbps, and 6 Gbps at IT and CT

Signal characteristic	Units	Minimum	Nominal	Maximum
Peak to peak voltage (V_{P-P}) [a]	mV(P-P)	850		1 200
Transmitter device off voltage [b] [l]	mV(P-P)			50
Withstanding voltage (non-operational)	mV(P-P)	2 000		
Rise/fall time [c]	UI	0.25 [d]		
Reference differential impedance [e]	ohm		100	
Reference common mode impedance [e]	ohm		25	
Common mode voltage limit (rms) [f]	mV			30
RJ [g, i]	UI			0.15 [j]
TJ [h, i]	UI			0.25 [d]
WDP at 6 Gbps [k]	dB			13
WDP at 3 Gbps [k]	dB			7
WDP at 1.5 Gbps [k]	dB			4.5

[a] See 5.7.4.6.6 for the V_{P-P} measurement method.
[b] The transmitter device off voltage is the maximum A.C. voltage measured at compliance points IT and CT when the transmitter is unpowered or transmitting D.C. idle (e.g., during idle time of an OOB signal).
[c] Rise/fall times are measured from 20 % to 80 % of the transition with a repeating 01b or 10b pattern (e.g., D10.2 or D21.5)(see the phy test patterns in the Protocol-Specific diagnostic page in SPL) on the physical link.
[d] 0.25 UI is $41.\overline{6}$ ps at 6 Gbps, $83.\overline{3}$ ps at 3 Gbps, and $166.\overline{6}$ ps at 1.5 Gbps.
[e] See 5.7.4.6.3 for transmitter device S-parameters characteristics.
[f] This is a broadband limit. For additional limits on spectral content, see figure 78 and table 25.
[g] The RJ measurement shall be performed with a repeating 0011b or 1100b pattern (e.g., D24.3)(see the phy test patterns in the Protocol-Specific diagnostic page in SPL) with SSC disabled. RJ is 14 times the RJ 1 sigma value, based on a BER of 10^{-12}. For simulations based on a BER of 10^{-15}, the RJ specified is 17 times the RJ 1 sigma value.
[h] The TJ measurement shall be performed with a repeating 0011b or 1100b pattern (e.g., D24.3)(see the phy test patterns in the Protocol-Specific diagnostic page in SPL). If the transmitter device supports SSC, then this test shall be performed with both SSC enabled and SSC disabled. TJ is equivalent to BUJ + RJ. ISI is minimized by the test pattern.
[i] The measurement shall include the effects of the JTF (see 5.7.3.2).
[j] 0.15 UI is 25 ps at 6 Gbps, 50 ps at 3 Gbps, and 100 ps at 1.5 Gbps.
[k] See 5.7.4.6.2 for the transmitter device test procedure.
[l] This is not applicable when optical mode is enabled.

5.7.4.7 Transmitter device signal output characteristics for OOB signals

Transmitter devices supporting SATA shall use SATA Gen1i or Gen2i signal output levels (see SATA) during the first OOB sequence (see SPL) after a power on or hard reset. If the phy does not receive COMINIT within a hot-plug timeout (see SPL), then the transmitter device shall increase its transmit levels to the OOB signal output levels specified in table 29 and perform the OOB sequence again. If no COMINIT is received within a hot-plug timeout of the second OOB sequence, then the transmitter device shall initiate another OOB sequence using SATA Gen1i or Gen2i signal output levels. The transmitter device shall continue alternating between transmitting COMINIT using SATA Gen1i or Gen2i signal output levels and transmitting COMINIT with SAS signal output levels until the phy receives COMINIT.

If the phy both transmits and receives COMSAS (i.e., a SAS phy or expander phy is attached), then the transmitter device shall set its transmit levels to the SAS signal output levels (see 5.7.4.4, 5.7.4.5, and 5.7.4.6) prior to beginning the SAS speed negotiation sequence (see SPL). If it had been using SATA Gen1i or Gen2i signal output levels, this mode transition (i.e., output voltage change) may result in a transient (see 5.7.4.4) during the idle time between COMSAS and the SAS speed negotiation sequence.

If the transmitter device is using SAS signal output levels and the phy does not receive COMSAS (i.e., a SATA phy is attached), then the transmitter device shall set its transmit levels to the SATA Gen1i or Gen2i signal output levels and restart the OOB sequence.

Transmitter devices that do not support SATA shall transmit OOB signals using SAS signal output levels.

Table 29 defines the transmitter device signal output characteristics for OOB signals.

Table 5 — Transmitter device signal output characteristics for OOB signals

Characteristic	Units	IT	CT
Maximum peak to peak voltage (i.e., 2 × Z2 in figure 75) [a]	mV(P-P)	1 600	
OOB offset delta [b] [g]	mV	± 25	
OOB common mode delta [c] [g]	mV	± 50	
Minimum OOB burst amplitude [d], if SATA is not supported	mV(P-P)	240	
Minimum OOB burst amplitude [d], if SATA is supported	mV(P-P)	240 [e, f]	N/A

[a] The recommended maximum peak to peak voltage is 1 200 mV(P-P).
[b] The maximum difference in the average differential voltage (D.C. offset) component between the burst times and the idle times of an OOB signal.
[c] The maximum difference in the average of the common-mode voltage between the burst times and the idle times of an OOB signal.
[d] With a measurement bandwidth of 4.5 GHz, each signal level during the OOB burst shall exceed the specified minimum differential amplitude before transitioning to the opposite bit value or before termination of the OOB burst as measured with each test load at IT and CT.
[e] Amplitude measurement methodologies of SATA and this standard differ. Under conditions of maximum rise/fall time and jitter, eye diagram methodologies used in this standard may indicate less signal amplitude than the technique specified by SATA. Implementers of designs supporting SATA are required to ensure interoperability and should perform additional system characterization with an eye diagram methodology using SATA devices.
[f] The OOB burst contains either 1.5 Gbps D24.3 characters, 1.5 Gbps ALIGN (0) primitives, or 3 Gbps ALIGN (0) primitives (see SPL).
[g] This is not applicable when optical mode is enabled.

5.7.6 Spread spectrum clocking (SSC)

5.7.6.1 SSC overview

Spread spectrum clocking (SSC) is the technique of modulating the operating frequency of a transmitted signal to reduce the measured peak amplitude of radiated emissions.

Phys transmit with SSC as defined in and receive with SSC as defined in 5.7.6.3.

NOTE 1 - Phys compliant with previous versions of this standard do not transmit with SSC. Phys compliant with previous versions of this standard that do not support being attached to SATA devices were not required to receive with SSC.

Table 43 defines the SSC modulation types.

Table 6 — SSC modulation types

SSC modulation type	Maximum SSC frequency deviation (SSC_{tol}) [a]
Center-spreading	+2 300 / -2 300 ppm
No-spreading	+0 / -0 ppm
Down-spreading	+0 / -2 300 ppm
SATA down-spreading [b]	+0 / -5 000 ppm

[a] This is in addition to the physical link rate long-term stability and tolerance defined in table 20 and table 30 (see 5.7.1).
[b] This is only used as a receiver parameter.

A phy may be transmitting with a different SSC modulation type than it is receiving (e.g., a phy is transmitting with center-spreading while it is receiving with down-spreading).

If the SSC modulation type is not no-spreading, then the phy shall transmit within the specified maximum SSC frequency deviation with an SSC modulation frequency that is a minimum of 30 kHz and a maximum of 33 kHz.

The SSC modulation profile (e.g., triangular) is vendor-specific, but should provide the maximum amount of electromagnetic interference (EMI) reduction. For center-spreading, the average amount of up-spreading (i.e., > 0 ppm) in the SSC modulation profile shall be the same as the average amount of down-spreading (i.e., < 0 ppm). The amount of asymmetry in the SSC modulation profile shall be less than 288 ppm.

NOTE 2 - 288 ppm is the rate of deletable primitives (see SPL) that are left over after accounting for the physical link rate long-term stability. It is calculated as the deletable primitive rate defined in previous versions of this standard of 1/2 048 (i.e., 488 ppm) minus the width between the extremes of the physical link rate long-term stability of +100/-100 ppm (i.e., 200 ppm).

SSC-induced jitter is included in TJ at the transmitter output.

The slope of the frequency deviation should not exceed 850 ppm/µs when computed over any 0.27 ± 0.01 µs interval of the SSC modulation profile, after filtering of the transmitter device jitter output by a single-pole low-pass filter with a cutoff frequency of 3.7 ± 0.2 MHz. Alternatively, the transmitter device jitter may be filtered by the closed-loop transfer function of a measurement equipment's PLL that is compliant with the JTF.

The slope is computed from the difference equation:

$$slope = (f(t) - f(t - 0.27\ \mu s)) / 0.27\ \mu s$$

where:

f(t) is the SSC frequency deviation expressed in ppm

NOTE 3 - A ± 2 300 ppm triangular SSC modulation profile has a slope of approximately 310 ppm/µs and meets the informative slope specification. Other SSC modulation profiles (e.g., exponential) may not meet the slope requirement. A modulation profile that has a slope of ± 850 ppm/µs over 0.27 µs creates a residual jitter of approximately 16.7 ps (i.e., 0.10 UI at 6 Gbps) after filtering by the JTF. This consumes the total BUJ budget of the transmitter device, which does not allow the transmitter device to contribute any other type of BUJ.

Activation or deactivation of SSC on a physical link that is not ~~D.C.~~OOB idle or negotiation idle (see SPL) shall be done without violating TJ at the transmitter device output after application of the JTF.

5.9 Out of band (OOB) signals

5.9.1 OOB signals overview

~~OOB signals are low-speed signal patterns that do not appear in normal data streams.~~ If D.C. mode (see x.x.x) is enabled, OOB signals are low-speed signal patterns that do not appear in normal data streams. When optical mode is enabled, OOB signals consist of a defined series of dwords. OOB signals consist of defined amounts of idle time followed by defined amounts of burst time. During the idle time, the physical link carries ~~D.C.~~ OOB idle (see 3.1.14)[fix xref]. During the burst time, the physical link carries ~~signal transitions~~dwords. The signals are differentiated by the length of idle time between the burst times. A phy shall either have D.C. mode enabled or optical mode enabled. The method to enable D.C. mode or optical mode is outside the scope of this standard.

SATA defines two OOB signals: COMINIT/COMRESET and COMWAKE. COMINIT and COMRESET are used in this standard interchangeably. Phys compliant with this standard identify themselves with an additional SAS-specific OOB signal called COMSAS.

Table 49 defines the timing specifications for OOB signals.

Table 7 — OOB signal timing specifications

Parameter	Minimum	Nominal	Maximum	Comments
OOB Interval (OOBI) [a]	$665.0\overline{6}$ ps [b]	$666.\overline{6}$ ps [c]	$668.2\overline{6}$ ps [d]	The time basis for burst times and idle times used to create OOB signals.
COMSAS detect timeout	13.686 μs [e]			The minimum time a receiver device shall allow to detect COMSAS after transmitting COMSAS.

[a] OOBI is different than UI(OOB) defined in SATA (e.g., SAS has tighter physical link rate long-term stability and different SSC frequency deviation). OOBI is based on:
A) 1.5 Gbps UI (see table 19 in 5.7.1);
B) physical link rate long-term stability (see table 20 in 5.7.2); and
C) center-spreading SSC (see table 43 in 5.7.6.1).

[b] $665.0\overline{6}$ ps equals $666.\overline{6} \times (1 - 0.0024)$.

[c] $666.\overline{6}$ equals 2000 / 3.

[d] $668.2\overline{6}$ ps equals $666.\overline{6} \times 1.0024$.

[e] 13.686 μs is $512 \times 40 \times$ Maximum OOBI.

To interoperate with interconnects compliant with previous versions of this standard, phys should create OOB burst times and idle times based on the UI for 1.5 Gbps without SSC modulation.

NOTE 4 - Previous versions of this standard defined OOBI based on the nominal UI for 1.5 Gbps with physical link rate long-term stability tolerance (see table 19 in 5.7.1) but not with SSC modulation (see table 43 in 5.7.6.1). Interconnects compliant with previous versions of this standard may have assumed phys had that characteristic.

5.9.2 Transmitting OOB signals

Table 50 describes the OOB signal transmitter requirements for the burst time, idle time, negation times, and signal times that are used to form each OOB signal.

Table 8 — OOB signal transmitter device requirements

Signal	Burst time	Idle time	Negation time	Signal time [a]
COMWAKE	160 OOBI [b]	160 OOBI [b]	280 OOBI [c]	2 200 OOBI [g]
COMINIT/COMRESET	160 OOBI [b]	480 OOBI [d]	800 OOBI [e]	4 640 OOBI [i]
COMSAS	160 OOBI [b]	1 440 OOBI [f]	2 400 OOBI [h]	12 000 OOBI [j]

[a] A signal time is six burst times plus six idle times plus one negation time.
[b] 160 OOBI is nominally $106.\overline{6}$ ns (see table 49 in).
[c] 280 OOBI is nominally $186.\overline{6}$ ns.
[d] 480 OOBI is nominally 320 ns.
[e] 800 OOBI is nominally $533.\overline{3}$ ns.
[f] 1 440 OOBI is nominally 960 ns.
[g] 2 200 OOBI (e.g., COMWAKE) is nominally $1\,466.\overline{6}$ ns.
[h] 2 400 OOBI is nominally 1 600 ns.
[i] 4 640 OOBI (e.g., COMINIT/COMRESET) is nominally $3\,093.\overline{3}$ ns.
[j] 12 000 OOBI (e.g., COMSAS) is nominally 8 000 ns.

If D.C. mode is enabled, an OOB idle consists of the transmission of D.C. idle.

If optical mode is enabled, an OOB idle consists of repetitions of the following steps:

1) transmission of six OOB_IDLE primitives with either starting disparity at 3.0 Gbps; and
2) transmission of up to 512 dwords set to 00000000h that are transmitted scrambled and 8b10b encoded at 3.0 Gbps.

An OOB burst consists of:

a) if D.C. mode is enabled, transmission of D24.3 characters or ALIGN (0) primitives with either starting disparity. The OOB burst should consist of D24.3 characters at 1.5 Gbps; or
b) if optical mode is enabled, transmission of ALIGN (3) primitives with either starting disparity at 3.0 Gbps.

To transmit an OOB signal, the transmitter device shall repeat these steps six times:

1) transmit ~~D.C.~~ OOB idle for an idle time; and
2) transmit an OOB burst ~~with either starting disparity consisting of D24.3 characters or ALIGN (0) primitives (see SPL)~~ for a burst time. ~~The OOB burst should consist of D24.3 characters.~~

NOTE 5 - Transmitter devices compliant with future versions of this standard may not transmit OOB bursts consisting of ALIGN (0) primitives.

The transmitter device shall then transmit ~~D.C.~~ OOB idle for an OOB signal negation time.

The transmitter device shall use signal output levels during burst time and idle time as described in 5.7.4.7.

When D.C. mode is enabled, t~~T~~he D24.3 characters or ALIGN (0) primitives (see SPL) used in OOB signals shall be transmitted ~~at 1.5 Gbps.~~ , and t~~T~~he OOB burst is only required to generate an envelope for the detection circuitry, as required for any signaling that may be A.C. coupled. A burst of D24.3 characters at 1.5 Gbps is equivalent to a square wave pattern that has a one for 2 OOBI and a zero for 2 OOBI. A transmitter may use this square wave pattern for the OOB signal. The start of the pattern may be one or zero. The signal rise and fall times:

a) shall be greater than (i.e., slower) or equal to the minimum (i.e., fastest) rise and fall times allowed by the fastest supported physical link rate of the transmitter device (see table 22 in 5.7.4.4); and

b) shall be less than (i.e., faster) or equal to the maximum (i.e., slowest) rise and fall times allowed at 1.5 Gbps.

Editor’s Note 1: In all of the OOB figured, replace “D.C. Idle” with “OOB Idle”.

Figure 92 describes OOB signal transmission.

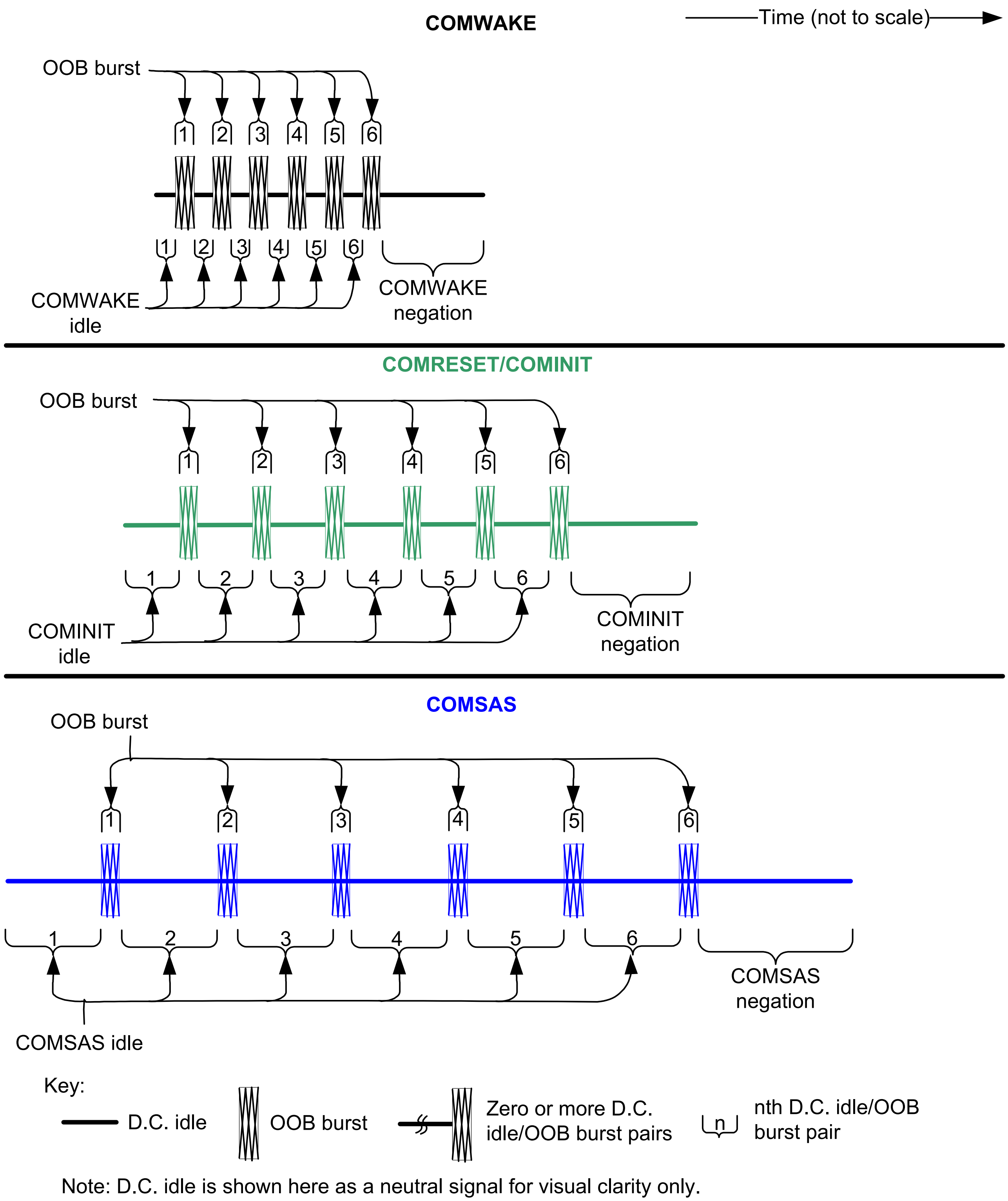

Figure 1 — OOB signal transmission

5.9.3 Receiving OOB signals

Table 51 describes the OOB signal receiver device requirements for detecting burst times, assuming T_{burst} is the length of the detected burst time. The burst time is not used to distinguish between signals.

Table 9 — OOB signal receiver device burst time detection requirements

Signal [a]	may detect	shall detect
COMWAKE	$T_{burst} \leq 100$ ns	$T_{burst} > 100$ ns
COMINIT/COMRESET	$T_{burst} \leq 100$ ns	$T_{burst} > 100$ ns
COMSAS	$T_{burst} \leq 100$ ns	$T_{burst} > 100$ ns

[a] Each burst time is transmitted as 160 OOBI, which is nominally 106.$\overline{6}$ ns (see table 50 in 5.9.2).

Table 52 describes the OOB signal receiver device requirements for detecting idle times, assuming T_{idle} is the length of the detected idle time.

Table 10 — OOB signal receiver device idle time detection requirements

Signal	may detect	shall detect	shall not detect
COMWAKE [a]	35 ns $\leq T_{idle} <$ 175 ns	101.3 ns $\leq T_{idle} \leq$ 112 ns	$T_{idle} <$ 35 ns or T_{idle} □ 175 ns
COMINIT/ COMRESET [b]	175 ns $\leq T_{idle} <$ 525 ns	304 ns $\leq T_{idle} \leq$ 336 ns	$T_{idle} <$ 175 ns or T_{idle} □ 525 ns
COMSAS [c]	525 ns $\leq T_{idle} <$ 1 575 ns	911.7 ns $\leq T_{idle} \leq$ 1 008 ns	$T_{idle} <$ 525 ns or T_{idle} □ 1 575 ns

[a] COMWAKE idle time is transmitted as 160 OOBI, which is nominally 106.$\overline{6}$ ns (see table 50 in 5.9.2).
[b] COMINIT/COMRESET idle time is transmitted as 480 OOBI, which is nominally 320 ns.
[c] COMSAS idle time is transmitted as 1 440 OOBI, which is nominally 960 ns.

Table 53 describes the OOB signal receiver device requirements for detecting negation times, assuming T_{idle} is the length of the detected idle time.

Table 11 — OOB signal receiver device negation time detection requirements

Signal	shall detect
COMWAKE [a]	$T_{idle} >$ 175 ns
COMINIT/COMRESET [b]	$T_{idle} >$ 525 ns
COMSAS [c]	$T_{idle} >$ 1 575 ns

[a] COMWAKE negation time is transmitted as 280 OOBI, which is nominally 186.$\overline{6}$ ns (see table 50 in 5.9.2).
[b] COMINIT/COMRESET negation time is transmitted as 800 OOBI is nominally 533.$\overline{3}$ ns.
[c] COMSAS negation time, which is transmitted as 2 400 OOBI, which is nominally 1 600 ns.

When D.C. mode is enabled, a~~A~~ SAS receiver device shall detect OOB bursts formed from any of the following:

a) D24.3 characters (see SPL) at 1.5 Gbps;
b) ALIGN (0) primitives (see SPL) at 1.5 Gbps; or
c) ALIGN (0) primitives at 3 Gbps.

NOTE 6 - ALIGN (0) primitives at 3 Gbps provide interoperability with transmitter devices compliant with previous versions of this standard and SATA.

When D.C mode is enabled, a SAS receiver device shall not qualify the OOB burst based on the characters received.

When optical mode is enabled, a SAS receiver device shall detect OOB bursts formed from ALIGN(3) primitives at 3Gbps.

5.9.4 Transmitting the SATA port selection signal

The SATA port selection signal shown in figure 93 causes the attached SATA port selector (see SPL) to select the attached phy (i.e., one of the port selector's host phys) as the active phy (see SATA).

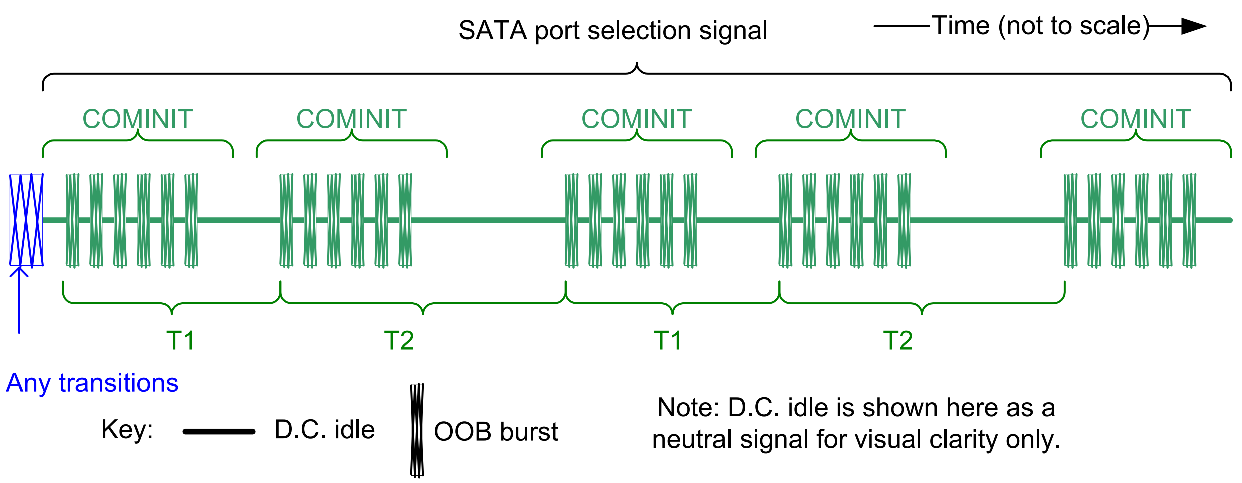

Figure 2 — SATA port selection signal

The SATA port selection signal shall be composed of 5 COMINIT signals, each starting a specified time interval, T1 or T2, as shown in figure 93, after the start of the OOB burst portion of the previous COMINIT signal. The values of T1 and T2 shall be as shown in table 54.

Table 12 — SATA port selection signal transmitter device requirements

Parameter	Time
T1	3×10^6 OOBI [a]
T2	12×10^6 OOBI [b]

[a] 3×10^6 OOBI is nominally 2 ms (see table 49 in).
[b] 12×10^6 OOBI is nominally 8 ms.

See SPL for information on usage of the SATA port selection signal.

Proposed Changes to SPL

In section 3 definitions:

0.0.0 D.C. idle: A differential signal level that is nominally 0 V(P-P)~~, used during the idle time (see 3.1.86) and negation time (see 3.1.116) of an OOB signal (see 0.0.7)~~. See SAS-2.1.

0.0.1 D.C. mode : A mode in which D.C. idle is used during the idle time and negation time of an OOB signal (see SAS-2.1), and during the RCDT time of speed negotiation windows (see x.x.x).

0.0.2 idle time: The part of an OOB signal (see 0.0.7) where ~~D.C.~~ OOB idle (see 3.1.38) is being transmitted. See 5.6.

0.0.3 negation time: The part of an OOB signal (see 0.0.7) during which ~~D.C.~~OOB idle (see 3.1.38) is transmitted after the last OOB burst (see 3.1.122). See 5.6.

0.0.4 negotiation idle: The transmission of D.C. idle when D.C mode (see x.x.x) is enabled, or a defined sequence of dwords at a vendor-specific transmit bit rate when optical mode is enabled (see x.x.x).

0.0.5 OOB idle: The transmission of D.C. idle when D.C mode (see x.x.x) is enabled, or a defined sequence of dwords when optical mode is enabled. See SAS-2.1.

0.0.6 optical mode: A mode in which a defined sequence of dwords is used during the idle time and negation time of an OOB signal (see SAS-2.1), and during the RCDT time of speed negotiation windows (see x.x.x).

0.0.7 speed negotiation transmit time (SNTT): During SNW-1, SNW-2, and Final-SNW, the time after RCDT during which ALIGN (0) or ALIGN (1) is transmitted. During SNW-3, the time after RCDT in which bit cells and ~~D.C.~~OOB idle are transmitted. See 5.7.4.2.2.

In section 5:

Editor's Note 2: In all of section 5 for the Phy Layer, replace "D.C. Idle" with either "OOB Idle" or "negotiation Idle". OOB Idle is used when referring to the idle time during the bit cells of SNW-3. All other cases use negotiation idle.

A possible exception to this is section 5.7.4.2.4 (and text that refers to that section specifically) regarding the detection of D.C. idle relative to the port selector.

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5.8.4 SAS speed negotiation states

5.8.4.1 SAS speed negotiation OOB idle

SAS speed negotiation states use negotiation idle at the beginning of each speed negotiation window.

If D.C. mode is enabled, a negotiation idle consists of the transmission of D.C. idle.

If optical mode is enabled, a negotiation idle consists of repetitions of the following steps:

1) transmission of six OOB_IDLE primitives with either starting disparity at a vendor-specific bit rate; and
2) transmission of up to 512 dwords set to 00000000h that are transmitted scrambled and 8b10b encoded at a vendor-specific bit rate.

In section 6:

6.2.2 Primitive summary

Table 14 defines the deletable primitives.

Table 13 — Deletable primitives

Primitive	Use [a]	From [b]			To [b]			Primitive sequence type [c]
		I	E	T	I	E	T	
ALIGN (0)	All, SpNeg	I	E	T	I	E	T	Single
ALIGN (1)	SAS, SpNeg							
ALIGN (2)	SAS							
ALIGN (3)								
MUX (LOGICAL LINK 0)	SAS	I	E	T	I	E	T	Single
MUX (LOGICAL LINK 1)								
NOTIFY (ENABLE SPINUP)	SAS	I	E				T	Single
NOTIFY (POWER LOSS EXPECTED)		I	E				T	
NOTIFY (RESERVED 1)					I	E	T	
NOTIFY (RESERVED 2)					I	E	T	
OOB_IDLE	SAS, spNeg	I	E	T	I	E	T	Single

[a] The Use column indicates when the primitive is used:
a) All: SAS logical links and SATA physical links;
b) SAS: SAS logical links, both outside connections or inside any type of connection;
c) NoConn: SAS logical links, outside connections;
d) Conn: SAS logical links, inside connections;
e) STP: SAS logical links, inside STP connections; or
f) SpNeg: SAS physical links, during speed negotiation.

[b] The From and To columns indicate the type of ports that originate each primitive or are the intended destinations of each primitive:
a) I for SAS initiator ports;
b) E for expander ports; and
c) T for SAS target ports.
Expander ports are not considered originators of primitives that are being forwarded from expander port to expander port.

[c] The Primitive sequence type columns indicate whether the primitive is a single primitive sequence, a repeated primitive sequence, a continued primitive sequence, a triple primitive sequence, or a redundant primitive sequence (see 6.2.4).

7.2.2 Primitive summary

Table 14 defines the deletable primitives.

Table 14 — Deletable primitives

Primitive	Use [a]	From [b]			To [b]			Primitive sequence type [c]
		I	E	T	I	E	T	
ALIGN (0)	All, SpNeg	I	E	T	I	E	T	Single
ALIGN (1)	SAS, SpNeg							
ALIGN (2)	SAS							
ALIGN (3)								
MUX (LOGICAL LINK 0)	SAS	I	E	T	I	E	T	Single
MUX (LOGICAL LINK 1)								
NOTIFY (ENABLE SPINUP)	SAS	I	E				T	Single
NOTIFY (POWER LOSS EXPECTED)		I	E				T	
NOTIFY (RESERVED 1)					I	E	T	
NOTIFY (RESERVED 2)					I	E	T	
OOB_IDLE	SAS, SpNeg	I	E	T	I	E	T	Single

[a] The Use column indicates when the primitive is used:
a) All: SAS logical links and SATA physical links;
b) SAS: SAS logical links, both outside connections or inside any type of connection;
c) NoConn: SAS logical links, outside connections;
d) Conn: SAS logical links, inside connections;
e) STP: SAS logical links, inside STP connections; or
f) SpNeg: SAS physical links, during speed negotiation.

[b] The From and To columns indicate the type of ports that originate each primitive or are the intended destinations of each primitive:
a) I for SAS initiator ports;
b) E for expander ports; and
c) T for SAS target ports.
Expander ports are not considered originators of primitives that are being forwarded from expander port to expander port.

[c] The Primitive sequence type columns indicate whether the primitive is a single primitive sequence, a repeated primitive sequence, a continued primitive sequence, a triple primitive sequence, or a redundant primitive sequence (see 7.2.4).

6.2.3 Primitive encodings

Table 15 defines the primitive encoding for deletable primitives.

Table 15 — Primitive encoding for deletable primitives

Primitive	Character				Hexadecimal
	1st	2nd	3rd	4th (last)	
ALIGN (0)	K28.5	D10.2	D10.2	D27.3	BC4A4A7Bh
ALIGN (1)	K28.5	D07.0	D07.0	D07.0	BC070707h
ALIGN (2)	K28.5	D01.3	D01.3	D01.3	BC616161h
ALIGN (3)	K28.5	D27.3	D27.3	D27.3	BC7B7B7Bh
MUX (LOGICAL LINK 0)	K28.5	D02.0	D16.7	D31.4	BC02F09Fh
MUX (LOGICAL LINK 1)	K28.5	D04.7	D31.4	D27.4	BCE49F9Bh
NOTIFY (ENABLE SPINUP)	K28.5	D31.3	D31.3	D31.3	BC7F7F7Fh
NOTIFY (POWER LOSS EXPECTED)	K28.5	D31.3	D07.0	D01.3	BC7F0761h
NOTIFY (RESERVED 1)	K28.5	D31.3	D01.3	D07.0	BC7F6107h
NOTIFY (RESERVED 2)	K28.5	D31.3	D10.2	D10.2	BC7F4A4Ah
OOB_IDLE	K28.5	D07.3	D01.4	D31.4	BC67819Fh

Editor's Note 3: Make corresponding change in Appendix showing the primitive encodings.

6.2.3.1 OOB_IDLE

OOB_IDLE is used for OOB signals when optical mode is enabled (see x.x.x).

In section 10:

Table 16 — DISCOVER response (part 1 of 4)

Byte\Bit	7	6	5	4	3	2	1	0
0	SMP FRAME TYPE (41h)							
1	FUNCTION (10h)							
2	FUNCTION RESULT							
3	RESPONSE LENGTH (00h or 1Ch)							
4	(MSB)	EXPANDER CHANGE COUNT						
5								(LSB)
6	Reserved							
8								

Table 16 — DISCOVER response (part 2 of 4)

Byte\Bit	7	6	5	4	3	2	1	0
9	PHY IDENTIFIER							
10 … 11	Reserved							
12	Reserved	ATTACHED DEVICE TYPE			ATTACHED REASON			
13	Reserved				NEGOTIATED LOGICAL LINK RATE			
14	Reserved				ATTACHED SSP INITIATOR	ATTACHED STP INITIATOR	ATTACHED SMP INITIATOR	ATTACHED SATA HOST
15	ATTACHED SATA PORT SELECTOR	Reserved			ATTACHED SSP TARGET	ATTACHED STP TARGET	ATTACHED SMP TARGET	ATTACHED SATA DEVICE
16 … 23	SAS ADDRESS							
24 … 31	ATTACHED SAS ADDRESS							
32	ATTACHED PHY IDENTIFIER							
33	Reserved					ATTACHED INSIDE ZPSDS PERSISTENT	ATTACHED REQUESTED INSIDE ZPSDS	ATTACHED BREAK_REPLY CAPABLE
34 … 39	Reserved for IDENTIFY address frame-related fields							
40	PROGRAMMED MINIMUM PHYSICAL LINK RATE				HARDWARE MINIMUM PHYSICAL LINK RATE			
41	PROGRAMMED MAXIMUM PHYSICAL LINK RATE				HARDWARE MAXIMUM PHYSICAL LINK RATE			
42	PHY CHANGE COUNT							
43	VIRTUAL PHY	Reserved			PARTIAL PATHWAY TIMEOUT VALUE			
44	Reserved				ROUTING ATTRIBUTE			
45	Reserved	CONNECTOR TYPE						
46	CONNECTOR ELEMENT INDEX							
47	CONNECTOR PHYSICAL LINK							
48 … 49	Reserved							
50 … 51	Vendor specific							
52 … 59	ATTACHED DEVICE NAME							

Table 16 — DISCOVER response (part 3 of 4)

<table>
<tr><th>Byte\Bit</th><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>60</td><td>Reserved</td><td>REQUESTED INSIDE ZPSDS CHANGED BY EXPANDER</td><td>INSIDE ZPSDS PERSISTENT</td><td>REQUESTED INSIDE ZPSDS</td><td>Reserved</td><td>ZONE GROUP PERSISTENT</td><td>INSIDE ZPSDS</td><td>ZONING ENABLED</td></tr>
<tr><td>61
62</td><td colspan="8">Reserved for zoning-related fields</td></tr>
<tr><td>63</td><td colspan="8">ZONE GROUP</td></tr>
<tr><td>64</td><td colspan="8">SELF-CONFIGURATION STATUS</td></tr>
<tr><td>65</td><td colspan="8">SELF-CONFIGURATION LEVELS COMPLETED</td></tr>
<tr><td>66
67</td><td colspan="8">Reserved for self-configuration related fields</td></tr>
<tr><td>68
75</td><td colspan="8">SELF-CONFIGURATION SAS ADDRESS</td></tr>
<tr><td>76
79</td><td colspan="8">PROGRAMMED PHY CAPABILITIES</td></tr>
<tr><td>80
83</td><td colspan="8">CURRENT PHY CAPABILITIES</td></tr>
<tr><td>84
87</td><td colspan="8">ATTACHED PHY CAPABILITIES</td></tr>
<tr><td>88
93</td><td colspan="8">Reserved</td></tr>
<tr><td>94</td><td colspan="4">REASON</td><td colspan="4">NEGOTIATED PHYSICAL LINK RATE</td></tr>
<tr><td>95</td><td colspan="5">Reserved</td><td>OPTICAL MODE ENABLED</td><td>NEGOTIATED SSC</td><td>HARDWARE MUXING SUPPORTED</td></tr>
<tr><td>96</td><td colspan="2">Reserved</td><td>DEFAULT INSIDE ZPSDS PERSISTENT</td><td>DEFAULT REQUESTED INSIDE ZPSDS</td><td>Reserved</td><td>DEFAULT ZONE GROUP PERSISTENT</td><td>Reserved</td><td>DEFAULT ZONING ENABLED</td></tr>
<tr><td>97</td><td colspan="8">Reserved</td></tr>
<tr><td>98</td><td colspan="8">Reserved</td></tr>
<tr><td>99</td><td colspan="8">DEFAULT ZONE GROUP</td></tr>
</table>

Table 16 — DISCOVER response (part 4 of 4)

Byte\Bit	7	6	5	4	3	2	1	0
100	Reserved		SAVED INSIDE ZPSDS PERSISTENT	SAVED REQUESTED INSIDE ZPSDS	Reserved	SAVED ZONE GROUP PERSISTENT	Reserved	SAVED ZONING ENABLED
101	Reserved							
102	Reserved							
103	SAVED ZONE GROUP							
104	Reserved		SHADOW INSIDE ZPSDS PERSISTENT	SHADOW REQUESTED INSIDE ZPSDS	Reserved	SHADOW ZONE GROUP PERSISTENT	Reserved	
105	Reserved							
106	Reserved							
107	SHADOW ZONE GROUP							
108	DEVICE SLOT NUMBER							
109	DEVICE SLOT GROUP NUMBER							
110 … 115	DEVICE SLOT GROUP OUTPUT CONNECTOR							
116 … 119	(MSB) CRC (LSB)							

An OPTICAL MODE ENABLE bit set to one indicates that the phy is operating in optical mode (see x.x.x). An OPTICAL MODE ENABLE bit set to zero indicates that the phy is operating in D.C. mode.

Table 17 — Enhanced phy control mode descriptor

Byte\Bit	7	6	5	4	3	2	1	0
0	Reserved							
1	PHY IDENTIFIER							
2 … 3	(MSB) DESCRIPTOR LENGTH (0010h) (LSB)							
4 … 7	PROGRAMMED PHY CAPABILITIES							

Table 17 — Enhanced phy control mode descriptor

Byte\Bit	7	6	5	4	3	2	1	0
8 – 11	CURRENT PHY CAPABILITIES							
12 – 15	ATTACHED PHY CAPABILITIES							
16 – 17	Reserved							
18	Reserved		OPTICAL MODE ENABLED	NEGOTIATED SSC	NEGOTIATED PHYSICAL LINK RATE			
19	Reserved							HARDWARE MUXING SUPPORTED