

T10/08-239r0 SAT-2: Definition Cleanup

To: T10 Technical Committee
From: Mark A. Overby, NVIDIA Corporation (moverby@nvidia.com)
Date: June 22, 2008 11:46 pm
Subject: T10/08-239r0 SAT-2: Definition Cleanup

Revision History:

Revision 0: Initial draft of document

1 Overview

This proposal addresses proposed definition changes before SAT-2 goes to letter ballot due to changes in other standards and specifications.

It shall be noted that the clause numbers in the following definitions are not correct, but will be correct when incorporated into SAT-2 (as the numbering is picked up automatically by framemaker)

2 Changes to SAT-2

Replace existing definitions with the following:

2.0.1 ATA device: A device that is compliant with the ATA standards and implements the General feature set.

2.0.2 ATA software reset: A reset that is triggered by an ATA task management function request (see ATA8-AAM, see 5.6).

Delete the following definitions:

2.0.3 first-party direct memory access (FPDMA): A method by which a device accesses host memory (see SATA 2.5).

Replace the Note 3 with the following text:

SAM-3 specifies what SAM-2 defines as autosense as the only valid way of returning SENSE data, but does not refer to it as autosense.