

Proposal for 6G SAS Rx Converged Tolerance Test

Mike Jenkins
LSI Corp.

Stressed Rx vs. Jitter Tolerance: Test Comparison

- Only difference is BUJ vs. SJ...but SJ is BUJ
- Stress non-SSC compliant RX with "standard" profile
- Stress SSC compliant RX with "inverse JTF" profile

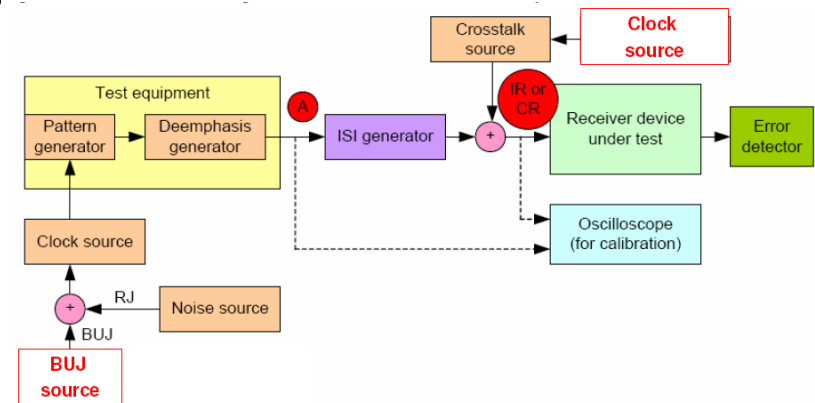


Figure 131 — Stressed receiver sensitivity test block diagram

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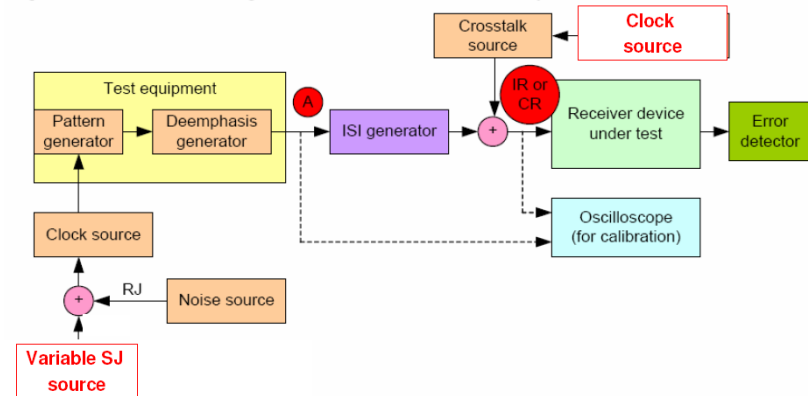


Figure 132 Jitter tolerance test block diagram

SJ Profile (non-SSC Compliant)

- This profile is inconsistent with TX jitter tests using JTF
 - Does not guarantee tolerance at lower frequencies

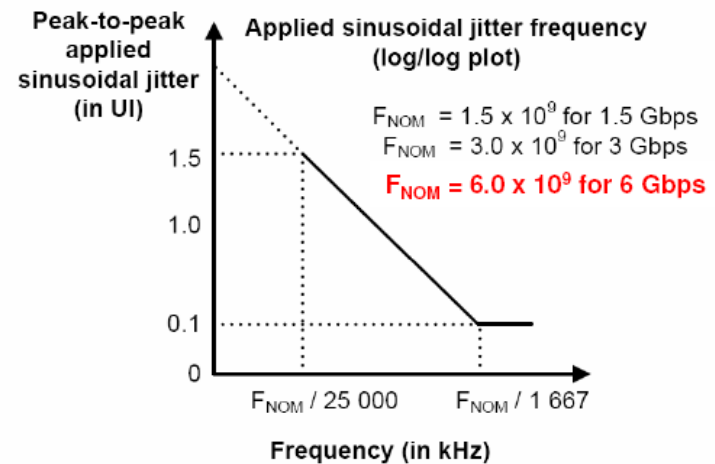
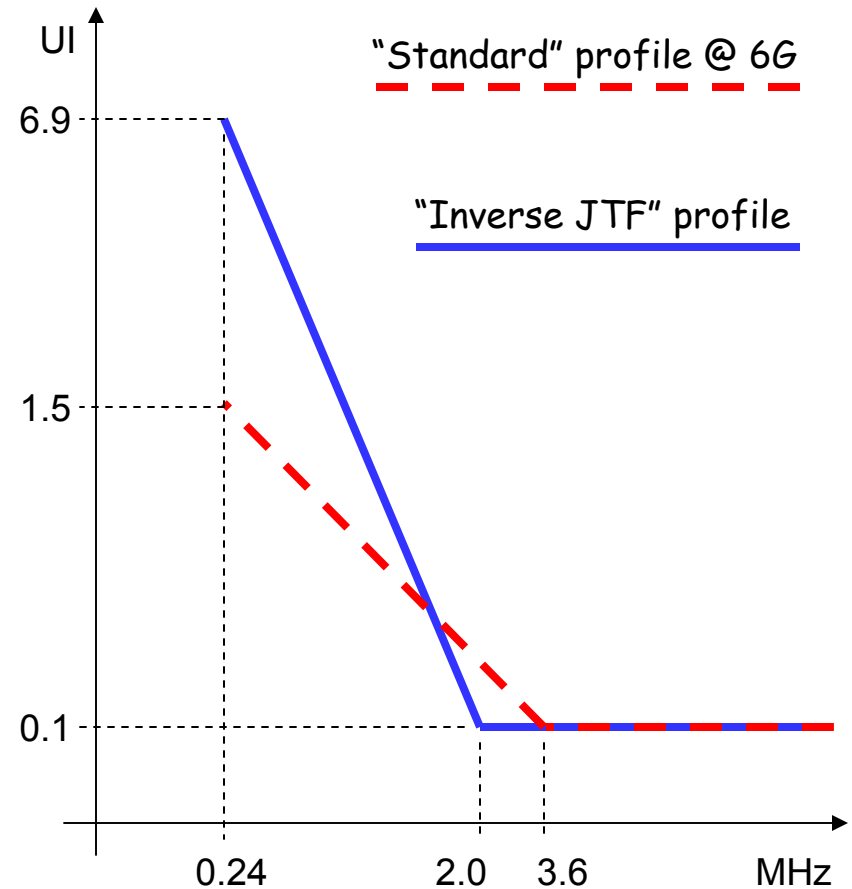


Figure 122 — Applied sinusoidal jitter

SJ Profile (SSC Compliant)

- Problem(?): Lab equipment SJ phase modulation limited to $\sim 8UI$
- This limits low frequency end of profile to $\sim 8x$ higher than SSC fundamental



Proposal

- Combine Stressed RX & Jitter Tolerance tests into one test with BUJ represented by a frequency profile of SJ
- To verify RX SSC tolerance, the SJ profile can be modified to track the inverse of the JTF