Minutes of SAS PHY Working Group conference call November 15, 2007

Attendance:

Mr. Jesse Jaramillo  Amphenol
Mr. Mickey Felton  EMC
Mr. Ramez Rizk  Emulex
Mr. Douglas Wagner  FCI
Mr. Mike Fitzpatrick  Fujitsu
Mr. Barry Olawsky  Hewlett Packard Co.
Mr. Harvey Newman  Infineon Technologies
Dr. Mark Seidel  Intel Corp.
Mr. Michael Jenkins  LSI Corp.
Mr. Gabriel Romero  LSI Corp.
Mr. Kevin Witt  Maxim Semiconductor
Mr. Mahbubul Bari  Maxim Semiconductor
Mr. Galen Fromm  Molex Inc.
Mr. Hock Seow  NEC Electronics America, Inc
Mr. Rick Hernandez  PMC-Sierra
Mr. Guillaume Fortin  PMC-Sierra
Mr. Yuming Tao  PMC-Sierra
Mr. Alvin Cox  Seagate Technology
Mr. Benoit Mercier  STMicroelectronics
Mr. Michael Fogg  TycoElectronics
Mr. Larry McMillan  WDC

21 in attendance

Agenda:

1. Transmitter Device Common Mode Voltage [Cox, Jenkins]
   Updated table for transmitter common mode specification to include mated connector. 
   Update will be included in 07-339r7 to be posted by 11/16. Please review.

2. 6Gbps PHY specification [Cox]
   Wording for TxRx connection to indicate budget allocation is required.
   Updated material posted on T10 reflector received comments from George Penokie. Comments 
   will be incorporated and included in 07-339r7 posting.

3. Proposed Cable Tables [McSorley]
   Does above clarification in TxRx connection resolve budget issues?
   Greg was not available for the call. Item 2 gives clarification to budgeting for cable loss. Update 
   expected on next call.

4. Interconnect Signal-to-Noise Ratio [Olawsky]
   Add a SNR for the delivered signal at the receiver compliance point (system requirement)?
   No update during the call. Barry will work with Kevin and have additional input on the next call.

5. Receiver Device Physical Testing [Witt, Bari]
   Barry will look at crosstalk concerning the 4mV rms NEXT budget.
   Need to clarify wording as total crosstalk. Kevin and Rick will work on wording.
   Need justification for TX bounded uncorrelated jitter (0.036 UI) and where it came from in FCAL 
   [Jenkins].

6. New item: SSC disabled during OOB and impact to common clock structure.
Mike Jenkins had some questions regarding SSC requirements and single clock structures for multiple ports. The spec requires that SSC be off during SNW1, SNW2, and FINAL SNW. This item was discussed at length during the Gen 3 speed negotiation development within the phy group in 2006.

Schedule:
Next conference call: December 13, 2007

Agenda:

1. Physical receiver testing
2. SNR requirements
3. Cable specification
4. SSC discontinuity restrictions
5. 07-339r7 comments
6. New business

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Webex information:
https://seagate.webex.com/seagate
Topic: SAS-2 PHY WG
Date: Thursday
Time: 10:00 am, Central Standard Time
Meeting number: 826 515 680
Meeting password: 6gbpsSAS