To: T10 Technical Committee
From: Kevin Marks - Dell, Inc.
Date: May 7, 2007
Subject: T10/07-232r0 - ADC-2 Letter Ballot resolution for comments HP-215 and QTM-92

Revision History
Revision 0 (5/07/07) – Initial proposal

Related Documents
ADC-2 T10/1741-D Revision 7b

New text to be added to ADC-2
Text to be deleted from ADC-2
z<<...Editorial text...>

Overview

Suggested Changes to ADC-2r7b:

6.1.2.4.4 Serial Attached SCSI port status data
The format of the DT device primary port status data for a SAS port (see SAS-1.1) is shown in table 23.

<<.INSERT Table 23.>>

A port initialization complete (PIC) bit set to one indicates that the port has successfully completed speed negotiation and the identification sequence (see SAS-1.1). When port initialization is complete the SAS port is ready to receive an Open address frame.

If the port supports the capability to detect a signal at the DT device primary port (e.g., COMINIT detected, see SAS-1.1), then A SIGNAL bit set to one indicates that a signal is detected at the DT device primary port (e.g., COMINIT detected). A SIGNAL bit set to zero indicates a signal is not detected. If the port does not support the capability to detect a signal at the DT device primary port, then the SIGNAL bit shall be set to the value of the port initialization complete (PIC) bit.

The NEGOTIATED PHYSICAL LINK RATE field indicates the negotiated physical link rate (see SAS-1.1) for the SAS port.

The HASHED SAS ADDRESS field contains the hashed version of the SAS address (see SAS-1.1) of the SAS port assigned to the DT device primary port.